

Verification of Concurrent Programs under Release Acquire

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IARCS Verification Seminar Series
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Concurrency

Concurrency

motivation

Concurrency

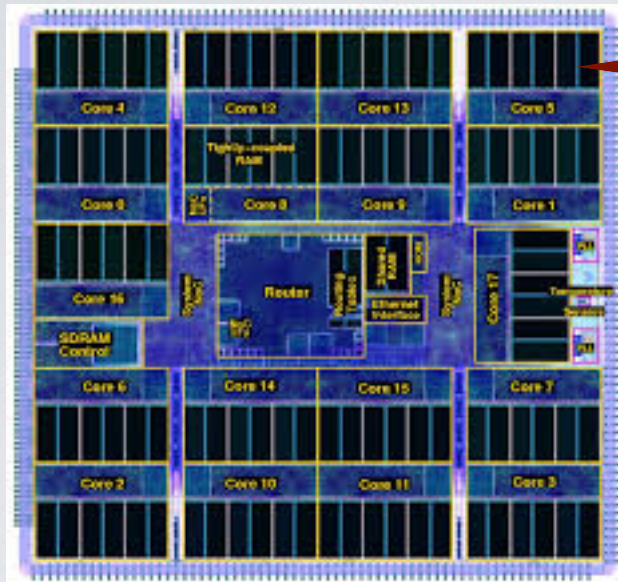
**Concurrent systems
are everywhere**

motivation

Concurrency

Concurrent systems
are everywhere

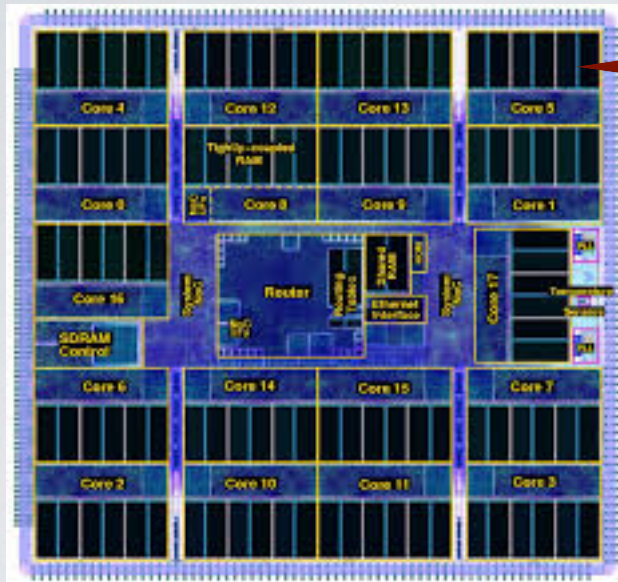
Multicore
architectures
motivation



Concurrency

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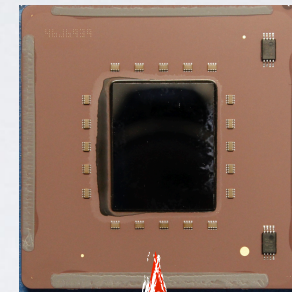
Multicore
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intel



ARM



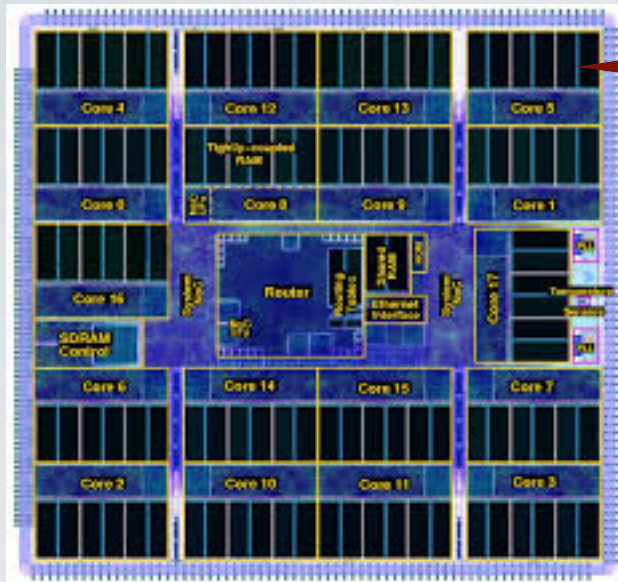
IBM
Power



Concurrency

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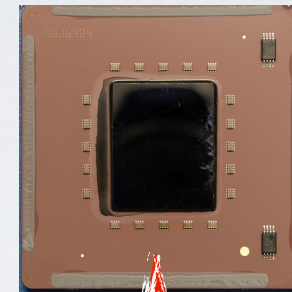
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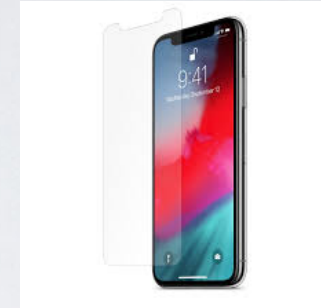
intel



ARM



IBM
Power

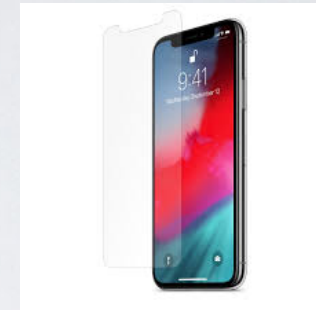
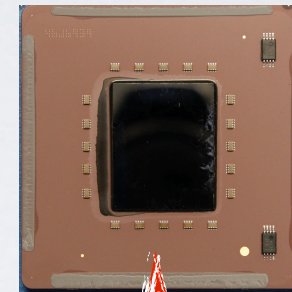
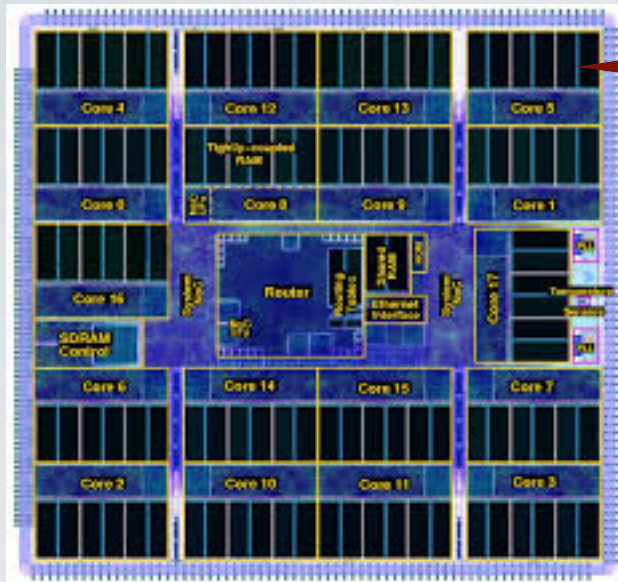


Distributed
databases

Concurrency

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intel

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Facebook

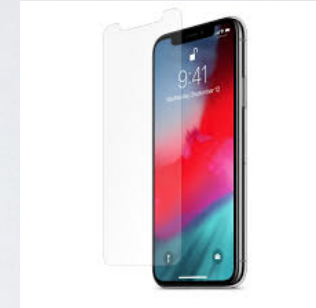
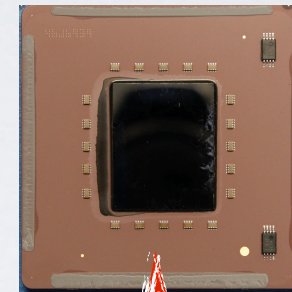
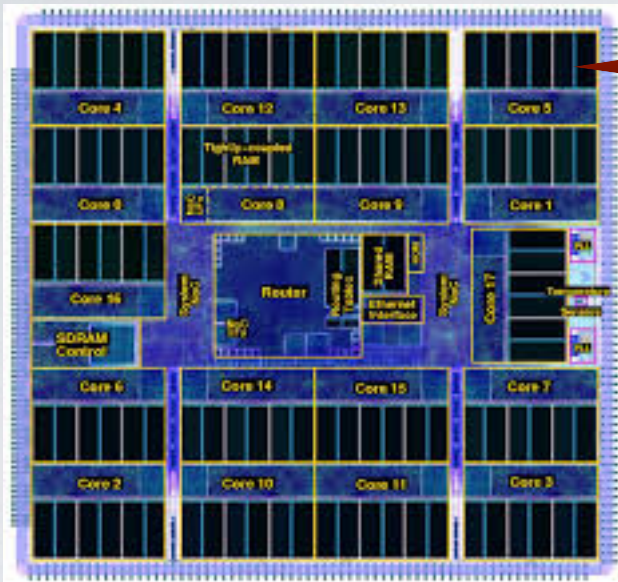
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Facebook

Programming
languages

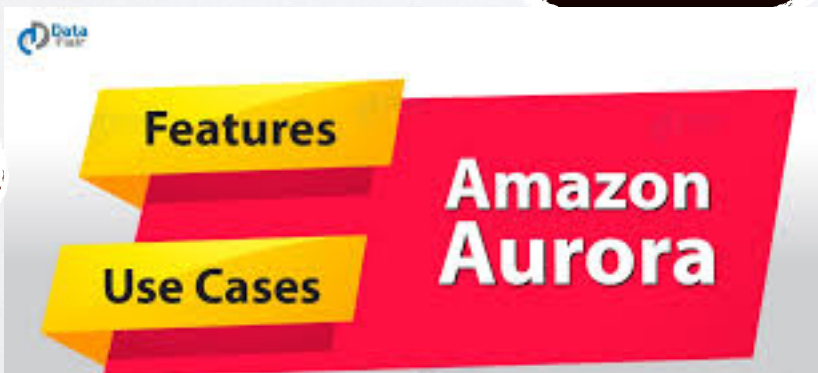
Concurrency with Modern C++

What every professional C++ programmer should know about concurrency



Rainer
Grimm

Distributed
databases



Weak memory models provide sound semantics for high performance concurrency

Weak memory models provide sound semantics for high performance concurrency



Weak memory models provide sound semantics for high performance concurrency

Interleaving semantics

Different threads observe memory events in different orders

Sequential Consistency

Release Acquire (RA)

Relaxed models

Easy

High

Programmability

Performance

Fragment of C11

The C11 Memory Model

The C11 Memory Model

Non-atomic Data accesses

The C11 Memory Model

Non-atomic Data accesses

Atomic Data accesses

Performance ↑

↓ **Synchronization**

The C11 Memory Model

Non-atomic Data accesses

Atomic Data accesses

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Relaxed

Release-Acquire

Sequential Consistency

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The C11 Memory Model

Non-atomic Data accesses

Atomic Data accesses

Performance ↑

Relaxed

Release-Acquire

Sequential Consistency

↓ Synchronization

Release-Acquire : all reads are **acquire**, all writes are **release**, updates (atomic read writes) are **acquire/release**

Store buffer

Init: $x=y=0$

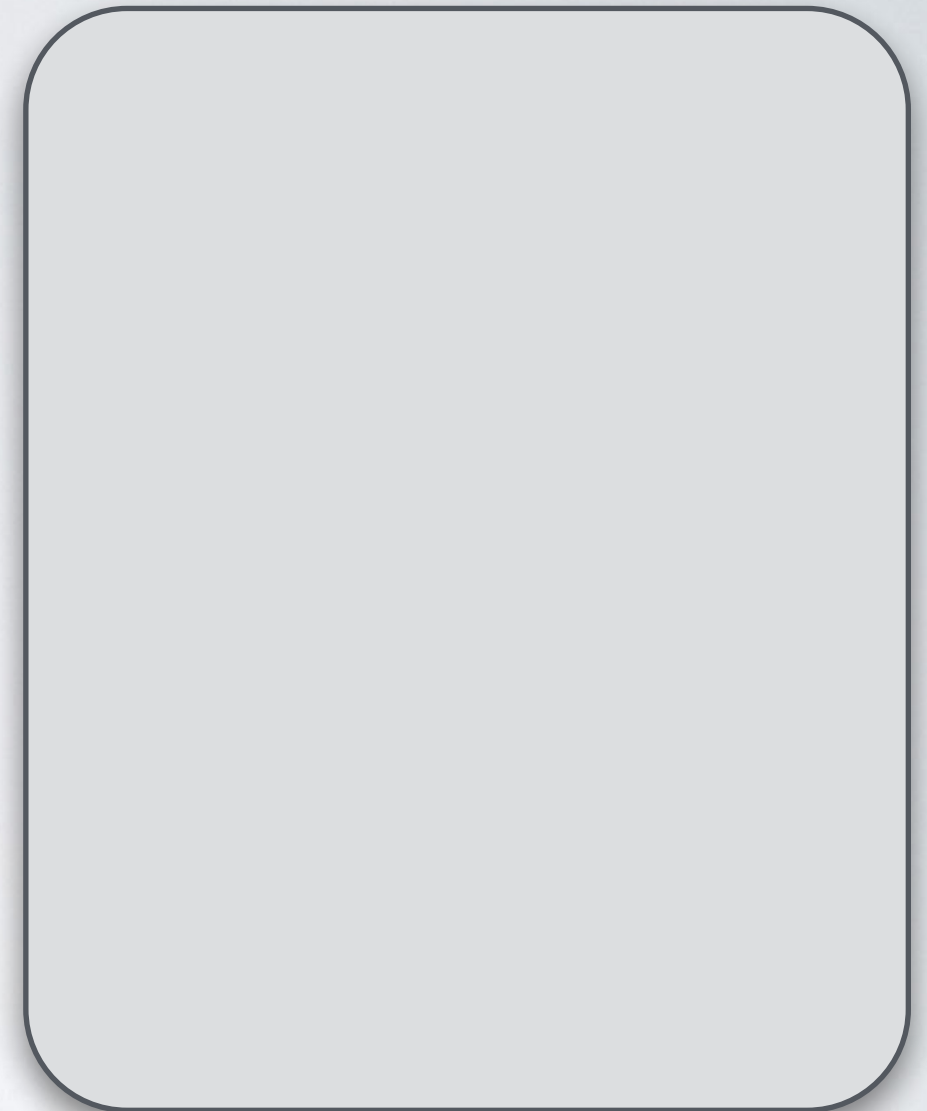
1. $x=1$;
2. $ry=y$;

Process 1

1. $y=1$;
2. $rx=x$;

Process 2

RA execution



$x=0$ $y=0$

Shared memory

Specification S: **not** ($rx=0$ && $ry=0$)

Store buffer

Init: $x=y=0$

1. $x=1$;
2. $ry=y$;

Process 1

1. $rx=x$;
2. $y=1$;

Process 2

RA execution

$r(x,0)$



$w(x,1)$

$x=0$ $y=0$

Shared memory

Specification S: **not** ($rx=0 \ \&\& \ ry=0$)

Store buffer

Init: $x=y=0$

1. $x=1$;
2. $ry=y$;

Process 1

1. $rx=x$;
2. $y=1$;

Process 2

RA execution

$r(x,0)$



$w(x,1)$

$x=1$ $y=0$

Shared memory

Specification S: **not** $(rx=0 \ \&\& \ ry=0)$

Store buffer

Init: $x=y=0$

1. $x=1$;
2. $ry=y$;

Process 1

1. $rx=x$;
2. $y=1$;

Process 2

RA execution

$r(x,0)$

$w(x,1)$

$r(y,0)$

$w(y,1)$

$x=1$ $y=0$

Shared memory

Specification S: **not** $(rx=0 \ \&\& \ ry=0)$

Store buffer

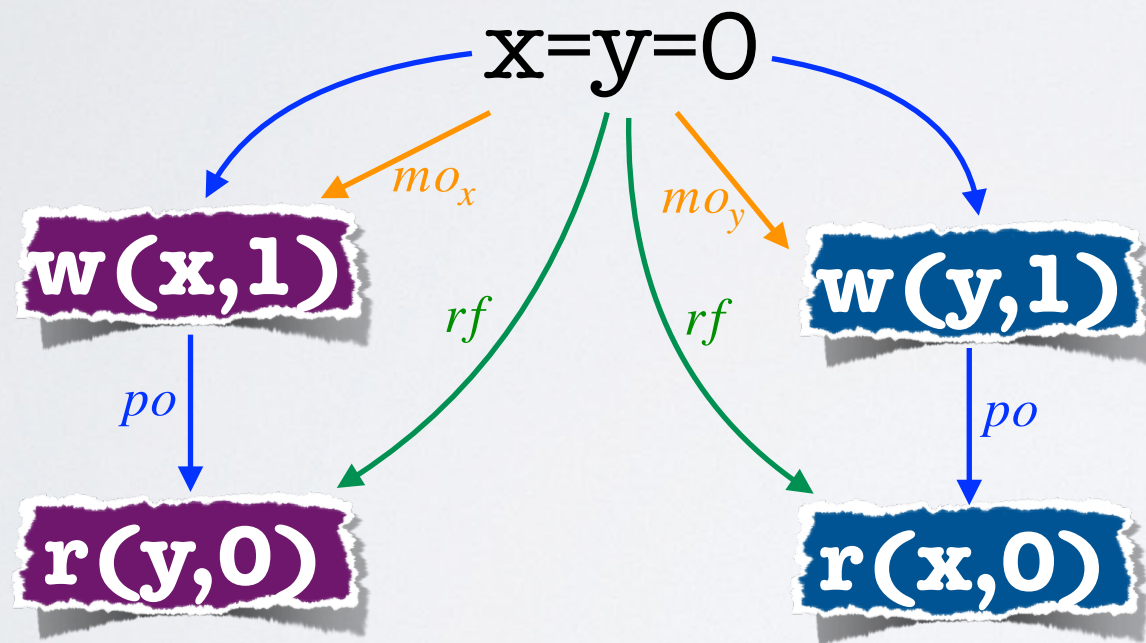
Init: $x=y=0$

1. $x=1$;
2. $ry=y$;

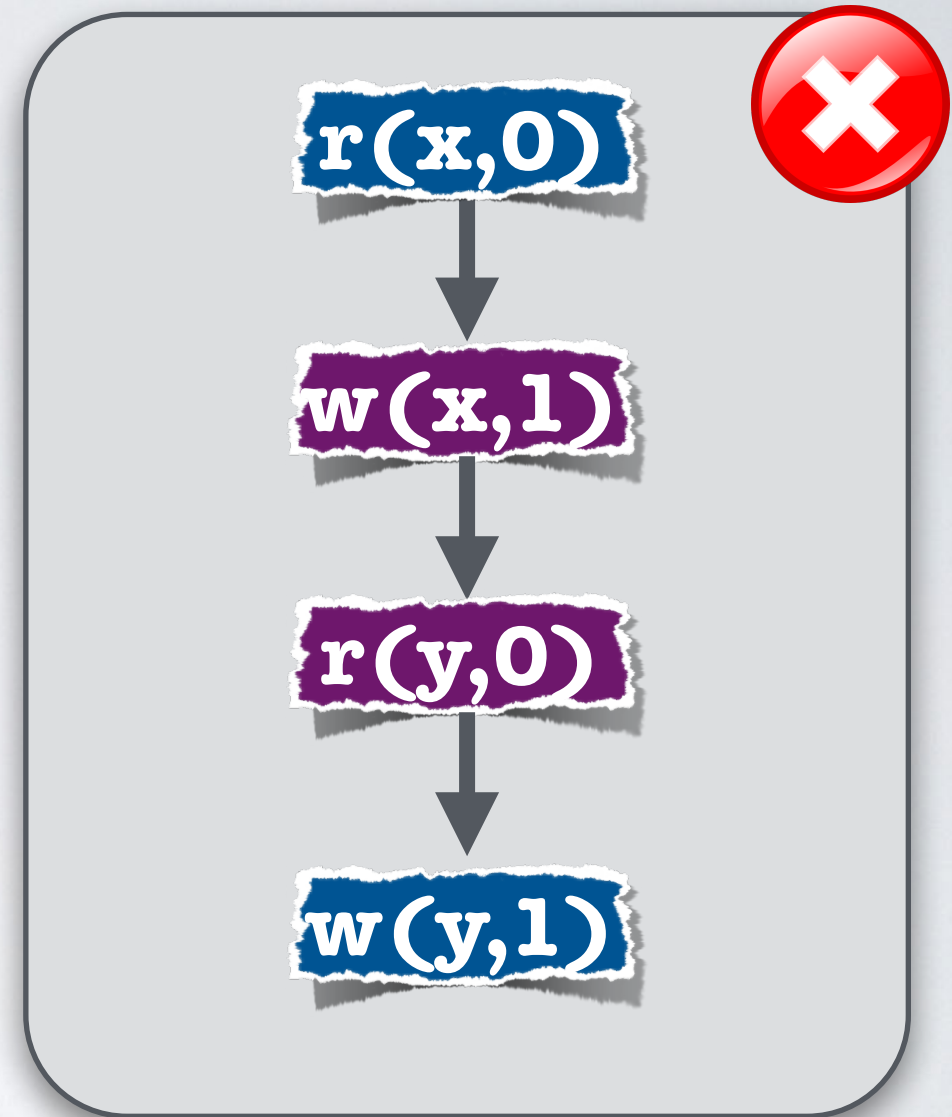
Process 1

1. $rx=x$;
2. $y=1$;

Process 2



RA execution



$x=1$ $y=1$

Shared memory

Specification S: **not** $(rx=0 \ \&\& \ ry=0)$

Message passing

Init: $x=y=0$

1. $x=1$;
2. $y=2$;

Process 1

1. while ($y=0$)
skip;
2. $rx=x$;

Process 2

Specification S: **not** ($rx=0$)

No weak behaviours

Message passing

Init: $x=y=0$

1. $x=1$;
2. $y=2$;

Process 1

1. while ($y=0$)
skip;
2. $rx=x$;

Process 2

Specification S: **not** ($rx=0$)

Reordering writes not allowed
in RA

No weak behaviours

Message passing

Init: $x=y=0$

1. $x=1$;
2. $y=2$;

Process 1

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Process 2

Specification S: **not** ($rx=0$)

Reordering of reads
not allowed in RA

Reordering writes not allowed
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No weak behaviours

Message passing

Init: $x=y=0$

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Process 2 is aware of the write of 1
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No weak behaviours

Message passing

Init: $x=y=0$

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Process 1

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No weak behaviours

Message passing

Init: $x=y=0$

1. $x=1$;
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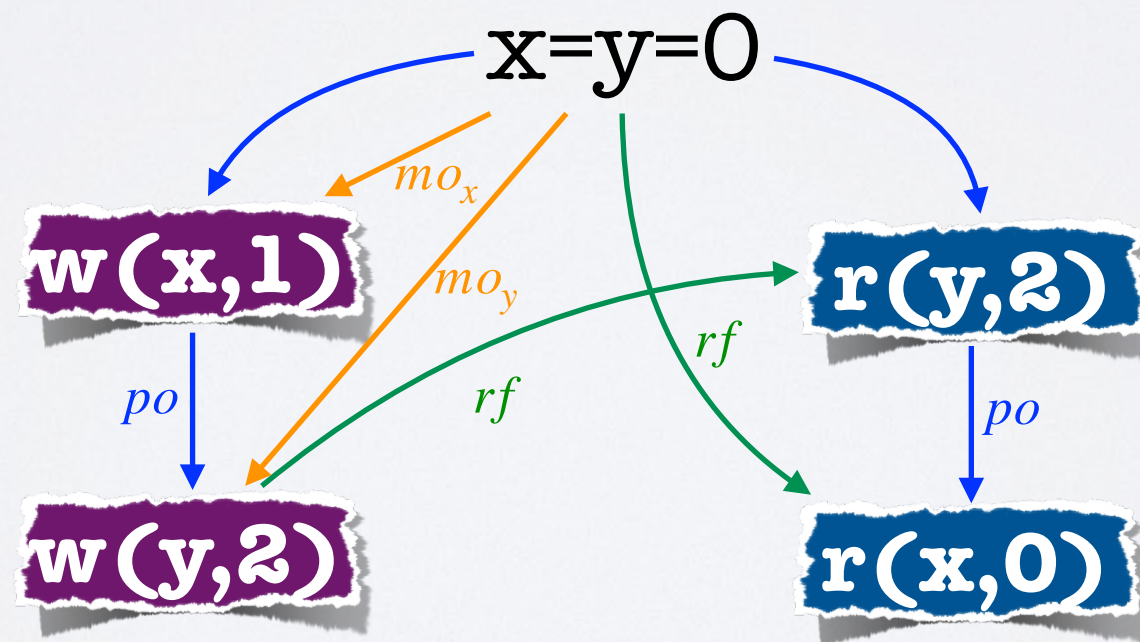
Process 1



1. while ($y=0$)
skip;
2. $rx=x$;

Process 2

Specification S: **not** ($rx=0$)



Message passing

Init: $x=y=0$

1. $x=1$;
2. $y=2$;

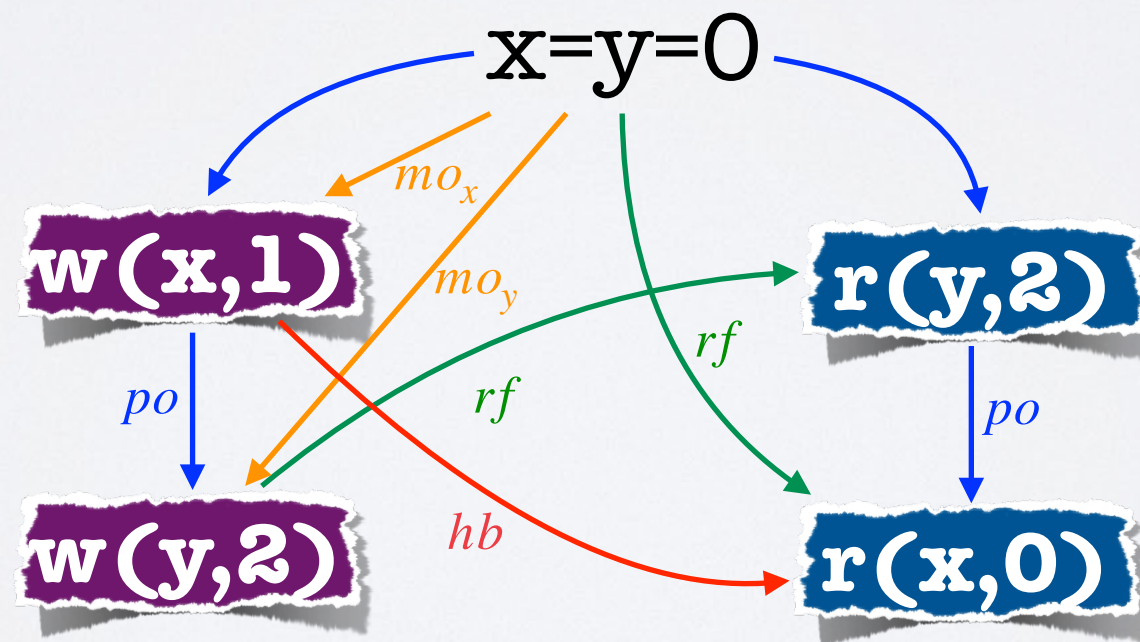
Process 1



1. $\text{while}(y=0)$
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Process 2

Specification S: **not** ($rx=0$)



Declarative Semantics for RA

reads-from (rf) : maps reads to corresponding writes s.t.

happens-before (hb) = ($po \cup rf$)⁺ is irreflexive

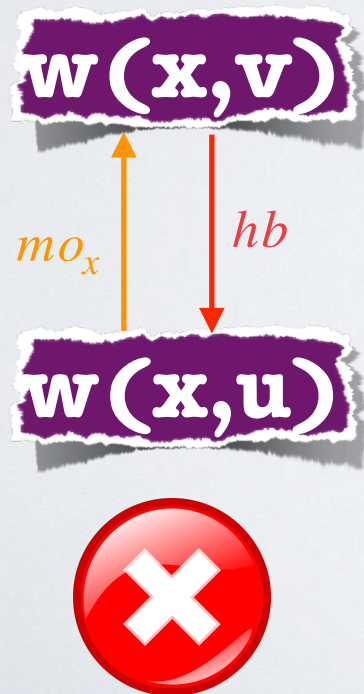
modification-order (mo) : total order on same-location writes s.t.

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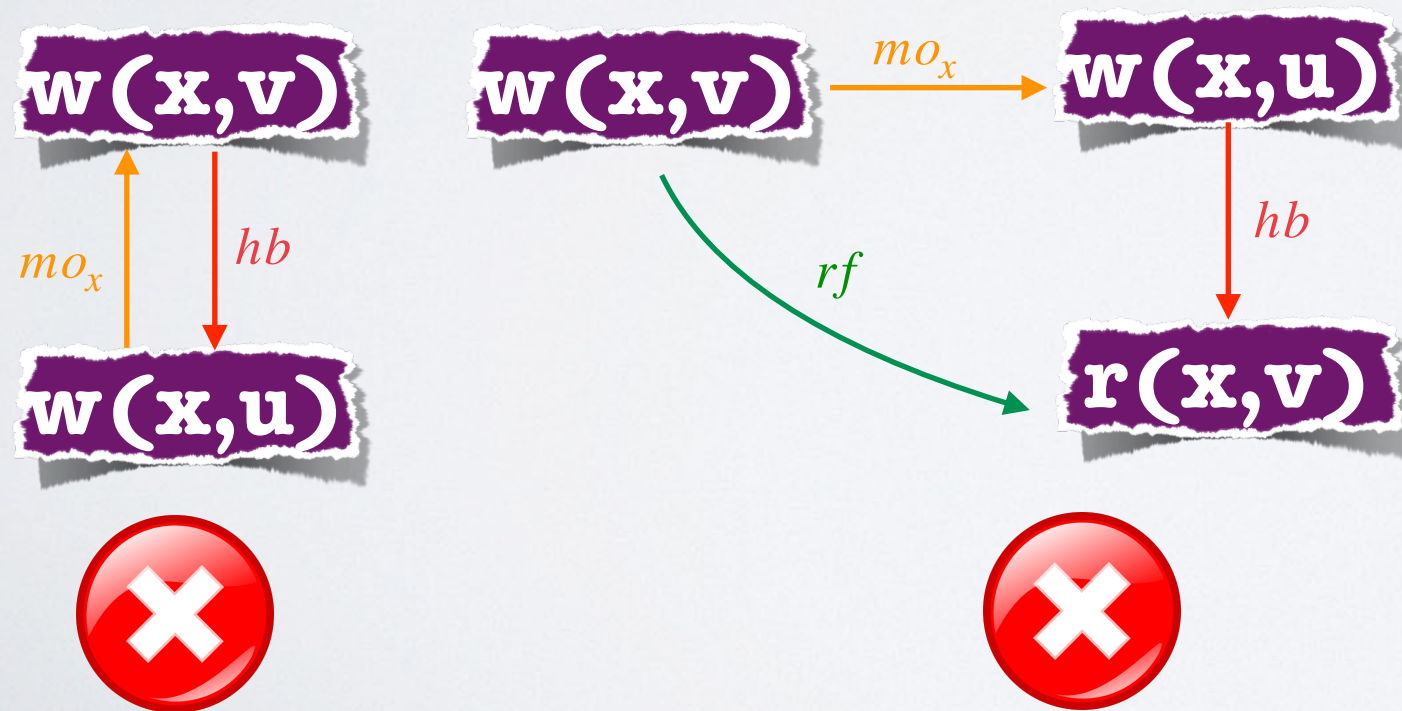


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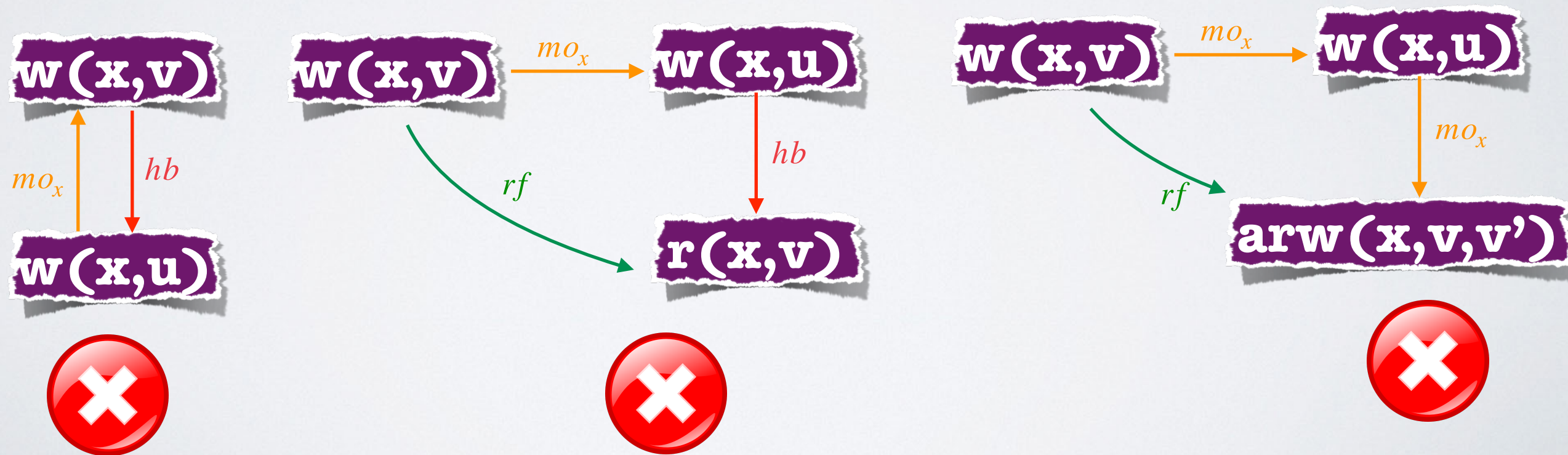


Declarative Semantics for RA

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modification-order (mo) : total order on same-location writes s.t.



Release-Acquire (RA)

Verified compilation schemes
to TSO and Power

Batty et al'11
Batty et al'12
Sarkar et al'12

Supports optimizations
 $Wx \rightarrow Ry$ to $Ry \rightarrow Wx$

SC does not!

Many program logics
RSL, GPS, OGRA

Absence of data races in SC
implies no weak behaviours

Not true for full C11!

Intuitive semantics

Operational Model for RA

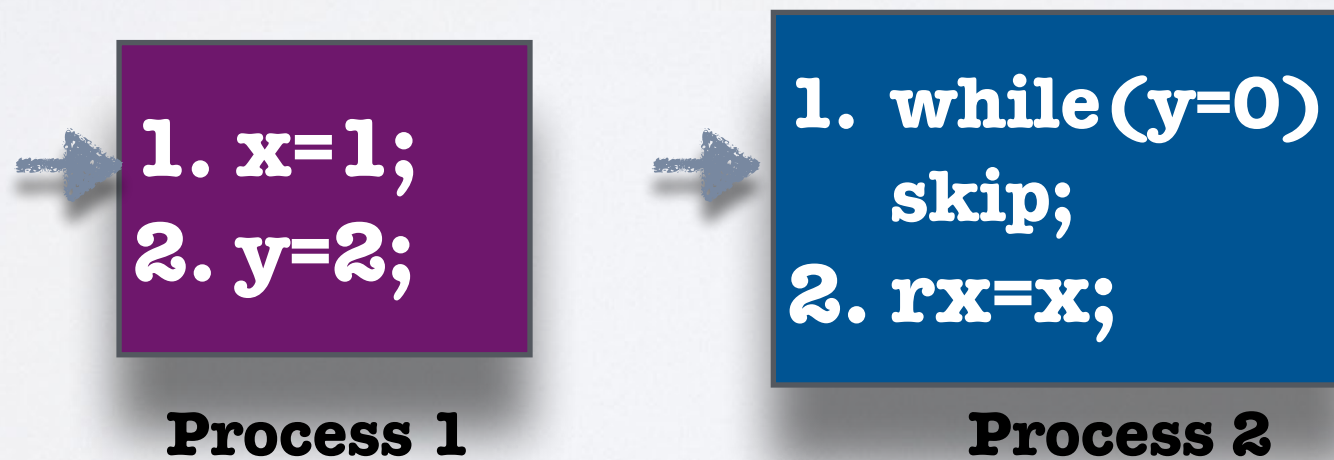
[J. Kang et al. POPL 2017, A. Podkopaev et al. 2016, Arxiv]

Init: x=y=0



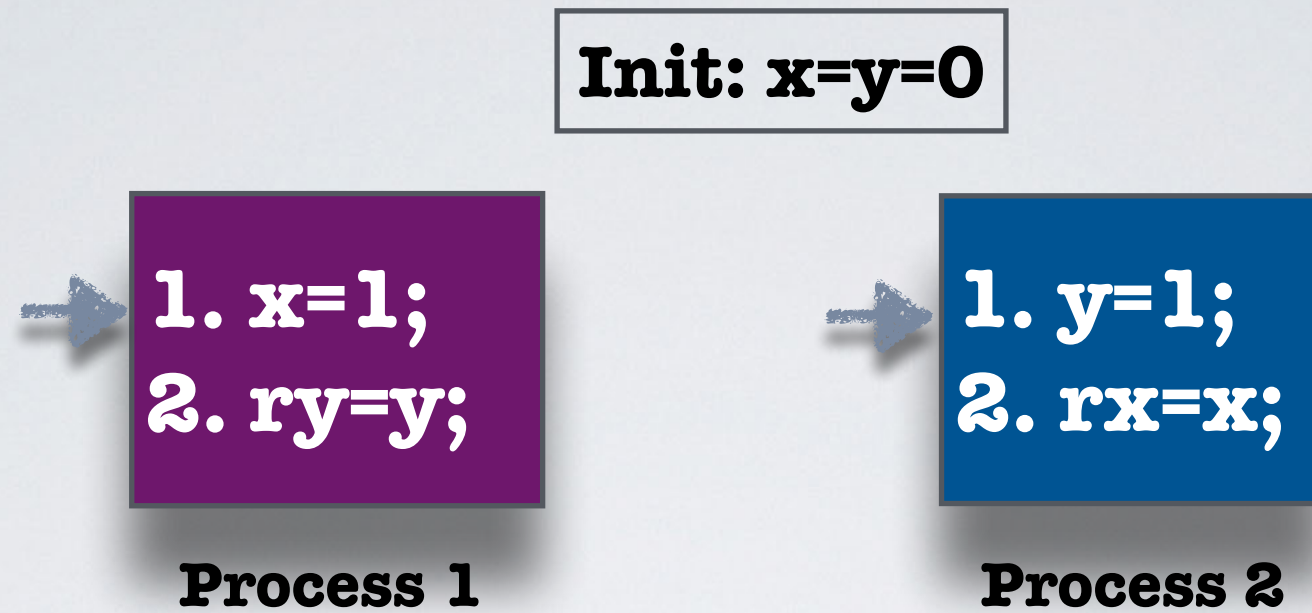
rx=0 and ry=0?

Init: x=y=0



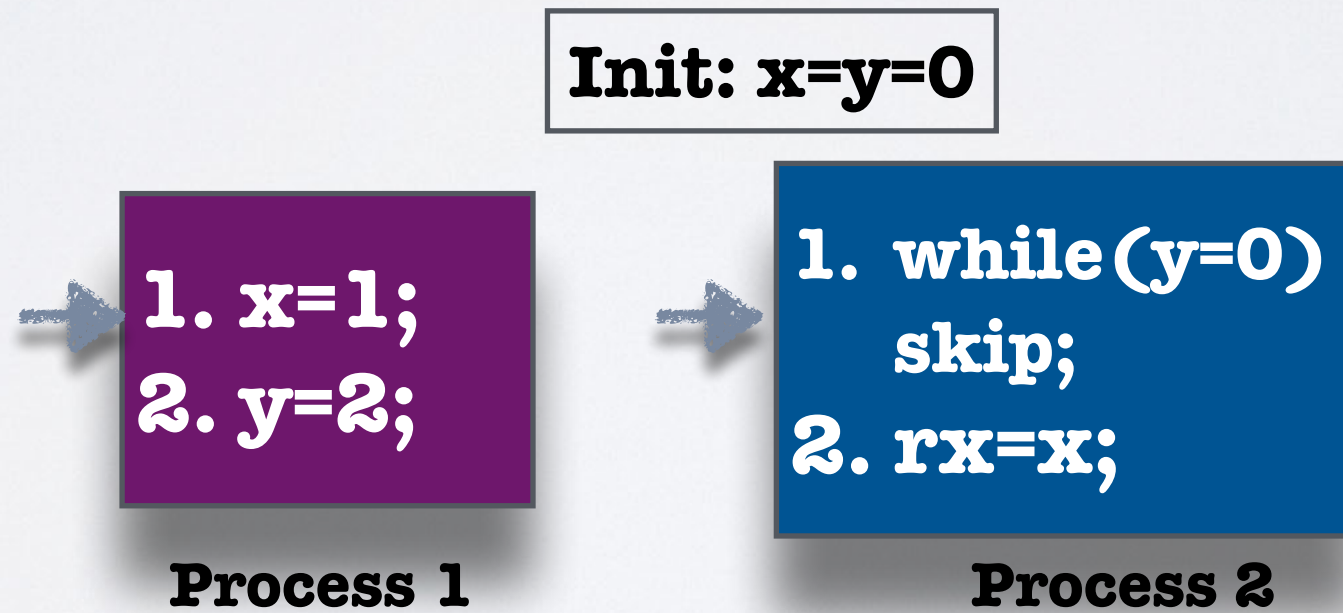
rx=1?

Store buffer



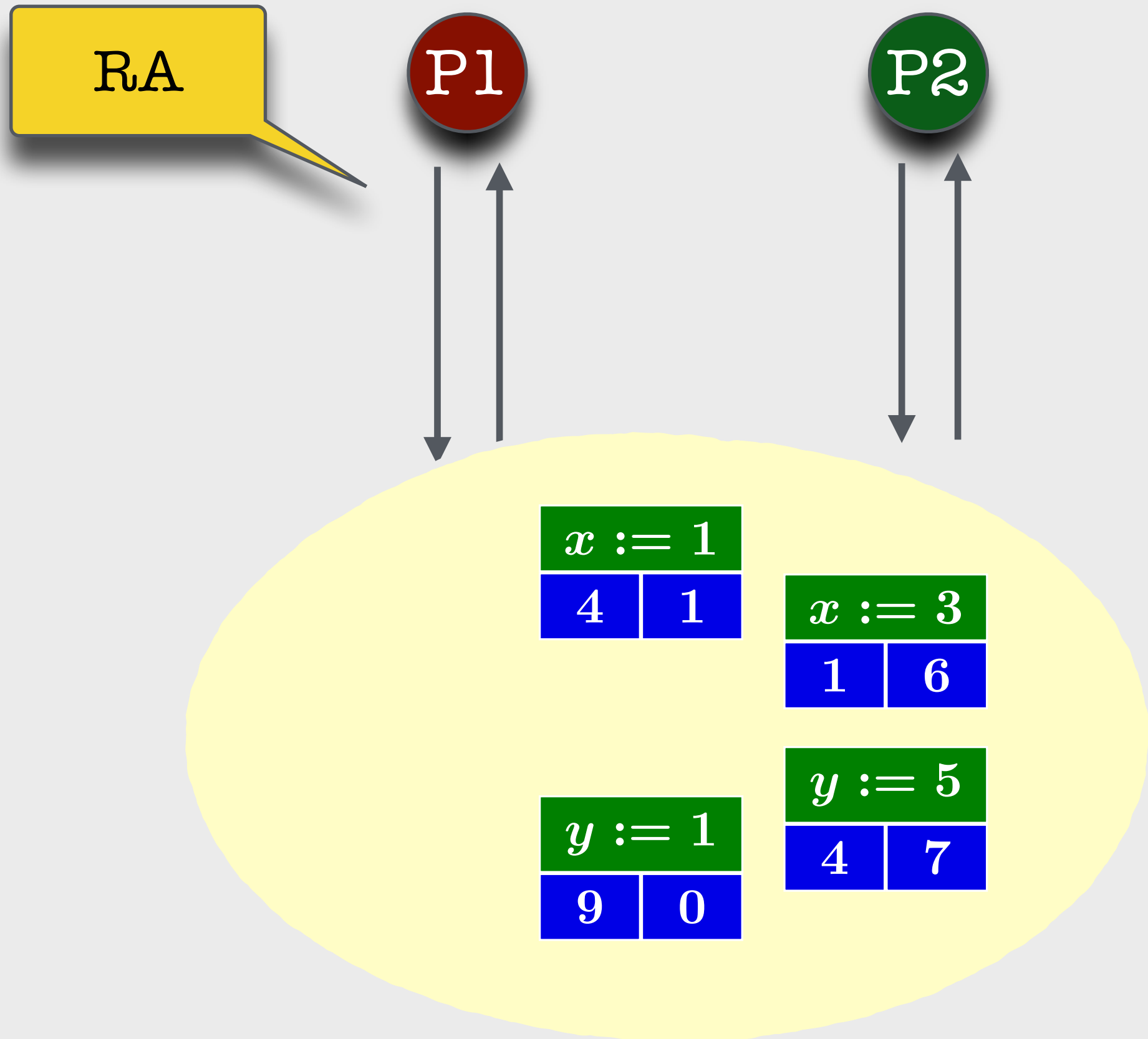
$rx=0$ and $ry=0$?

Message passing

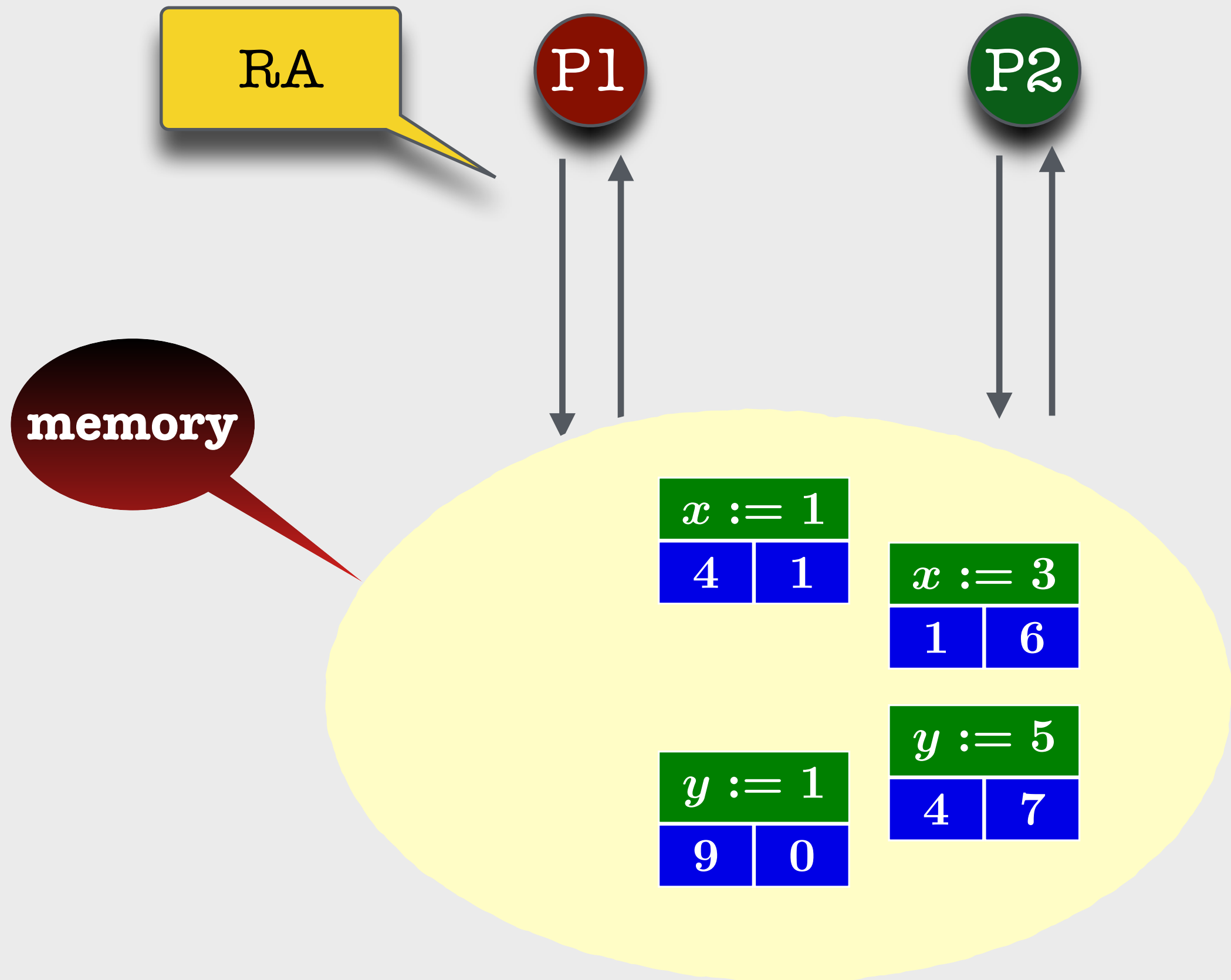


$rx=1$?

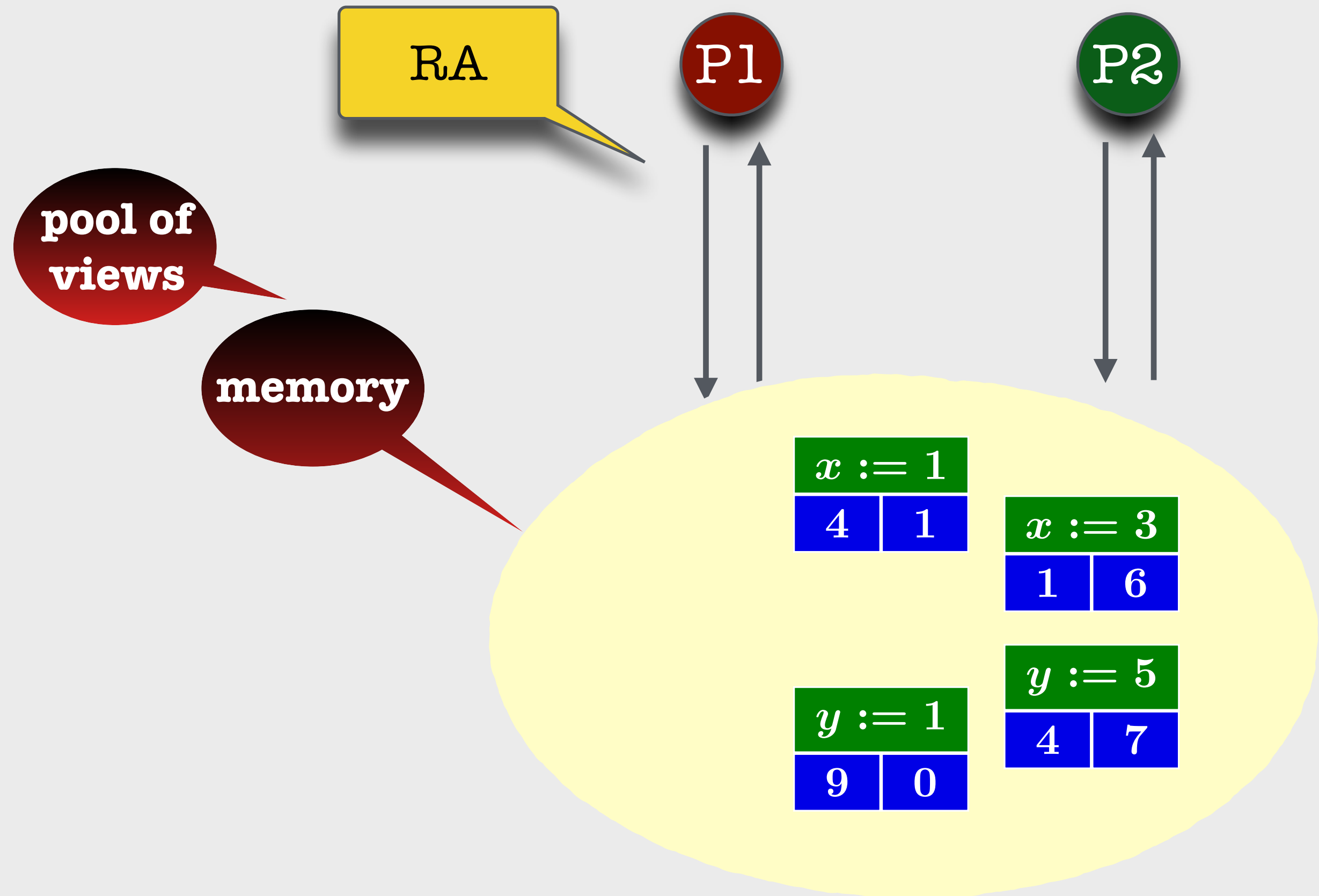
RA: High Level Description



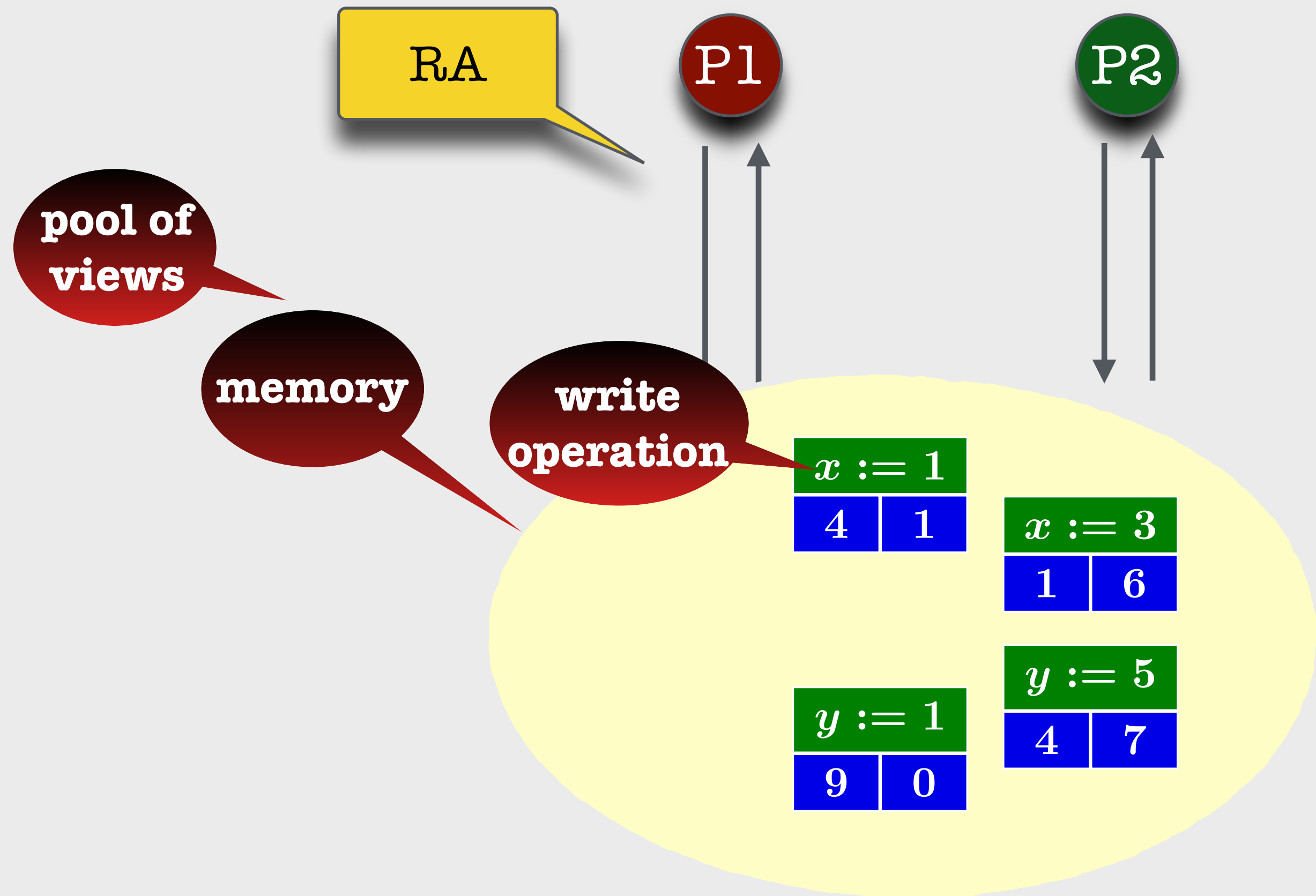
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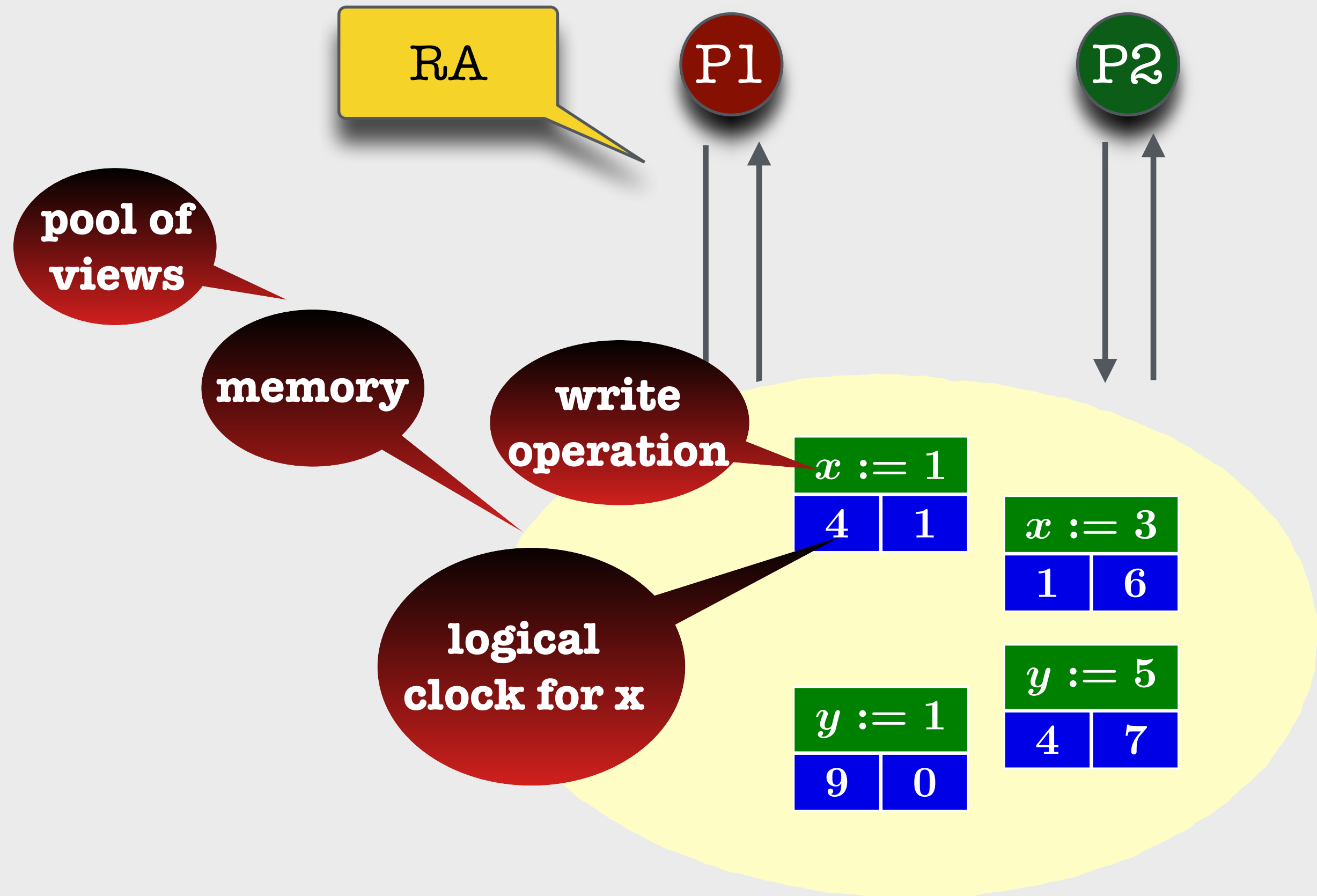
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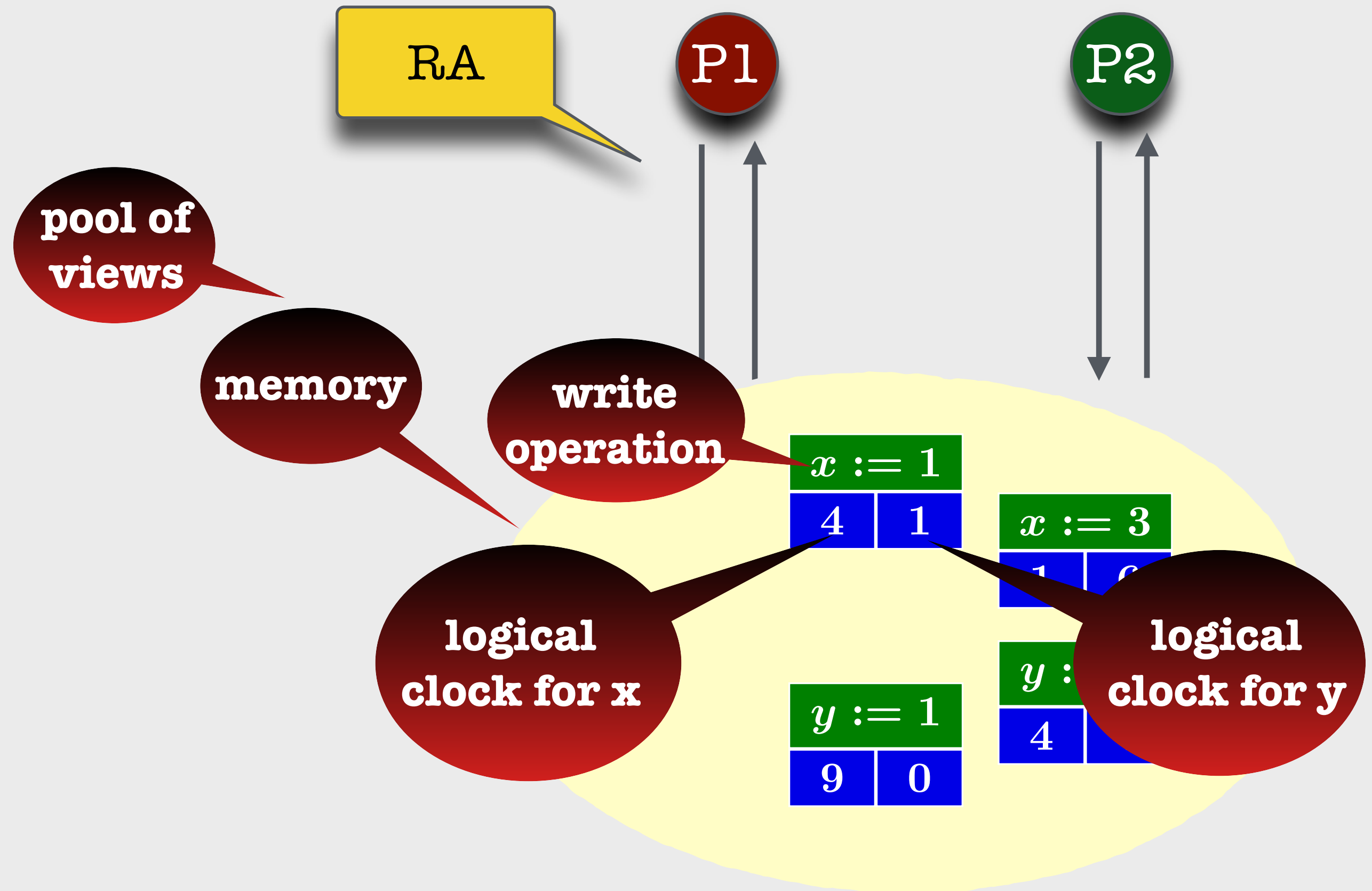
RA: High Level Description



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RA: High Level Description



RA: High Level Description

$P_1 : a := x$

$P_1 : x := 2$

$P_1 : b := y$

3	2
---	---

local
view

P1

P2

$x := 1$	
4	1

$x := 3$	
1	6

$y := 1$	
9	0

$y := 5$	
4	7

RA: High Level Description

$P_1 : a := x$

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3	2
---	---

local
view

P1

Read

1. select view in memory
2. variable time stamp \geq yours
3. update local view

$x := 1$	
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RA: High Level Description

P_1 : $a := x$

P_1 : $x := 2$

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3	2
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local view

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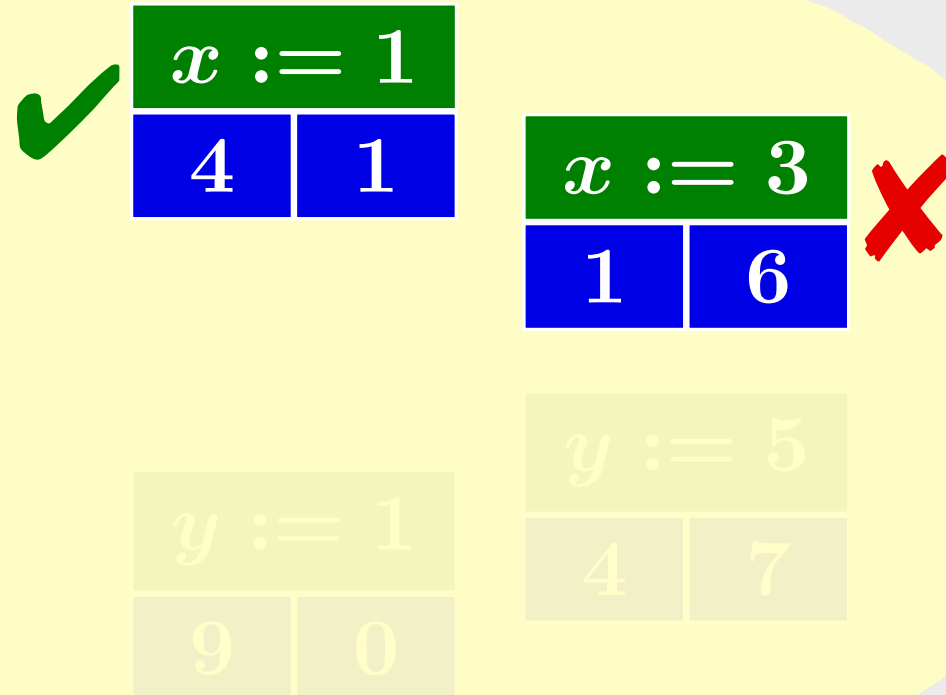
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---	---

local view

P1

Read

1. select view in memory
2. variable time stamp \geq yours
3. update local view



RA: High Level Description

$P_1 : a := x$

$P_1 : x := 2$

$P_1 : b := y$

3	2
---	---

local
view

P1

Read

1. select view in memory
2. ~~variable time stamp > yours~~
3. update local view

$x := 1$

4	1
---	---

$x := 3$

1	6
---	---

$y := 5$

4	7
---	---

$y := 1$

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RA: High Level Description

$P_1 : a := x$

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3	2
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local view

P1

Read

1. select view in memory
2. variable time stamp $>$ yours
3. update local view

max

$x := 1$	
4	1

$x := 3$	
1	6

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9	0

$y := 5$	
4	7

RA: High Level Description

$P_1 : a := x$

$P_1 : x := 2$

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4	2
---	---

local view

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1. select view in memory
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4	1

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RA: High Level Description

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local view

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4	1

$x := 3$	
1	6

$y := 1$	
9	0

$y := 5$	
4	7

RA: High Level Description

Write

1. create new local view
2. variable time stamp:
 - i. newer than yours
 - ii. not in memory
3. copy new view to memory

P_1 : $a := 1$

P_1 : $x := 2$

▶ P_1 : $b := y$

4	2
---	---

local view

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$x := 1$	
4	1

$x := 3$	
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$y := 5$	
4	7

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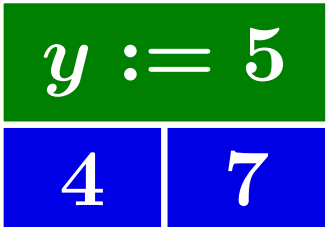
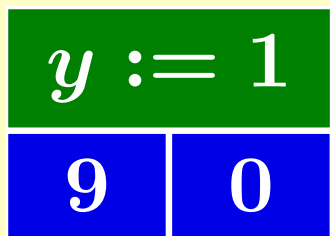
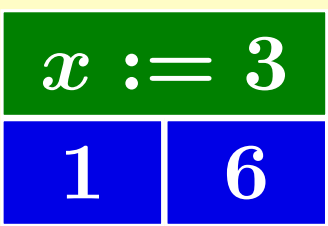
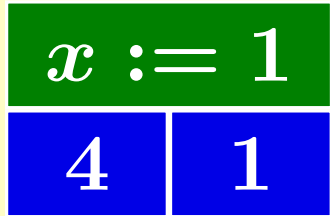
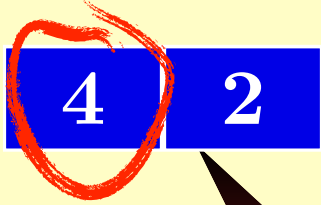
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$P_1 : a := 1$

$P_1 : x := 2$

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ii. not in memory

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$P_1 : a := 1$

$P_1 : x := 2$

▶ $P_1 : b := y$

$x := 2$
5 2

5 2

4 2

P1

local view

$x := 1$
4 1

4 1

$x := 3$
1 6

1 6

$y := 1$
9 0

9 0

$y := 5$
4 7

4 7

RA: High Level Description

Write

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$P_1 : a := 1$

$P_1 : x := 2$



$P_1 : b := y$

$x := 2$

5	2
---	---

5	2
---	---

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$y := 1$

9	0
---	---

$y := 5$

4	7
---	---

RA: High Level Description

Write

1. create new local view

2. variable time stamp:

i. newer than yours

ii. not in memory

3. copy new view to memory

$P_1 : a := 1$

$P_1 : x := 2$



$P_1 : b := y$

$x := 2$

5	2
---	---

5	2
---	---

P1

local view

$x := 1$

4	1
---	---

$x := 3$

1	6
---	---

$y := 1$

9	0
---	---

$y := 5$

4	7
---	---

RA: High Level Description

Write

1. create new local view

2. variable time stamp:

i. newer than yours

ii. not in memory

3. copy new view to memory

$P_1 : a := 1$

$P_1 : x := 2$

$P_1 : b := y$

P1

5	2
---	---

local view

$x := 2$
5 2

$x := 1$
4 1

$y := 1$
9 0

$x := 3$
1 6

$y := 5$
4 7

RA: High Level Description

P_1 : $a := 1$

P_1 : $x := 2$

P_1 : $b := y$

5	2
---	---

local
view

P1

P2

$x := 2$
5 2

$x := 1$
4 1

$y := 1$
9 0

$x := 3$
1 6

$y := 5$
4 7

RA: High Level Description

P_1 : $a := 1$

P_1 : $x := 2$

P_1 : $b := y$

5	2
---	---

local view

P1

Read

1. select view in memory
2. variable time stamp \geq yours
3. update local view

$x := 2$
5 2

$x := 1$
4 1

$x := 3$
1 6

$y := 1$
9 0

$y := 5$
4 7



RA: High Level Description

P_1 : $a := 1$

P_1 : $x := 2$

P_1 : $b := y$

5	2
---	---

local view

P1

Read

1. select view in memory
2. variable time stamp \geq yours
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$x := 2$
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RA: High Level Description

P_1 : $a := 1$

P_1 : $x := 2$

P_1 : $b := 5$

5	2
---	---

local view

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Read

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$x := 2$
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4 1

$x := 3$
1 6

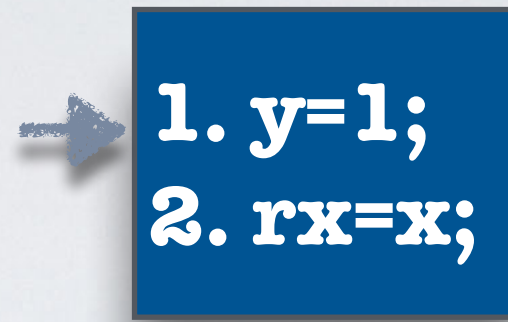
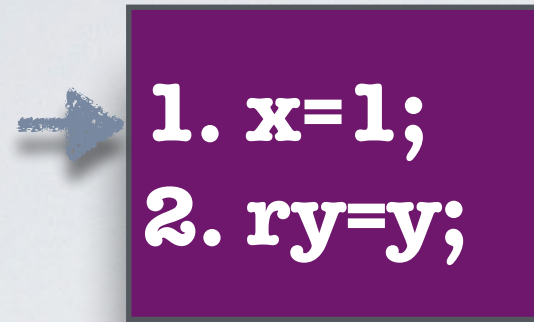
$y := 1$
9 0

$y := 5$
4 7

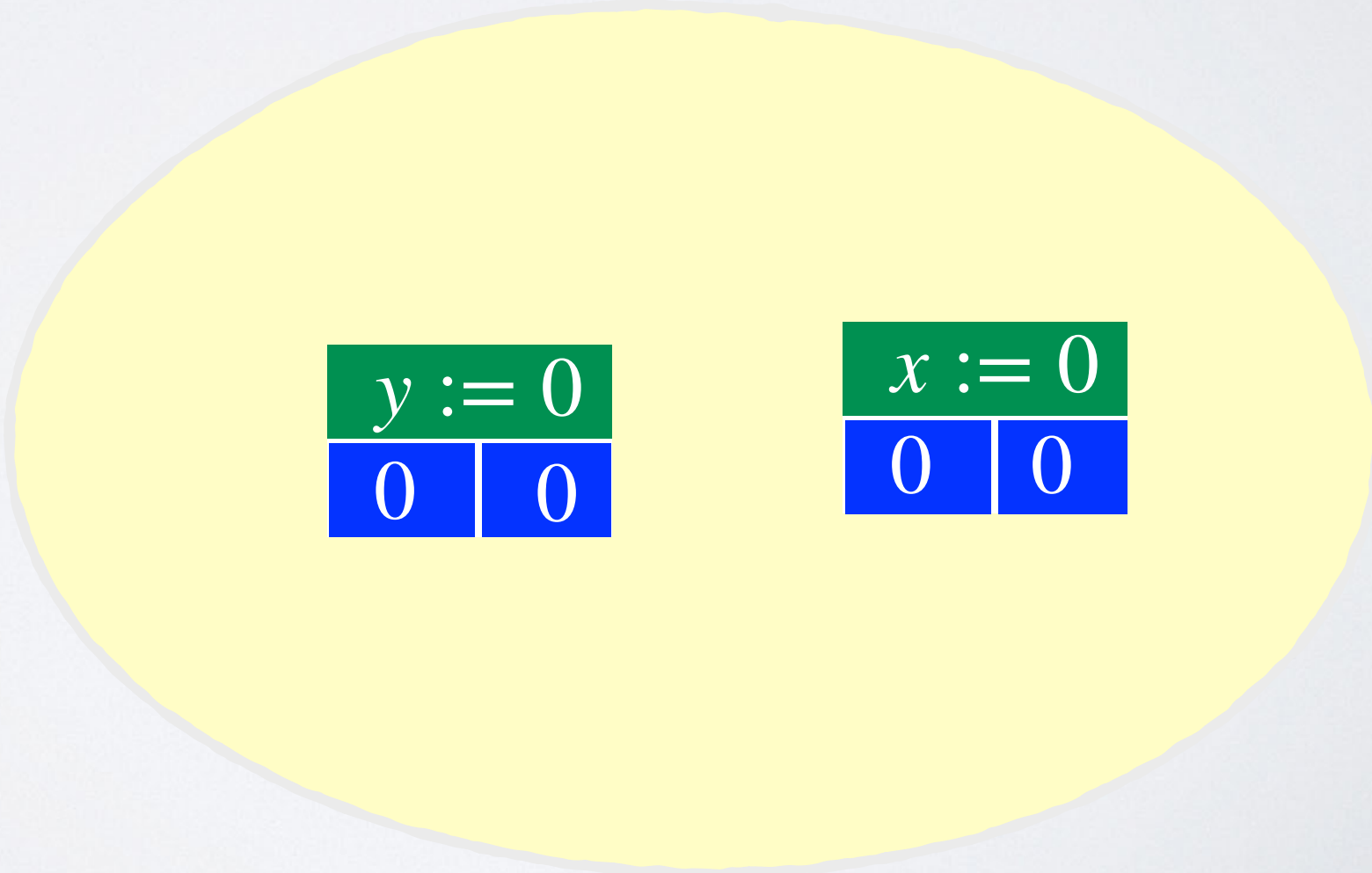


Init: $x=y=0$

Store buffer

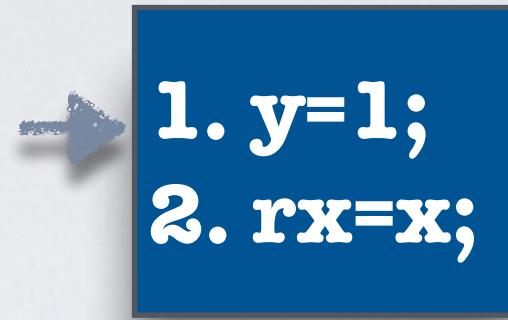
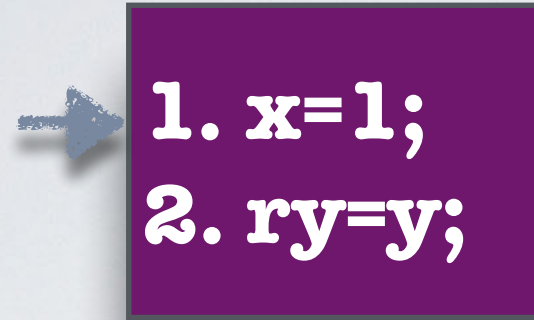


$rx=0$ and $ry=0$?

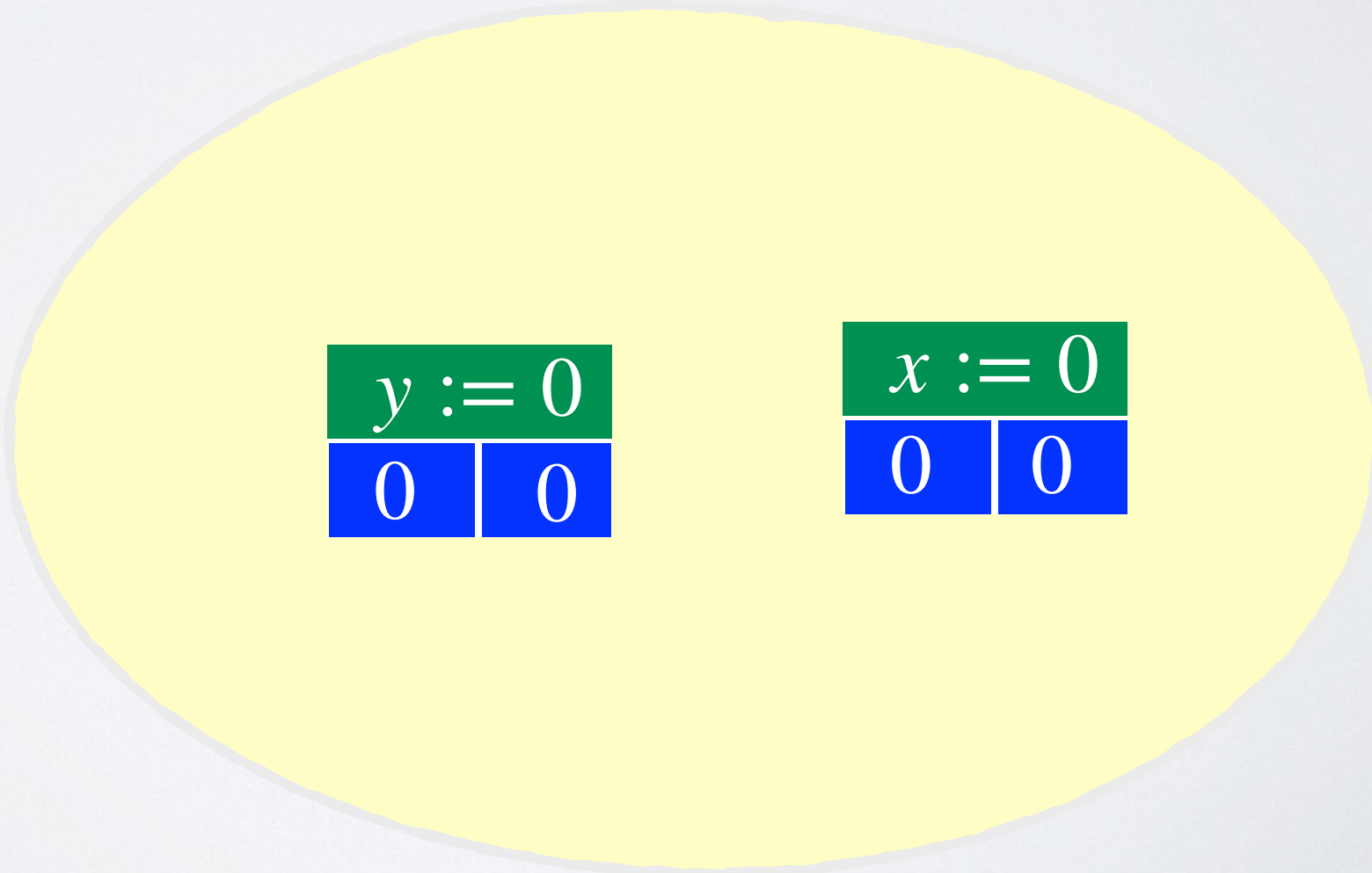


Init: x=y=0

Store buffer

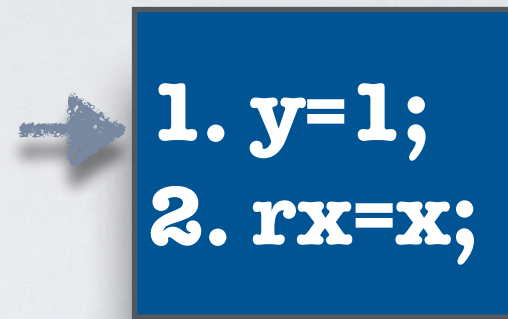
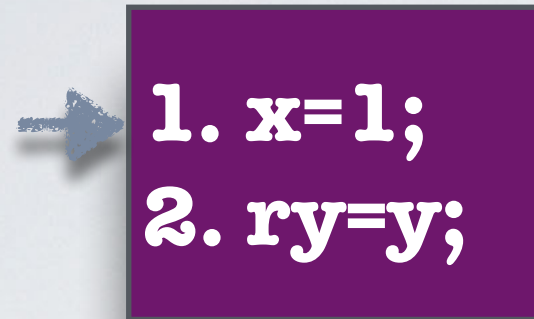


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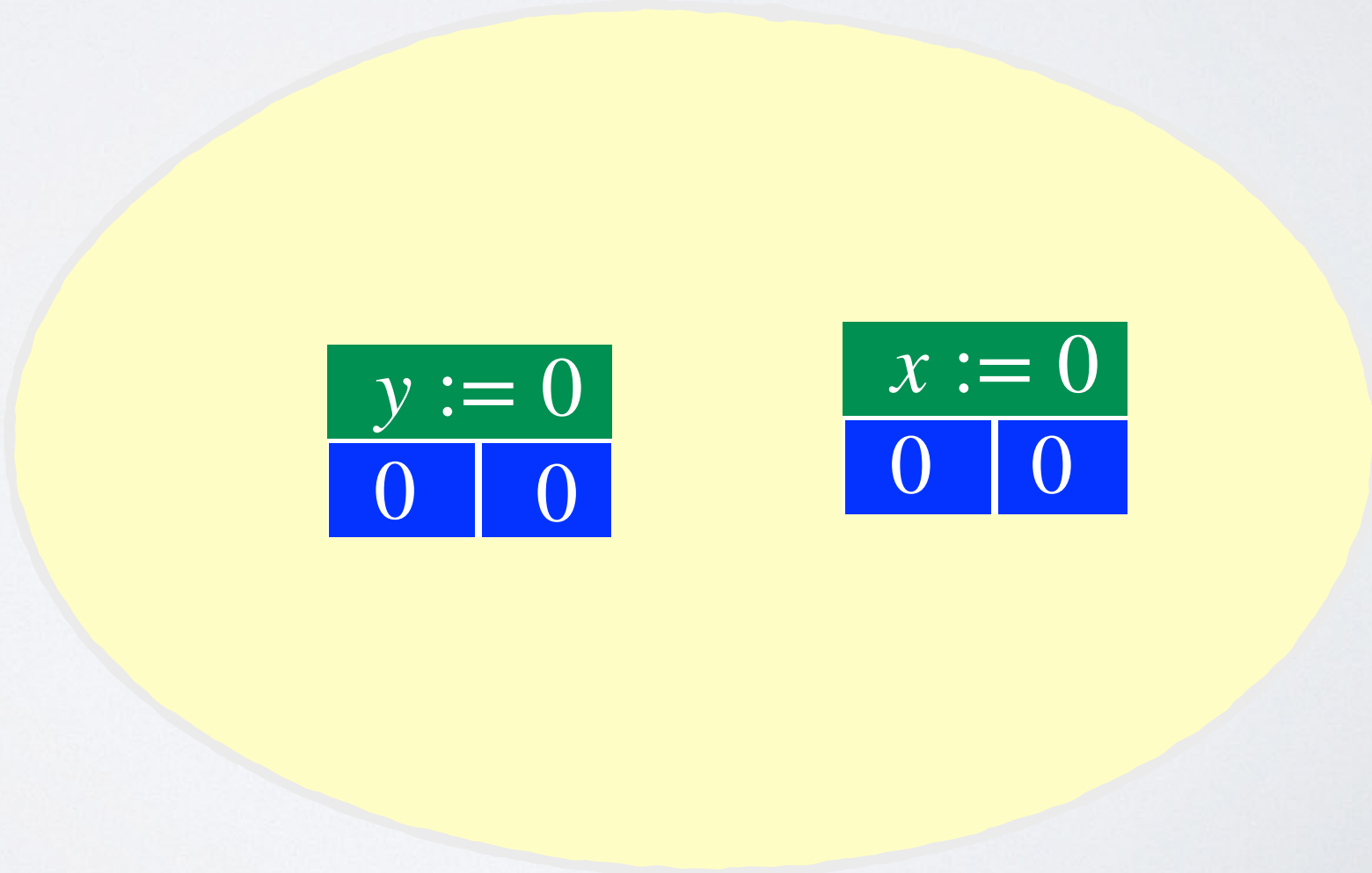


Init: x=y=0

Store buffer

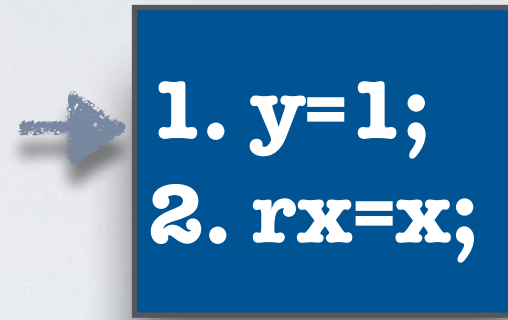
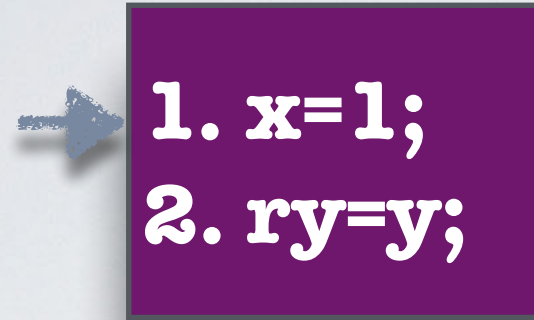


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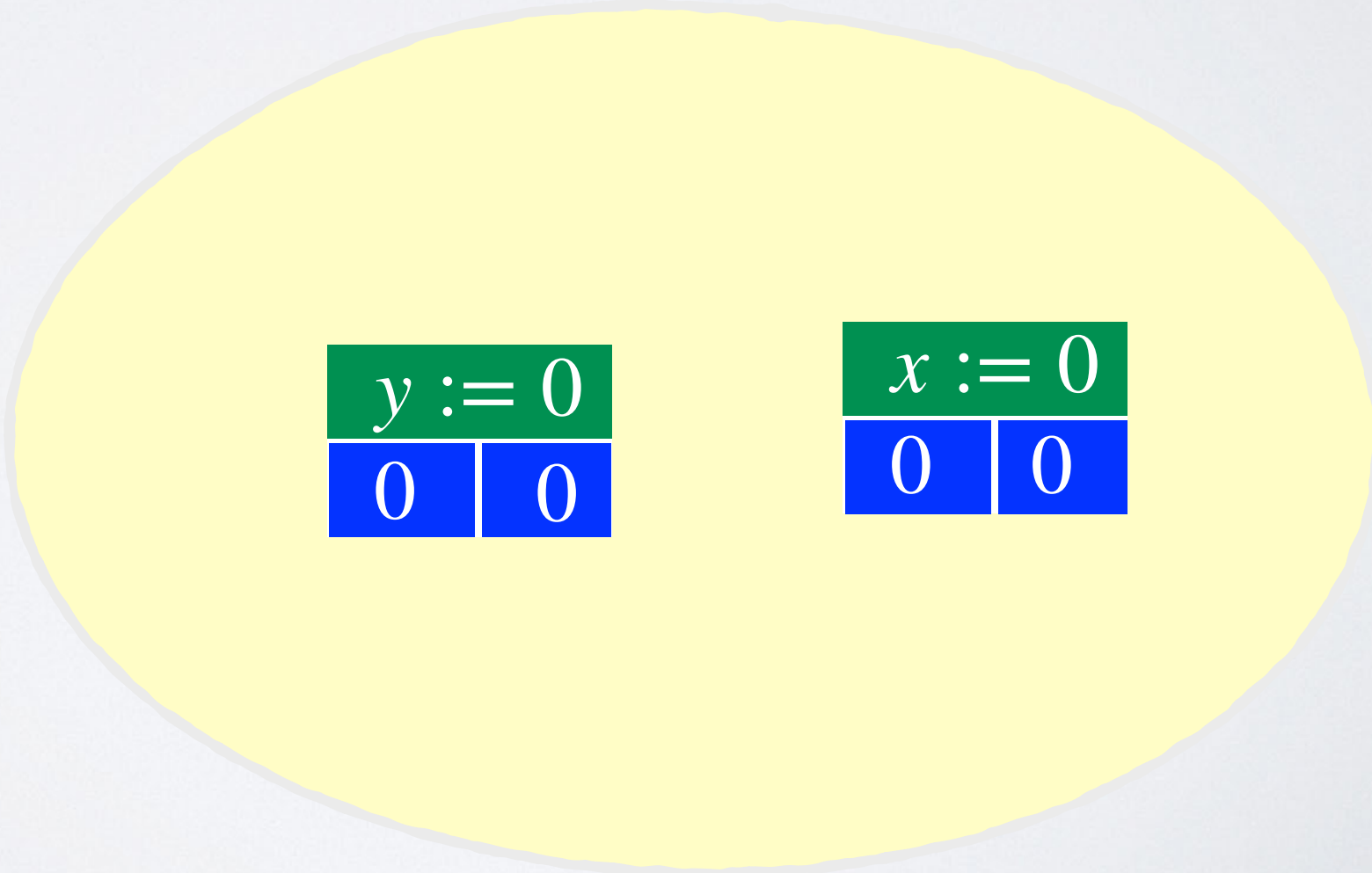


Init: $x=y=0$

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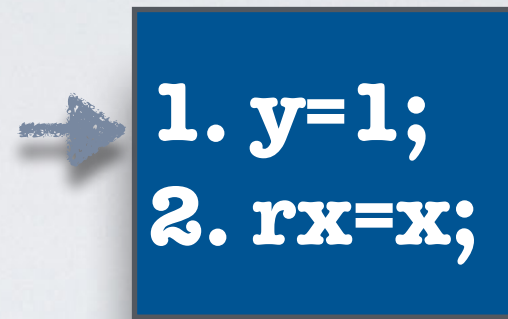
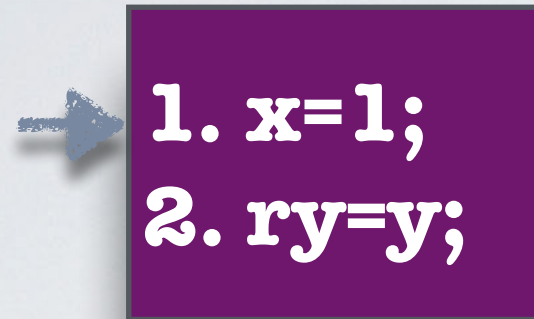


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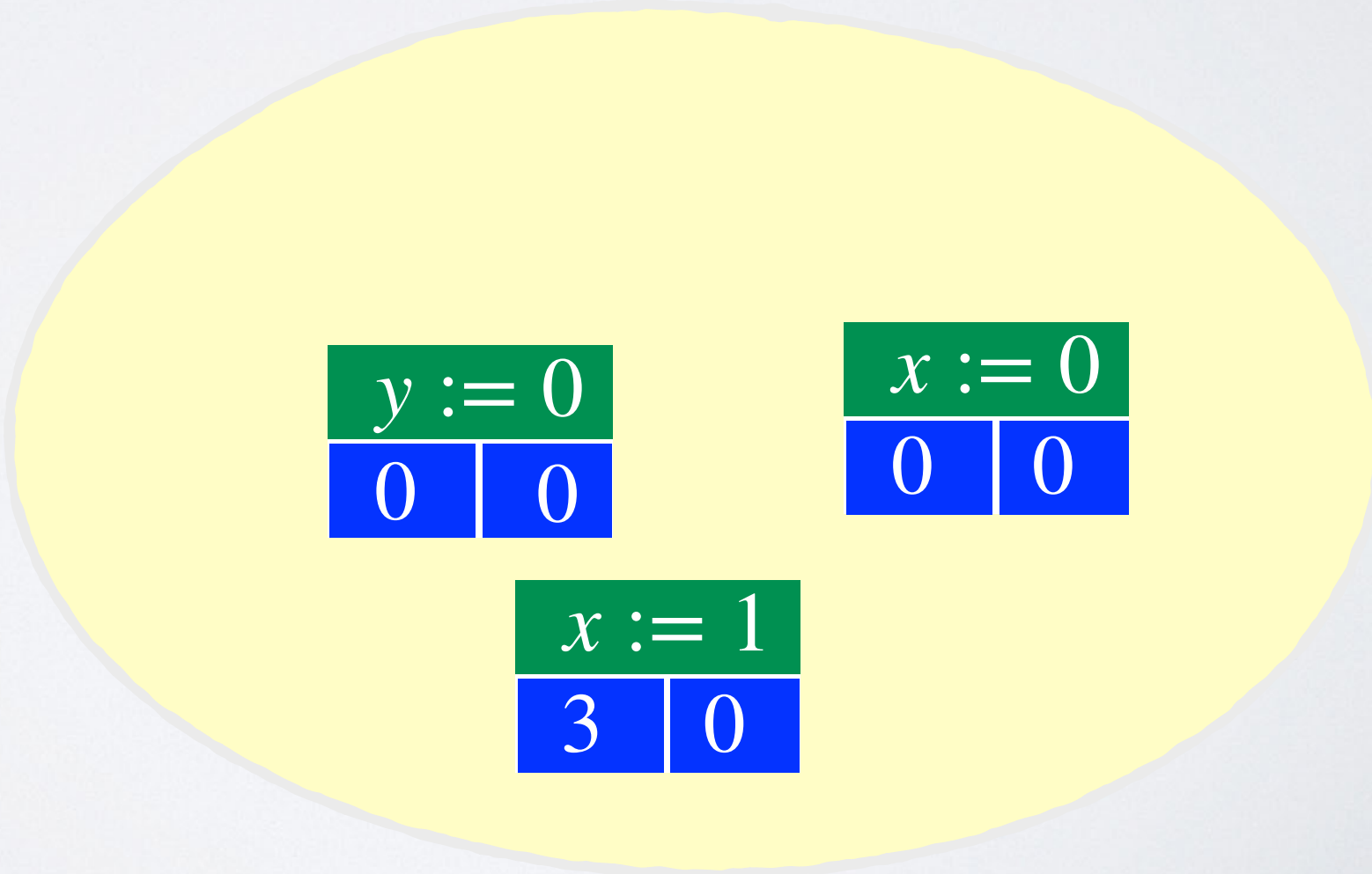


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Store buffer

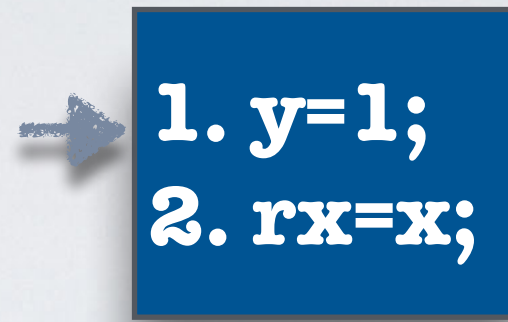
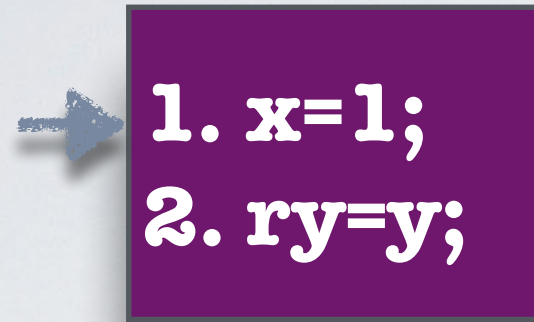


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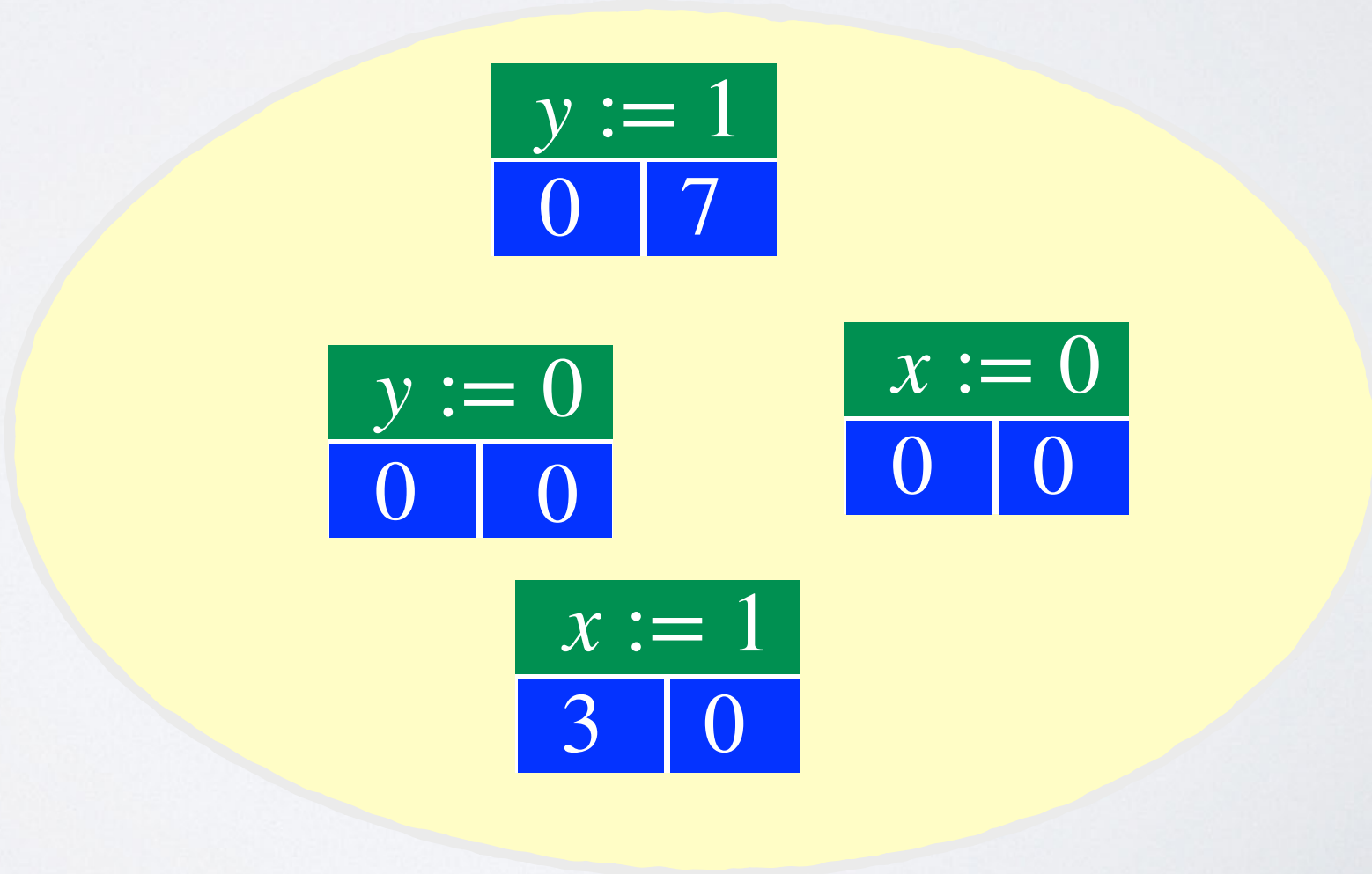


Init: $x=y=0$

Store buffer



$rx=0$ and $ry=0$?



RA: High Level Description

▶ $r := 1$

$y := 1$

$r' := 2$

$\text{arw}(x, r, r')$

0	0
---	---

also known as CAS/RMW

$x := 1$	
3	0

$x := 0$	
0	0

$y := 0$	
0	0

RA: High Level Description

$r := 1$

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RA: High Level Description

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0	7
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Atomic read write (or CAS)

1. select view in memory with value of r
2. variable time stamp $t \geq$ yours
3. update local view, and write with value of r'
4. choose timestamp $t+1$ in the new message

also known as CAS/RMW

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3	0

$x := 0$	
0	0

$y := 1$	
0	7

$y := 0$	
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RA: High Level Description

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$r' := 2$

$\text{arw}(x, r, r')$

4	7
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4. choose timestamp $t+1$ in the new message

also known as CAS/RMW

$x := 2$
4 7

$x := 1$
3 0

$x := 0$
0 0

$y := 1$
0 7

$y := 0$
0 0

RA run

Register values: $\$r1=0$ $\$r2=0$

Process 1

1. $\$r1 = x;$
2. $y = 1;$
3. $\$r3 = x;$

0 | 0

Process 2

1. $\$r2 = y;$
2. $x = 1;$
3. $x = 2;$

0 | 0

$x := 0$
0 | 0

$y := 0$
0 | 0

Reachable: $\$r1 = 0$, $\$r2 = 1$ and $\$r3 = 2$?

RA run

Register values: $\$r1=0$ $\$r2=0$

Process 1

1. $\$r1 = 0;$
2. $y = 1;$
3. $\$r3 = x;$

0 | 0

Process 2

1. $\$r2 = y;$
2. $x = 1;$
3. $x = 2;$

0 | 0

$x := 0$
0 | 0

$y := 0$
0 | 0

$r(x,0)$

Reachable: $\$r1 = 0$, $\$r2 = 1$ and $\$r3 = 2$?

RA run

Register values: $\$r1=0$ $\$r2=0$

Process 1

1. $\$r1 = 0;$
2. $y = 1;$
3. $\$r3 = x;$

0	2
---	---

Process 2

1. $\$r2 = y;$
2. $x = 1;$
3. $x = 2;$

0	0
---	---

$y := 1$	
0	2

$x := 0$	
0	0

$y := 0$	
0	0

Reachable: $\$r1 = 0$, $\$r2 = 1$ and $\$r3 = 2$?

$r(x,0)$

$w(y,1)$

Register values: $\$r1=0$ $\$r2=0$

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0	2
---	---

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2. $x = 1;$
3. $x = 2;$

0	2
---	---

$y := 1$	
0	2

$x := 0$	
0	0

$y := 0$	
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Reachable: $\$r1 = 0$, $\$r2 = 1$ and $\$r3 = 2$?

RA run

$r(x,0)$

$w(y,1)$

$r(y,1)$

Register values: $\$r1=0$ $\$r2=0$

Process 1

1. $\$r1 = 0$;
2. $y = 1$;
3. $\$r3 = x$;

0 | 2

Process 2

1. $\$r2 = 1$;
2. $x = 1$;
3. $x = 2$;

1 | 2

$x := 1$
1 | 2

$y := 1$
0 | 2

$x := 0$
0 | 0

$y := 0$
0 | 0

Reachable: $\$r1 = 0$, $\$r2 = 1$ and $\$r3 = 2$?

RA run

$r(x,0)$

$w(y,1)$

$r(y,1)$

$w(x,1)$

Register values: $\$r1=0$ $\$r2=0$

Process 1

1. $\$r1 = 0;$
2. $y = 1;$
3. $\$r3 = x;$

0	2
---	---

Process 2

1. $\$r2 = 1;$
2. $x = 1;$
3. $x = 2;$

3	2
---	---

$x := 1$
1 2

$x := 2$
3 2

$y := 1$
0 2

$x := 0$
0 0

$y := 0$
0 0

RA run

$r(x,0)$

$w(y,1)$

$r(y,1)$

$w(x,1)$

$w(x,2)$

Reachable: $\$r1 = 0$, $\$r2 = 1$ and $\$r3 = 2$?

Register values: $\$r1=0$ $\$r2=0$

Process 1

1. $\$r1 = 0$;
2. $y = 1$;
3. $\$r3 = 2$;

Process 2

1. $\$r2 = 1$;
2. $x = 1$;
3. $x = 2$;

3 2

3 2

$x := 1$
1 2

$x := 2$
3 2

$y := 1$
0 2

$x := 0$
0 0

$y := 0$
0 0

Reachable: $\$r1 = 0$, $\$r2 = 1$ and $\$r3 = 2$?

RA run

$r(x,0)$

$w(y,1)$

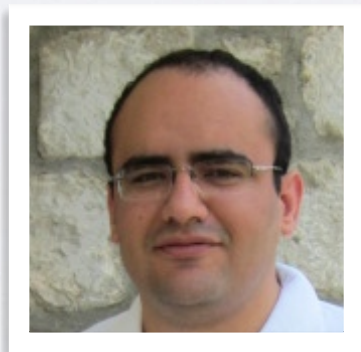
$r(y,1)$

$w(x,1)$

$w(x,2)$

$r(x,2)$

(Non parameterized) Reachability under RA



PLDI 2019

The Reachability Problem

Given a **program P** and a (control + memory) **state s**

- State Reachability Problem (Safety)

Is **s** reachable in **P** under **RA** ?

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Each process is finite-state

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The Reachability Problem

Given a **program P** and a (control + memory) **state s**

- State Reachability Problem (Safety)

Is **s** reachable in **P** under **RA**?

Decidability/ Complexity?

Each process is finite-state

- For **SC**, the reachability problem is PSPACE-complete
- **Nontrivial** for **RA** since the set of paths is **nonregular**

The state reachability problem is **undecidable** for RA

- By reduction from the Post's correspondence Problem

	<i>u</i>	<i>v</i>
1.	<i>b</i>	<i>bb</i>
2.	<i>abba</i>	<i>a</i>
3.	<i>ba</i>	<i>aa</i>
4.	<i>b</i>	<i>abb</i>

214 is a solution

The state reachability problem is **undecidable** for RA

- By reduction from the Post's correspondence Problem

The state reachability problem is **undecidable** for RA

- By reduction from the Post's correspondence Problem

P1

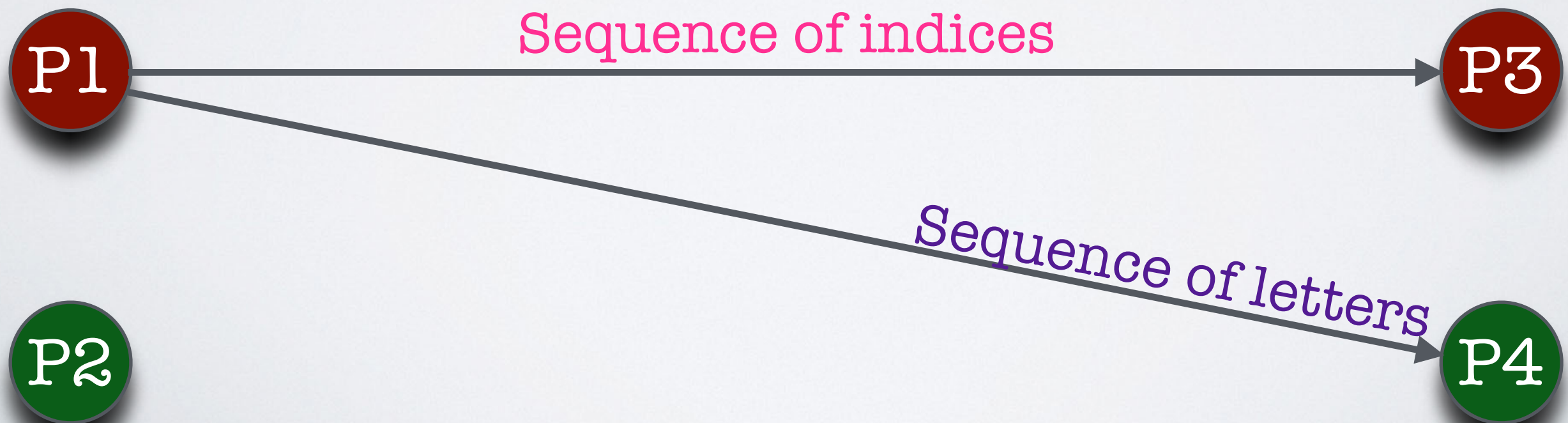
P3

P2

P4

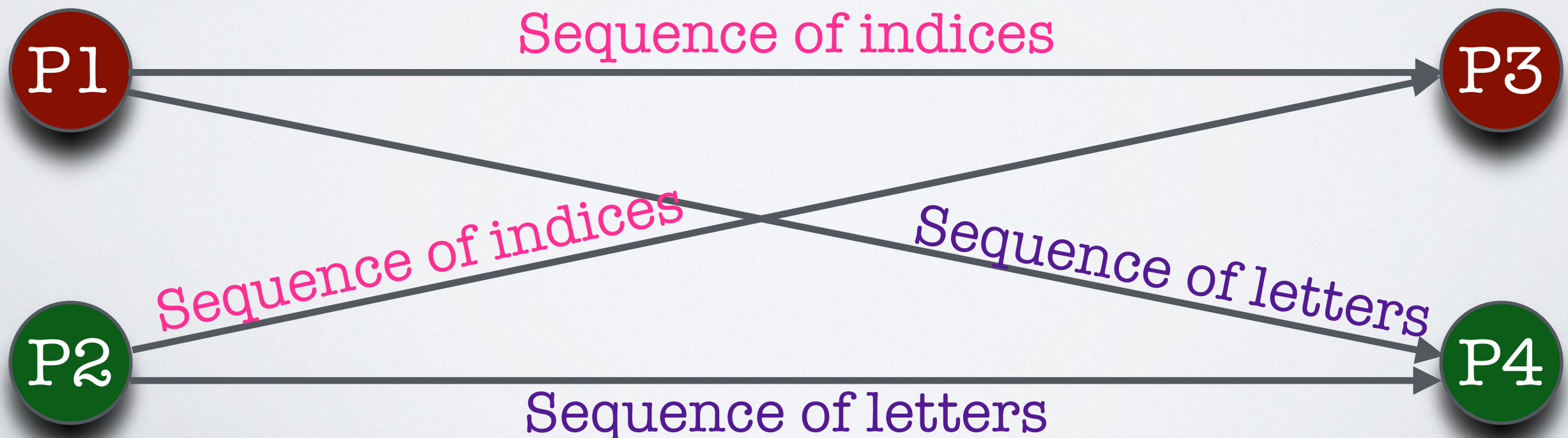
The state reachability problem is **undecidable** for RA

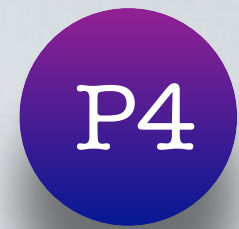
- By reduction from the Post's correspondence Problem



The state reachability problem is **undecidable** for RA

- By reduction from the Post's correspondence Problem




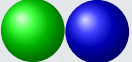

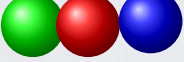

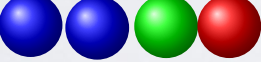
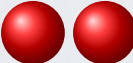



1

2

3

4


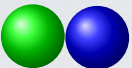
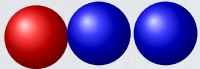
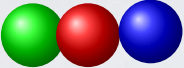

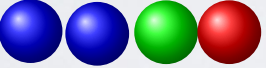


	A	B
1		
2		
3		
4		

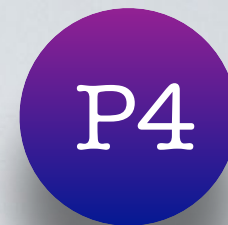
1

2

3

4

	A	B
1		
2		
3		
4		



$$x_1 = \text{green ball}$$

$$x_2 = \text{blue ball}$$

$$x_1 = \text{green ball}$$

$$y_1 = 1$$

$$x_2 = \text{red ball}$$

$$x_1 = \text{blue ball}$$

$$x_2 = \text{blue ball}$$


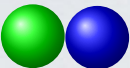
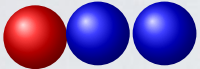
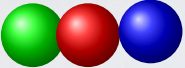

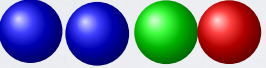


$$y_2 = 2$$

$$x_1 = \text{green ball}$$

$$x_2 = \text{red ball}$$

$$x_1 = \text{red ball}$$

$$y_1 = 3$$

	A	B
1		
2		
3		
4		

P1

P2

P4

$$x_1 = \text{green ball}$$

$$x_2 = \text{blue ball}$$

$$x_1 = \text{green ball}$$

$$y_1 = 1$$

$$x_2 = \text{red ball}$$

$$x_1 = \text{blue ball}$$

$$x_2 = \text{blue ball}$$

$$y_2 = 2$$


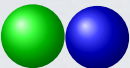
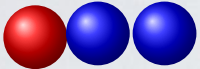
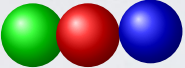

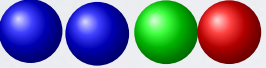


$$x_1 = \text{green ball}$$

$$x_2 = \text{red ball}$$

$$x_1 = \text{red ball}$$

$$y_1 = 3$$



	A	B
1		
2		
3		
4		

P1

P2

P4

$$x_1 = \text{green}$$

$$x_2 = \text{blue}$$

$$x_1 = \text{green}$$

$$y_1 = 1$$

$$x_2 = \text{red}$$

$$x_1 = \text{blue}$$

$$x_2 = \text{blue}$$

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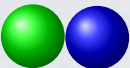
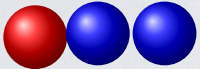
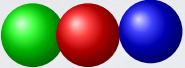

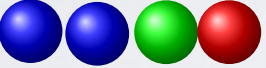


$$x_1 = \text{green}$$

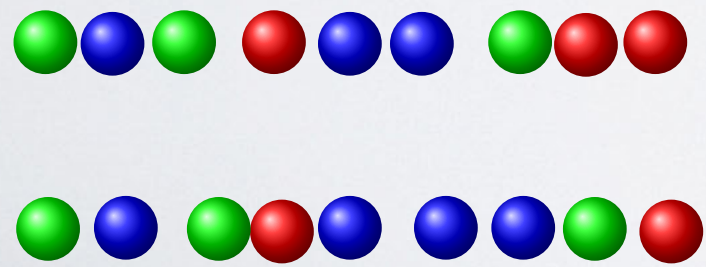
$$x_2 = \text{red}$$

$$x_1 = \text{red}$$

$$y_1 = 3$$



	A	B
1		
2		
3		
4		



$x_1 = \text{Green}$

$x_2 = \text{Blue}$

$x_1 = \text{Green}$

$y_1 = 1$

$x_2 = \text{Red}$

$x_1 = \text{Blue}$

$x_2 = \text{Blue}$

$y_2 = 2$

$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Red}$

$y_1 = 3$



$x_3 = \text{Green}$

$x_4 = \text{Blue}$

$y_3 = 1$

$x_3 = \text{Green}$

$x_4 = \text{Red}$

$x_3 = \text{Blue}$

$y_4 = 2$

$x_4 = \text{Blue}$

$x_3 = \text{Blue}$

$x_4 = \text{Green}$

$x_3 = \text{Red}$

$y_3 = 3$



P4

	A	B
1		
2		
3		
4		



$x_1 = \text{Green}$

$x_2 = \text{Blue}$

$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Blue}$

$x_2 = \text{Blue}$

$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Red}$



$x_3 = \text{Green}$

$x_4 = \text{Blue}$

$x_3 = \text{Green}$

$x_4 = \text{Red}$

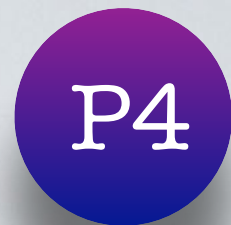
$x_3 = \text{Blue}$

$x_4 = \text{Blue}$

$x_3 = \text{Blue}$

$x_4 = \text{Green}$

$x_3 = \text{Red}$



	A	B
1		
2		
3		
4		

P1

$x_1 = \text{green} \checkmark$

$x_2 = \text{blue}$

$x_1 = \text{green}$

$x_2 = \text{red}$

$x_1 = \text{blue}$

$x_2 = \text{blue}$

$x_1 = \text{green}$

$x_2 = \text{red}$

$x_1 = \text{red}$

P2

$x_3 = \text{green} \checkmark$

$x_4 = \text{blue}$

$x_3 = \text{green}$

$x_4 = \text{red}$

$x_3 = \text{blue}$

$x_4 = \text{blue}$

$x_3 = \text{blue}$

$x_4 = \text{green}$

$x_3 = \text{red}$

P4



	A	B
1		
2		
3		
4		

P1

$x_1 = \text{Green} \checkmark$

$x_2 = \text{Blue} \checkmark$

$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Blue}$

$x_2 = \text{Blue}$

$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Red}$

P2

$x_3 = \text{Green} \checkmark$

$x_4 = \text{Blue} \checkmark$

$x_3 = \text{Green}$

$x_4 = \text{Red}$

$x_3 = \text{Blue}$

$x_4 = \text{Blue}$

$x_3 = \text{Blue}$

$x_4 = \text{Green}$


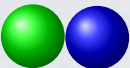
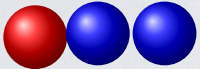
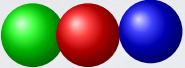

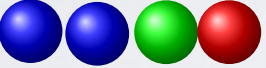


$x_3 = \text{Red}$

P4

$\text{Green} \quad \text{Green}$

$\text{Blue} \quad \text{Blue}$



	A	B
1		
2		
3		
4		

P1

$x_1 = \text{Green} \checkmark$

$x_2 = \text{Blue} \checkmark$

$x_1 = \text{Green} \checkmark$

$x_2 = \text{Red}$

$x_1 = \text{Blue}$

$x_2 = \text{Blue}$

$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Red}$

P2

$x_3 = \text{Green} \checkmark$

$x_4 = \text{Blue} \checkmark$

$x_3 = \text{Green} \checkmark$

$x_4 = \text{Red}$

$x_3 = \text{Blue}$

$x_4 = \text{Blue}$

$x_3 = \text{Blue}$

$x_4 = \text{Green}$

$x_3 = \text{Red}$

P4

$\text{Green} \quad \text{Green}$

$\text{Blue} \quad \text{Blue}$

$\text{Green} \quad \text{Green}$



	A	B
1		
2		
3		
4		

P1

$x_1 = \text{green} \checkmark$

$x_2 = \text{blue} \checkmark$

$x_1 = \text{green} \checkmark$

$x_2 = \text{red} \checkmark$

$x_1 = \text{blue}$

$x_2 = \text{blue}$

$x_1 = \text{green}$

$x_2 = \text{red}$

$x_1 = \text{red}$

P2

$x_3 = \text{green} \checkmark$

$x_4 = \text{blue} \checkmark$

$x_3 = \text{green} \checkmark$

$x_4 = \text{red} \checkmark$

$x_3 = \text{blue}$

$x_4 = \text{blue}$

$x_3 = \text{blue}$

$x_4 = \text{green}$

$x_3 = \text{red}$

P4

$\text{green} \quad \text{green}$

$\text{blue} \quad \text{blue}$

$\text{green} \quad \text{green}$

$\text{red} \quad \text{red}$

$\text{green} \text{ blue} \text{ green} \text{ red} \text{ blue} \text{ blue} \text{ green} \text{ red} \text{ red}$

$\text{green} \text{ blue} \text{ green} \text{ red} \text{ blue} \text{ blue} \text{ blue} \text{ green} \text{ red}$

	A	B
1		
2		
3		
4		

P1

$x_1 = \text{Green} \checkmark$
 $x_2 = \text{Blue} \checkmark$
 $x_1 = \text{Green} \checkmark$

$x_2 = \text{Red} \checkmark$
 $x_1 = \text{Blue} \checkmark$
 $x_2 = \text{Blue}$

$x_1 = \text{Green}$
 $x_2 = \text{Red}$
 $x_1 = \text{Red}$

P2

$x_3 = \text{Green} \checkmark$
 $x_4 = \text{Blue} \checkmark$

$x_3 = \text{Green} \checkmark$
 $x_4 = \text{Red} \checkmark$
 $x_3 = \text{Blue} \checkmark$

$x_4 = \text{Blue}$
 $x_3 = \text{Blue}$
 $x_4 = \text{Green}$
 $x_3 = \text{Red}$

P4

	A	B
1		
2		
3		
4		

P1

$x_1 = \text{Green} \checkmark$

$x_2 = \text{Blue} \checkmark$

$x_1 = \text{Green} \checkmark$

$x_2 = \text{Red} \checkmark$

$x_1 = \text{Blue} \checkmark$

$x_2 = \text{Blue}$

$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Red} \checkmark$

P2

$x_3 = \text{Green} \checkmark$

$x_4 = \text{Blue} \checkmark$

$x_3 = \text{Green} \checkmark$

$x_4 = \text{Red} \checkmark$

$x_3 = \text{Blue} \checkmark$

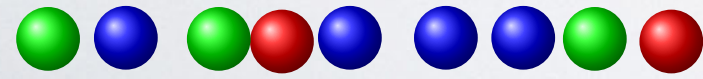
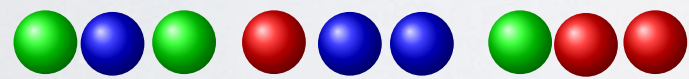
$x_4 = \text{Blue}$

$x_3 = \text{Blue}$

$x_4 = \text{Green}$

$x_3 = \text{Red} \checkmark$

P4



	A	B
1		
2		
3		
4		

P1

$x_1 = \text{Green} \checkmark$

$x_2 = \text{Blue} \checkmark$

$x_1 = \text{Green} \checkmark$

P2

$x_3 = \text{Green} \checkmark$

$x_4 = \text{Blue} \checkmark$

P4



$x_1 = \text{Green} \checkmark$

$x_2 = \text{Blue} \checkmark$

$x_1 = \text{Red} \checkmark$

$x_2 = \text{Blue} \checkmark$

P4 jumps and cheats!



$x_1 = \text{Green}$

$x_2 = \text{Red}$

$x_1 = \text{Red} \checkmark$

$x_4 = \text{Blue}$

$x_3 = \text{Blue}$

$x_4 = \text{Green}$

$x_3 = \text{Red} \checkmark$



$$x_1 = \text{green dot}$$

$$x_2 = \text{red dot}$$

$$x_1 = \text{blue dot}$$

$$x_2 = \text{blue dot}$$

$$x_1 = \text{green dot}$$

$$x_2 = \text{red dot}$$

$$x_1 = \text{red dot}$$

$$x_3 = \text{green dot}$$

$$x_4 = \text{red dot}$$

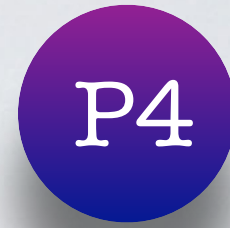
$$x_3 = \text{red dot}$$

$$x_4 = \text{blue dot}$$

$$x_3 = \text{green dot}$$

$$x_4 = \text{green dot}$$

$$x_3 = \text{red dot}$$



$t_1 \quad x_1 = \text{green}$

$x_3 = \text{green}$

$t_2 \quad x_2 = \text{red}$

$x_4 = \text{red}$

$t_3 \quad x_1 = \text{blue}$

$x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$x_3 = \text{red}$



$$t_1 \quad x_1 = \text{green}$$

$$t'_1 \quad x_3 = \text{green}$$

$$t_2 \quad x_2 = \text{red}$$

$$t'_2 \quad x_4 = \text{red}$$

$$t_3 \quad x_1 = \text{blue}$$

$$t'_3 \quad x_3 = \text{red}$$

$$t_4 \quad x_2 = \text{blue}$$

$$t'_4 \quad x_4 = \text{blue}$$

$$t_5 \quad x_1 = \text{green}$$

$$t'_5 \quad x_3 = \text{green}$$

$$t_6 \quad x_2 = \text{red}$$

$$t'_6 \quad x_4 = \text{green}$$

$$t_7 \quad x_1 = \text{red}$$

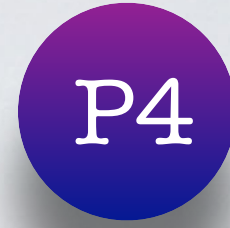
$$t'_7 \quad x_3 = \text{red}$$



P1



P2



P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green}$

$t'_1 \quad x_3 = \text{green}$

$t_2 \quad x_2 = \text{red}$

$t'_2 \quad x_4 = \text{red}$

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$t'_7 \quad x_3 = \text{red}$



P1



P2



P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green}$

$t_2 \quad x_2 = \text{red}$

$t'_2 \quad x_4 = \text{red}$

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

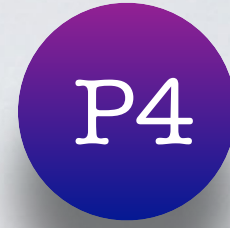
$t'_7 \quad x_3 = \text{red}$



P1



P2



P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$

$t_2 \quad x_2 = \text{red}$

$t'_2 \quad x_4 = \text{red}$

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	t'_1	0
-------	---	--------	---

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



$t_2 \quad x_2 = \text{red}$

$t'_2 \quad x_4 = \text{red}$

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	t'_1	0
-------	---	--------	---

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red}$

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	t'_1	0
-------	---	--------	---

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{blue}$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

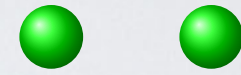
$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

t_1 $x_1 = \text{green} \checkmark$

t'_1 $x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

t_2 $x_2 = \text{red} \checkmark$

t'_2 $x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

t_3 $x_1 = \text{blue}$

t'_3 $x_3 = \text{red}$

t_4 $x_2 = \text{blue} \checkmark$

t'_4 $x_4 = \text{blue}$

t_5 $x_1 = \text{green}$

t'_5 $x_3 = \text{green}$

t_6 $x_2 = \text{red}$

t'_6 $x_4 = \text{green}$

t_7 $x_1 = \text{red}$

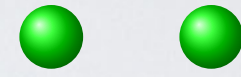
t'_7 $x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue} \checkmark$

$t'_4 \quad x_4 = \text{blue} \checkmark$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue} \checkmark$

$t'_4 \quad x_4 = \text{blue} \checkmark$



	t_4		t'_4
--	-------	--	--------

$t_5 \quad x_1 = \text{green}$

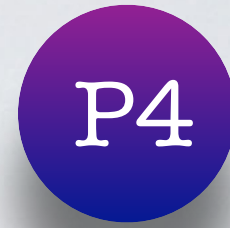
$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue} \checkmark$

$t'_4 \quad x_4 = \text{blue} \checkmark$



t_3	t_4	t'_3	t'_4
-------	-------	--------	--------

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red}$

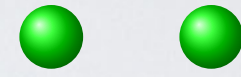
$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

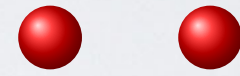
$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue} \checkmark$

$t'_4 \quad x_4 = \text{blue} \checkmark$



t_3	t_4	t'_3	t'_4
-------	-------	--------	--------

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red} \checkmark$

$t'_7 \quad x_3 = \text{red}$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$



t_3	t_4	t'_3	t'_4
-------	-------	--------	--------

$t_4 \quad x_2 = \text{blue} \checkmark$

$t'_4 \quad x_4 = \text{blue} \checkmark$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

$t_7 \quad x_1 = \text{red} \checkmark$

$t'_7 \quad x_3 = \text{red} \checkmark$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

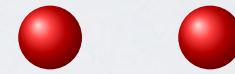
$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$



t_3	t_4	t'_3	t'_4
-------	-------	--------	--------

$t_4 \quad x_2 = \text{blue} \checkmark$

$t'_4 \quad x_4 = \text{blue} \checkmark$

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

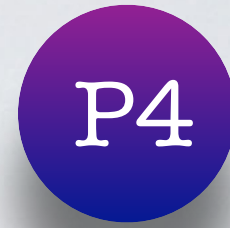
$t'_6 \quad x_4 = \text{green}$



t_7		t'_7	
-------	--	--------	--

$t_7 \quad x_1 = \text{red} \checkmark$

$t'_7 \quad x_3 = \text{red} \checkmark$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$



t_1	0	t'_1	0
-------	---	--------	---

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red} \checkmark$



t_1	t_2	t'_1	t'_2
-------	-------	--------	--------

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue} \checkmark$

$t'_4 \quad x_4 = \text{blue} \checkmark$



t_3	t_4	t'_3	t'_4
-------	-------	--------	--------

$t_5 \quad x_1 = \text{green}$

$t'_5 \quad x_3 = \text{green}$

$t_6 \quad x_2 = \text{red}$

$t'_6 \quad x_4 = \text{green}$

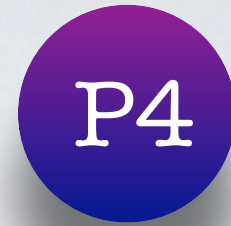


t_7	t_6	t'_7	t'_6
-------	-------	--------	--------

$t_7 \quad x_1 = \text{red} \checkmark$

$t'_7 \quad x_3 = \text{red} \checkmark$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



$$t_1 \quad x_1 = \bullet$$

$$t'_1 \quad x_3 = \bullet$$

$$t_2 \quad x_2 = \bullet$$

$$t'_2 \quad x_4 = \bullet$$

$$t_3 \quad x_1 = \bullet$$

$$t'_3 \quad x_3 = \bullet$$

$$t_4 \quad x_2 = \bullet$$

$$t'_4 \quad x_4 = \bullet$$

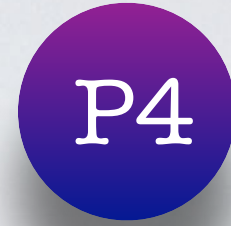
$$t_5 \quad x_1 = \bullet$$

$$t'_5 \quad x_3 = \bullet$$

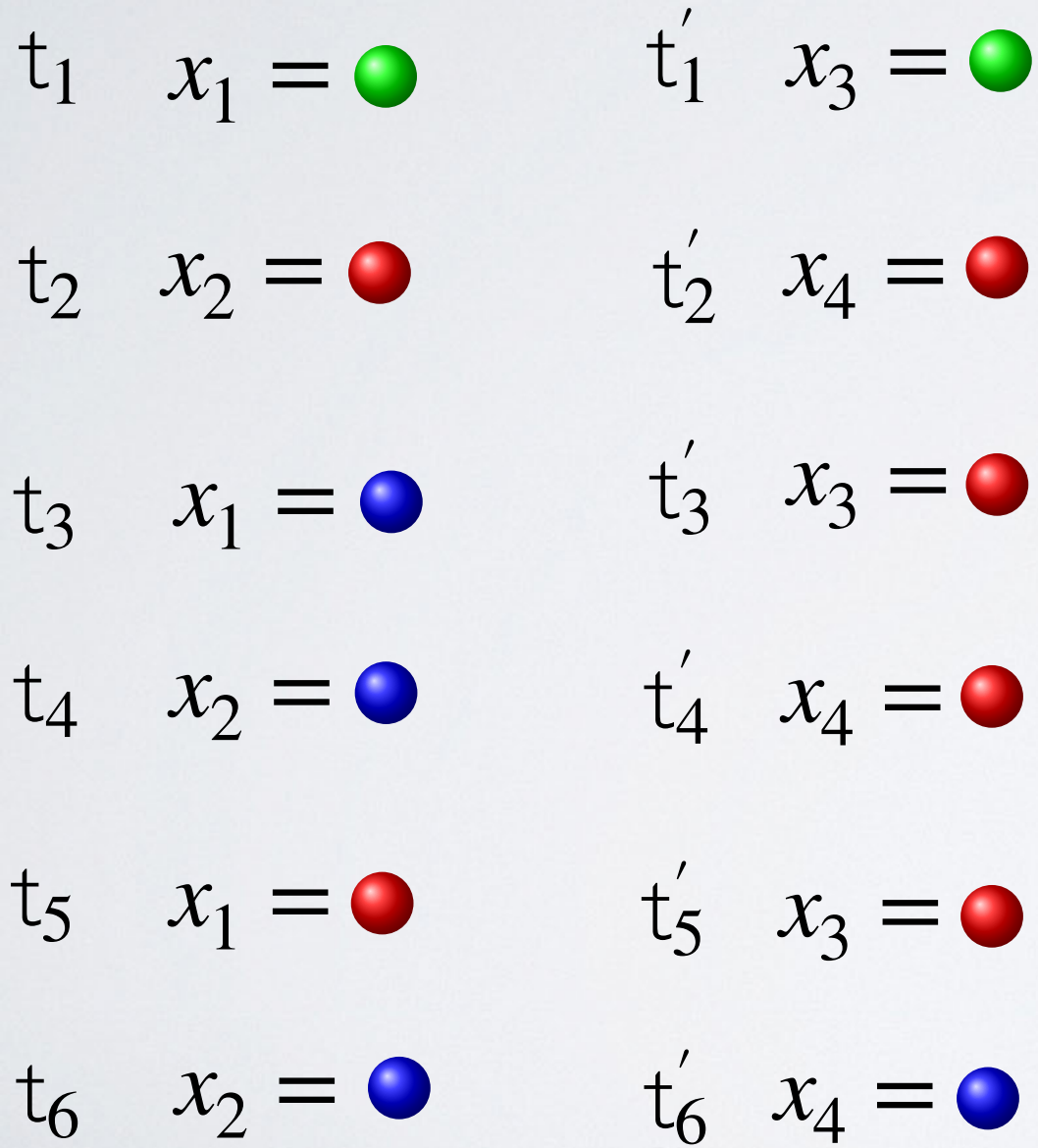
$$t_6 \quad x_2 = \bullet$$

$$t'_6 \quad x_4 = \bullet$$

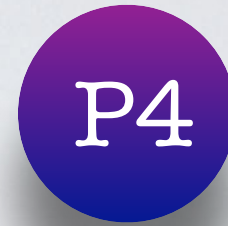
$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0



$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

arw(x_1 , ● ●)

t_1	0	0	0
-------	---	---	---

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet$

$t_2 \quad x_2 = \bullet$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

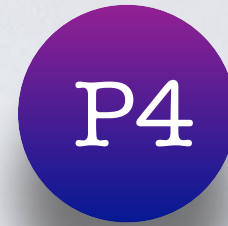
$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

arw(x_1 , ● ●)

t_1	0	0	0
$t_1 + 1$	0	0	0

$$t_1 \quad x_1 = \bullet \checkmark$$

$$t'_1 \quad x_3 = \bullet$$

$$t_2 \quad x_2 = \bullet$$

$$t'_2 \quad x_4 = \bullet$$

$$t_3 \quad x_1 = \bullet$$

$$t'_3 \quad x_3 = \bullet$$

$$t_4 \quad x_2 = \bullet$$

$$t'_4 \quad x_4 = \bullet$$

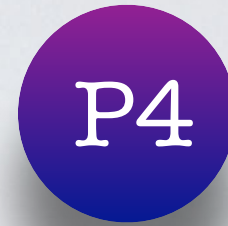
$$t_5 \quad x_1 = \bullet$$

$$t'_5 \quad x_3 = \bullet$$

$$t_6 \quad x_2 = \bullet$$

$$t'_6 \quad x_4 = \bullet$$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



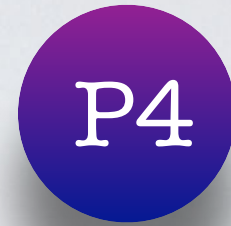
x_1	x_2	x_3	x_4
0	0	0	0

arw(x_1 , ●) ●
 r(x_2 , ●)

t_1	0	0	0
$t_1 + 1$	0	0	0

- | | | | |
|-------|----------------------------|--------|-----------------|
| t_1 | $x_1 = \bullet \checkmark$ | t'_1 | $x_3 = \bullet$ |
| t_2 | $x_2 = \bullet$ | t'_2 | $x_4 = \bullet$ |
| t_3 | $x_1 = \bullet$ | t'_3 | $x_3 = \bullet$ |
| t_4 | $x_2 = \bullet$ | t'_4 | $x_4 = \bullet$ |
| t_5 | $x_1 = \bullet$ | t'_5 | $x_3 = \bullet$ |
| t_6 | $x_2 = \bullet$ | t'_6 | $x_4 = \bullet$ |

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



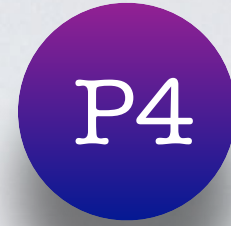
x_1	x_2	x_3	x_4
0	0	0	0

arw(x_1 , ● ●)
r(x_2 , ●)

t_1	0	0	0
$t_1 + 1$	0	0	0

- | | | | |
|-------|----------------------------|--------|-----------------|
| t_1 | $x_1 = \bullet \checkmark$ | t'_1 | $x_3 = \bullet$ |
| t_2 | $x_2 = \bullet$ | t'_2 | $x_4 = \bullet$ |
| t_3 | $x_1 = \bullet$ | t'_3 | $x_3 = \bullet$ |
| t_4 | $x_2 = \bullet$ | t'_4 | $x_4 = \bullet$ |
| t_5 | $x_1 = \bullet$ | t'_5 | $x_3 = \bullet$ |
| t_6 | $x_2 = \bullet$ | t'_6 | $x_4 = \bullet$ |

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



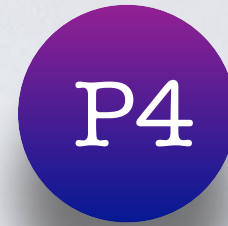
x_1	x_2	x_3	x_4
0	0	0	0

arw(x_1 , ●) ●
 r(x_2 , ●)

t_1	0	0	0
$t_1 + 1$	0	0	0

- | | | | |
|-------|----------------------------|--------|-----------------|
| t_1 | $x_1 = \bullet \checkmark$ | t'_1 | $x_3 = \bullet$ |
| t_2 | $x_2 = \bullet$ | t'_2 | $x_4 = \bullet$ |
| t_3 | $x_1 = \bullet$ | t'_3 | $x_3 = \bullet$ |
| t_4 | $x_2 = \bullet$ | t'_4 | $x_4 = \bullet$ |
| t_5 | $x_1 = \bullet$ | t'_5 | $x_3 = \bullet$ |
| t_6 | $x_2 = \bullet$ | t'_6 | $x_4 = \bullet$ |

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

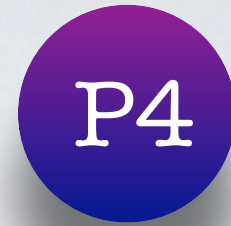
arw(x_1 , \bullet \bullet)

r(x_2 , \bullet)

arw(x_3 , \bullet \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0
$t_1 + 1$	0	t'_1	0

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw(x_1 , \bullet \bullet)

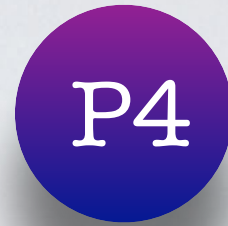
r(x_2 , \bullet)

arw(x_3 , \bullet \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

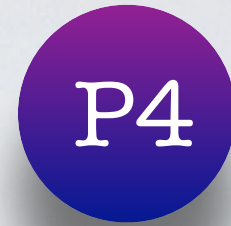
arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

arw($x_3, \bullet \bullet$)

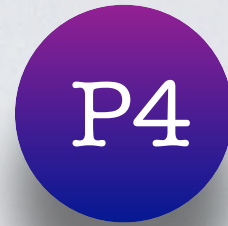
r(x_4, \bullet)

x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw(x_1 , $\bullet \bullet$)

r(x_2 , \bullet)

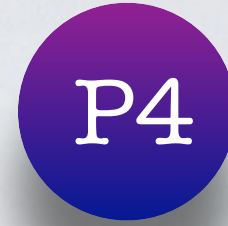
arw(x_3 , $\bullet \bullet$)

r(x_4 , \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



$t_1 \quad x_1 = \text{green} \checkmark$

$t'_1 \quad x_3 = \text{green} \checkmark$

$t_2 \quad x_2 = \text{red} \checkmark$

$t'_2 \quad x_4 = \text{red}$

$t_3 \quad x_1 = \text{blue}$

$t'_3 \quad x_3 = \text{red}$

$t_4 \quad x_2 = \text{blue}$

$t'_4 \quad x_4 = \text{red}$

$t_5 \quad x_1 = \text{red}$

$t'_5 \quad x_3 = \text{red}$

$t_6 \quad x_2 = \text{blue}$

$t'_6 \quad x_4 = \text{blue}$

arw(x_1 , green ●)

r(x_2 , ●)

arw(x_3 , green ●)

r(x_4 , ●)

arw(x_2 , red ●)

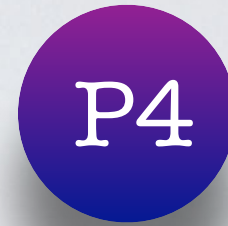
x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_1 + 1$	t_2	$t'_1 + 1$	0
-----------	-------	------------	---

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw(x_1 , $\bullet \bullet$)

r(x_2 , \bullet)

arw(x_3 , $\bullet \bullet$)

r(x_4 , \bullet)

arw(x_2 , $\bullet \bullet$)

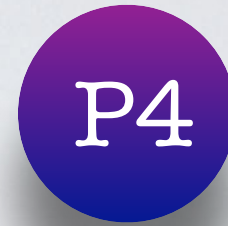
x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet$

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

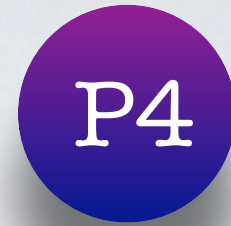
x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet$

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

r(x_2, \bullet)

arw($x_3, \bullet \bullet$)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

r(x_4, \bullet)

arw($x_2, \bullet \bullet$)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

r(x_1, \bullet)

arw($x_4, \bullet \bullet$)

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
-----------	-----------	------------	--------

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

arw($x_4, \bullet \bullet$)

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

arw($x_4, \bullet \bullet$)

r(x_3, \bullet)

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

arw($x_4, \bullet \bullet$)

r(x_3, \bullet)

x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

arw($x_4, \bullet \bullet$)

r(x_3, \bullet)

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

$t_5 \quad x_1 = \bullet$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

r(x_2, \bullet)

arw($x_3, \bullet \bullet$)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

arw($x_2, \bullet \bullet$)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

r(x_1, \bullet)

arw($x_4, \bullet \bullet$)

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

$t_5 \quad x_1 = \bullet \checkmark$

$t'_5 \quad x_3 = \bullet$

r(x_3, \bullet)

arw($x_1, \bullet \bullet$)

t_5	t_4	$t'_1 + 1$	$t'_2 + 1$
-------	-------	------------	------------

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

x_1	x_2	x_3	x_4
0	0	0	0

$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

arw($x_1, \bullet \bullet$)

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

r(x_2, \bullet)

arw($x_3, \bullet \bullet$)

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

r(x_4, \bullet)

arw($x_2, \bullet \bullet$)

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

r(x_1, \bullet)

arw($x_4, \bullet \bullet$)

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

$t_5 \quad x_1 = \bullet \checkmark$

$t'_5 \quad x_3 = \bullet$

r(x_3, \bullet)

arw($x_1, \bullet \bullet$)

t_5	t_4	$t'_1 + 1$	$t'_2 + 1$
$t_5 + 1$	t_4	$t'_1 + 1$	$t'_2 + 1$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$

P1

P2

P4

x_1	x_2	x_3	x_4
0	0	0	0

$$t_1 \quad x_1 = \bullet \checkmark$$

$$t'_1 \quad x_3 = \bullet \checkmark$$

$$\text{arw}(x_1, \bullet \bullet)$$

$$r(x_2, \bullet)$$

t_1	0	0	0
$t_1 + 1$	0	0	0

$$t_2 \quad x_2 = \bullet \checkmark$$

$$t'_2 \quad x_4 = \bullet \checkmark$$

$$\text{arw}(x_3, \bullet \bullet)$$

$$r(x_4, \bullet)$$

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$$t_3 \quad x_1 = \bullet$$

$$t'_3 \quad x_3 = \bullet$$

$$\text{arw}(x_2, \bullet \bullet)$$

$$r(x_1, \bullet)$$

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$$t_4 \quad x_2 = \bullet$$

$$t'_4 \quad x_4 = \bullet$$

$$\text{arw}(x_4, \bullet \bullet)$$

$$r(x_3, \bullet)$$

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

$$t_5 \quad x_1 = \bullet \checkmark$$

$$t'_5 \quad x_3 = \bullet$$

$$\text{arw}(x_1, \bullet \bullet)$$

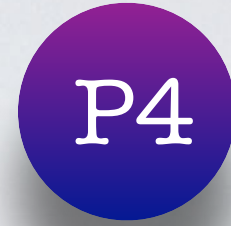
t_5	t_4	$t'_1 + 1$	$t'_2 + 1$
$t_5 + 1$	t_4	$t'_1 + 1$	$t'_2 + 1$

$$t_6 \quad x_2 = \bullet$$

$$t'_6 \quad x_4 = \bullet$$

$$r(x_2, \bullet)$$

$$x_1 = x_2 = x_3 = x_4 = \bullet$$



$t_1 \quad x_1 = \bullet \checkmark$

$t'_1 \quad x_3 = \bullet \checkmark$

$t_2 \quad x_2 = \bullet \checkmark$

$t'_2 \quad x_4 = \bullet \checkmark$

$t_3 \quad x_1 = \bullet$

$t'_3 \quad x_3 = \bullet$

$t_4 \quad x_2 = \bullet$

$t'_4 \quad x_4 = \bullet$

$t_5 \quad x_1 = \bullet \checkmark$

$t'_5 \quad x_3 = \bullet$

$t_6 \quad x_2 = \bullet$

$t'_6 \quad x_4 = \bullet$

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

arw($x_3, \bullet \bullet$)

r(x_4, \bullet)

arw($x_2, \bullet \bullet$)

r(x_1, \bullet)

arw($x_4, \bullet \bullet$)

r(x_3, \bullet)

arw($x_1, \bullet \bullet$)

r(x_2, \bullet)

x_1	x_2	x_3	x_4
0	0	0	0

t_1	0	0	0
$t_1 + 1$	0	0	0

$t_1 + 1$	0	t'_1	0
$t_1 + 1$	0	$t'_1 + 1$	0

$t_1 + 1$	t_2	$t'_1 + 1$	0
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	0

$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	t'_2
$t_1 + 1$	$t_2 + 1$	$t'_1 + 1$	$t'_2 + 1$

t_5	t_4	$t'_1 + 1$	$t'_2 + 1$
$t_5 + 1$	t_4	$t'_1 + 1$	$t'_2 + 1$

$$t_4 > t_2 + 1$$

RA without arw

UNDECIDABLE

NON PRIMITIVE RECURSIVE

**NONELEMENTARY
(Primitive Recursive)**

ELEMENTARY

⋮

3EXPSPACE

2EXPSPACE

2EXPTIME

EXPSPACE

EXPTIME

PSPACE

NP

P

18, 2048, 2^{2^2} , ...
2048, 2^{2^2} , ...
ELEMENTARY (EXPSPACE)
3EXPSPACE
2EXPSPACE
EXPSPACE $2^{p(n)}$

UNDECIDABLE

NON PRIMITIVE RECURSIVE

NONELEMENTARY
(Primitive Recursive)

Reachability of RA without arw
OPEN

P

18, 2048, 2^{2^2} , ...
2048, 2^{2^2} , ...
ELEMENTARY, 2^{2^2} , ...
EXPSPACE

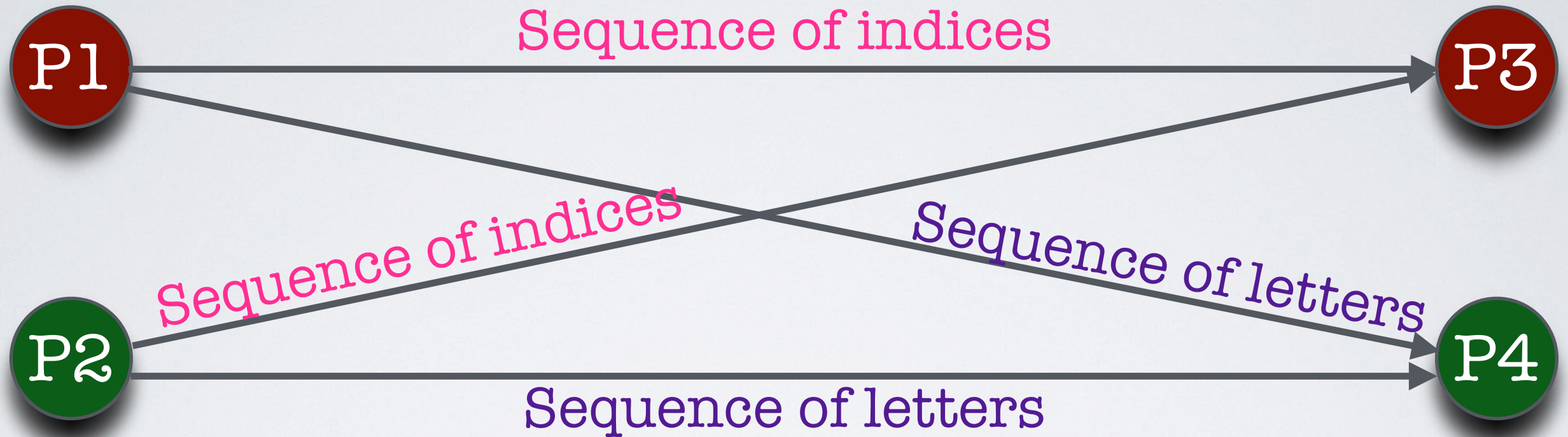
(n)

Context-bounded Analysis (CBA)

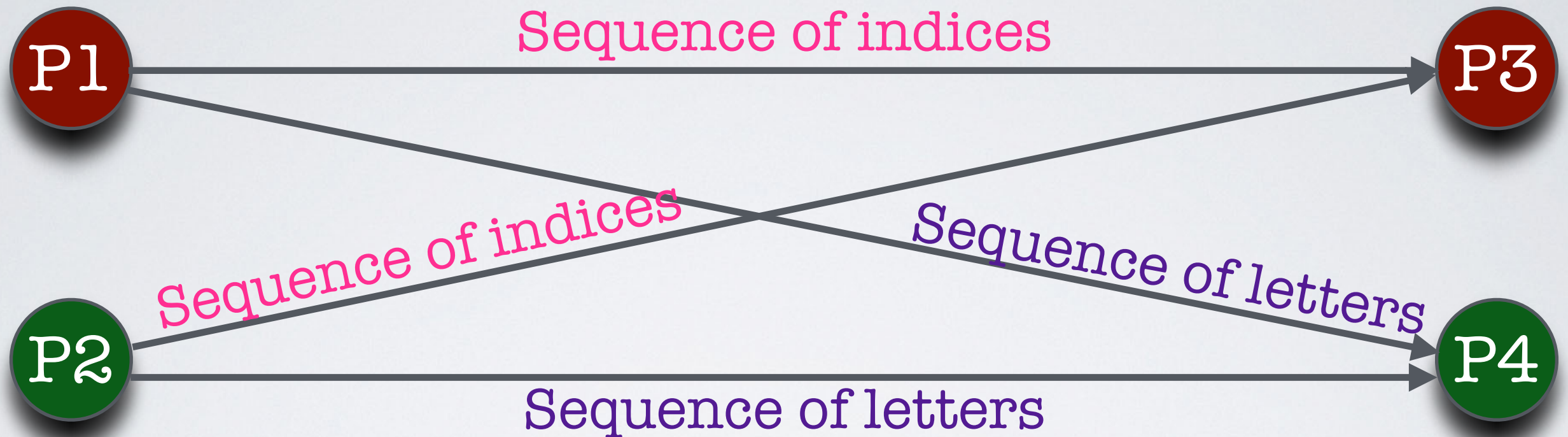
Context-bounded Analysis (CBA)

- ◆ **Efficient under-approximation** technique for SC [Qadeer et al. 2005, Lal et al. 2009, Torre et al. 2009]
- ◆ A context denotes one active process in a run
- ◆ For instance, $(P_1P_2)^*$ has an unbounded context switch, while $P_1P_2^*$ is 1-context bounded
 - Several tools: **CHESS**, **Corral**, **CSeq**, etc

Context-bounded Analysis (CBA)

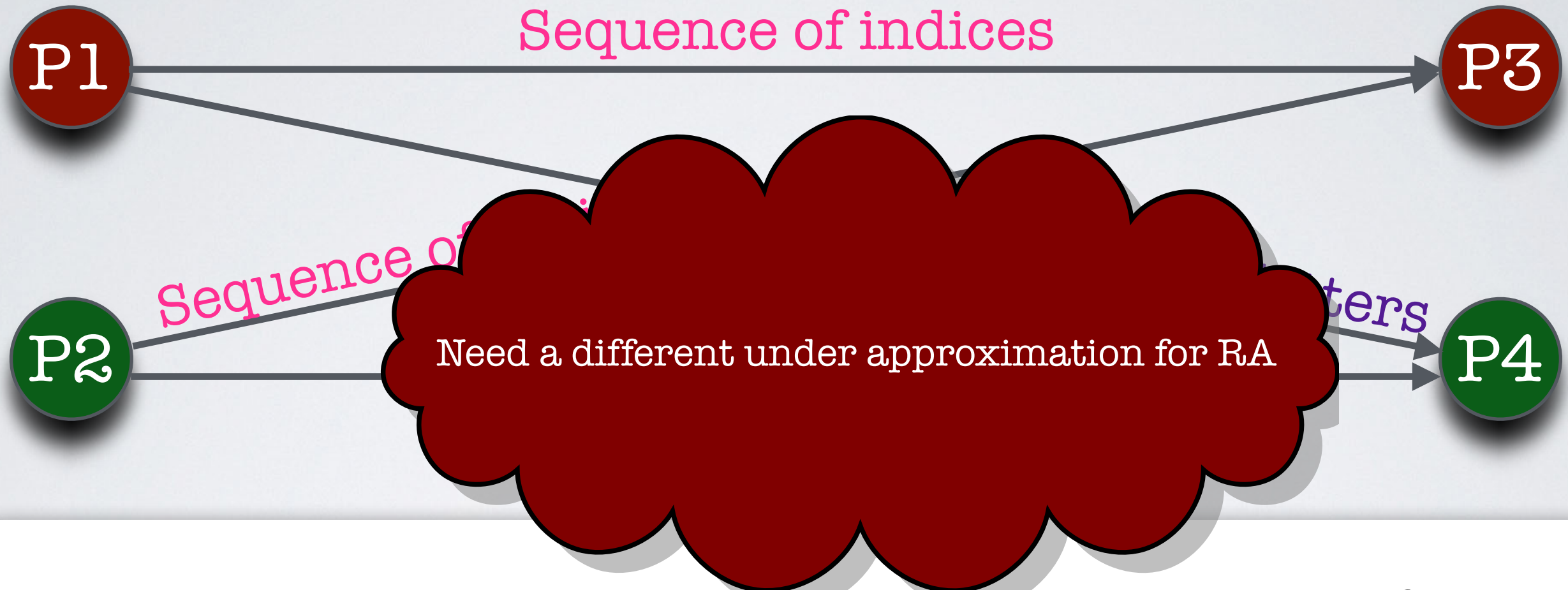


Context-bounded Analysis (CBA)



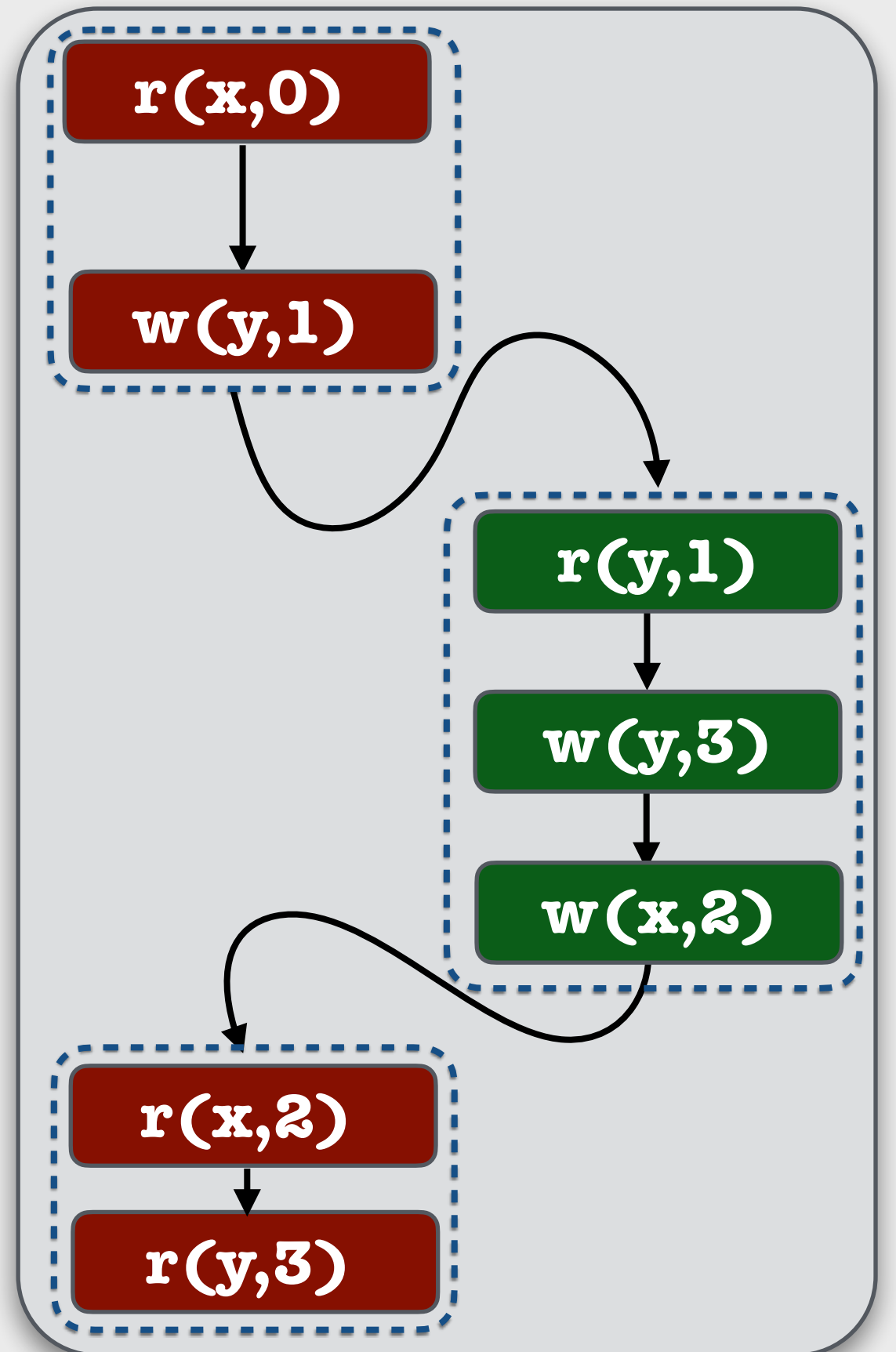
The state reachability problem is still **undecidable** for **RA** with a bounded number (3) of context switches (context: only one “**active**” process)
P1 runs; P2 runs; P3 runs; P4 runs

Context-bounded Analysis (CBA)



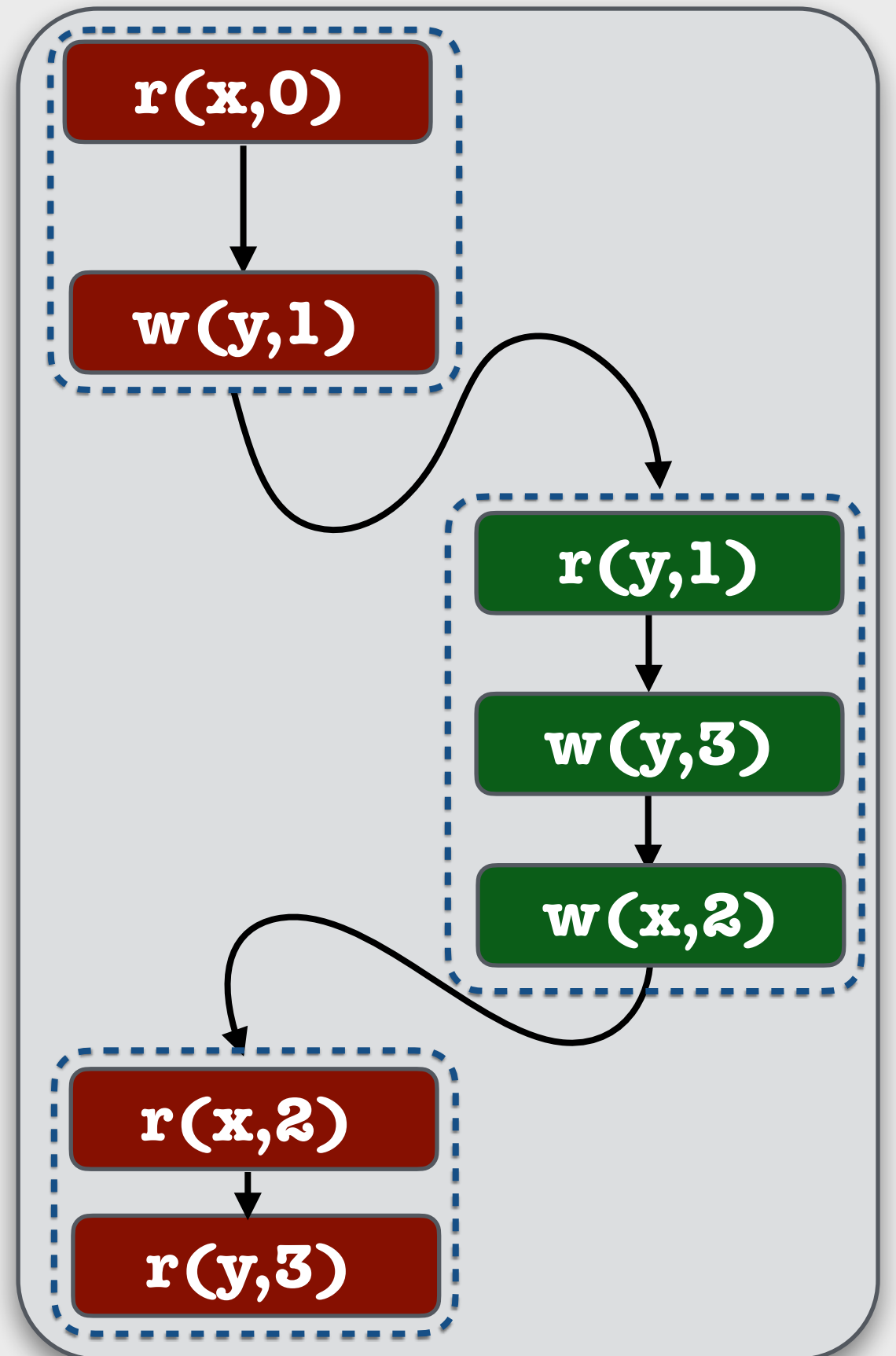
The state reachability problem is still **undecidable** for **RA** with a bounded number (3) of context switches (context: only one “**active**” process)
P1 runs; P2 runs; P3 runs; P4 runs

View Switch



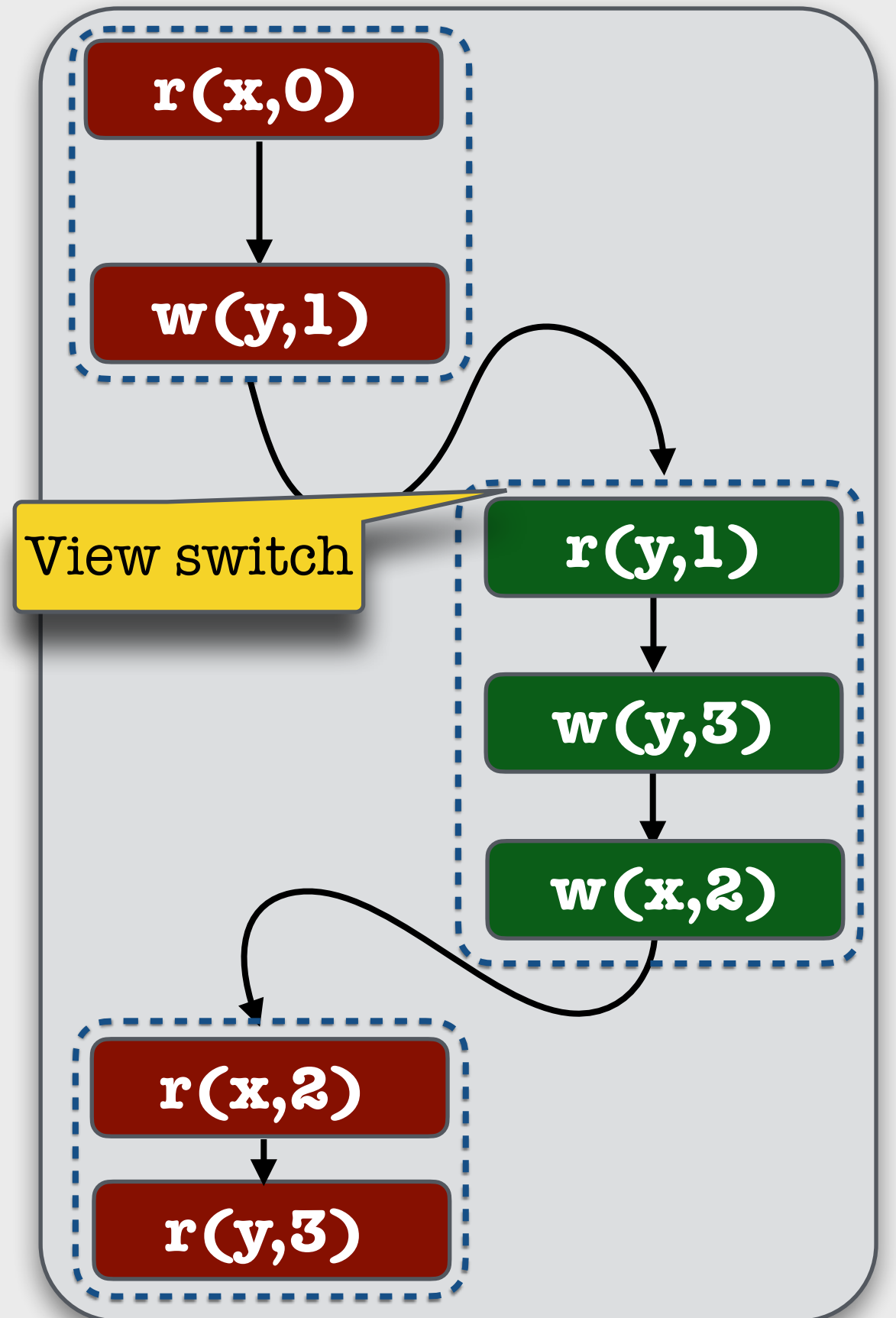
View Switch

A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view



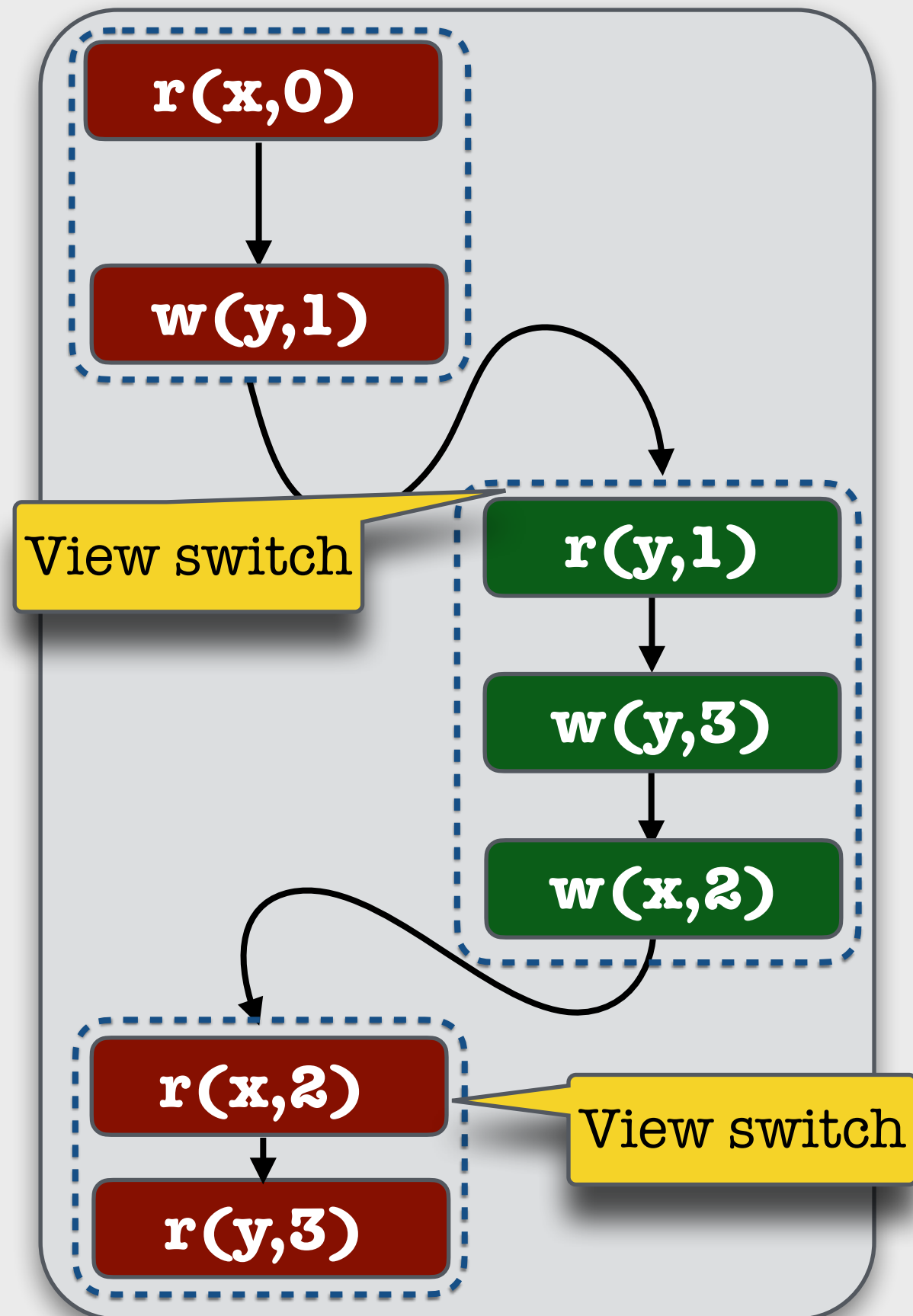
View Switch

A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view



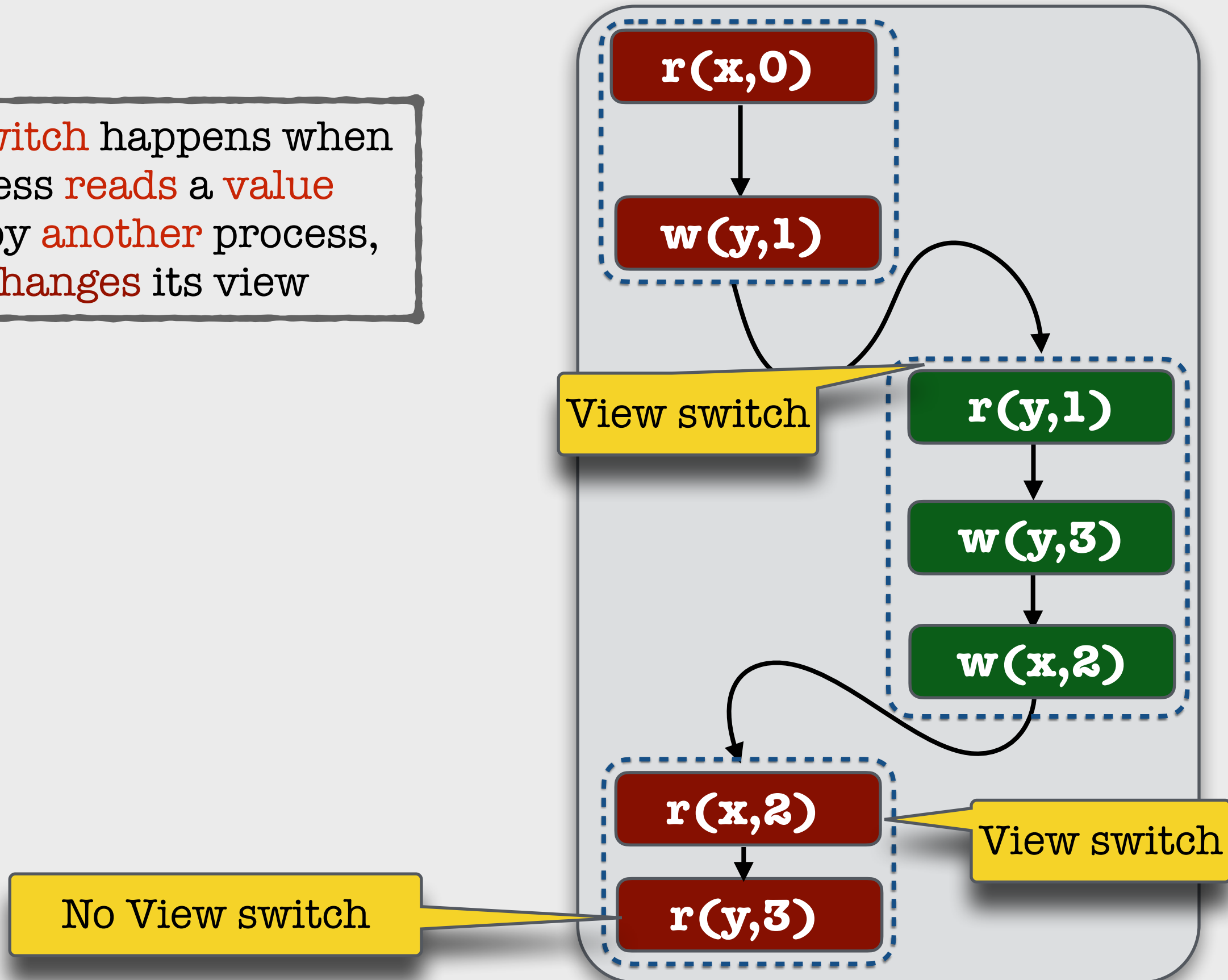
View Switch

A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view



View Switch

A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view

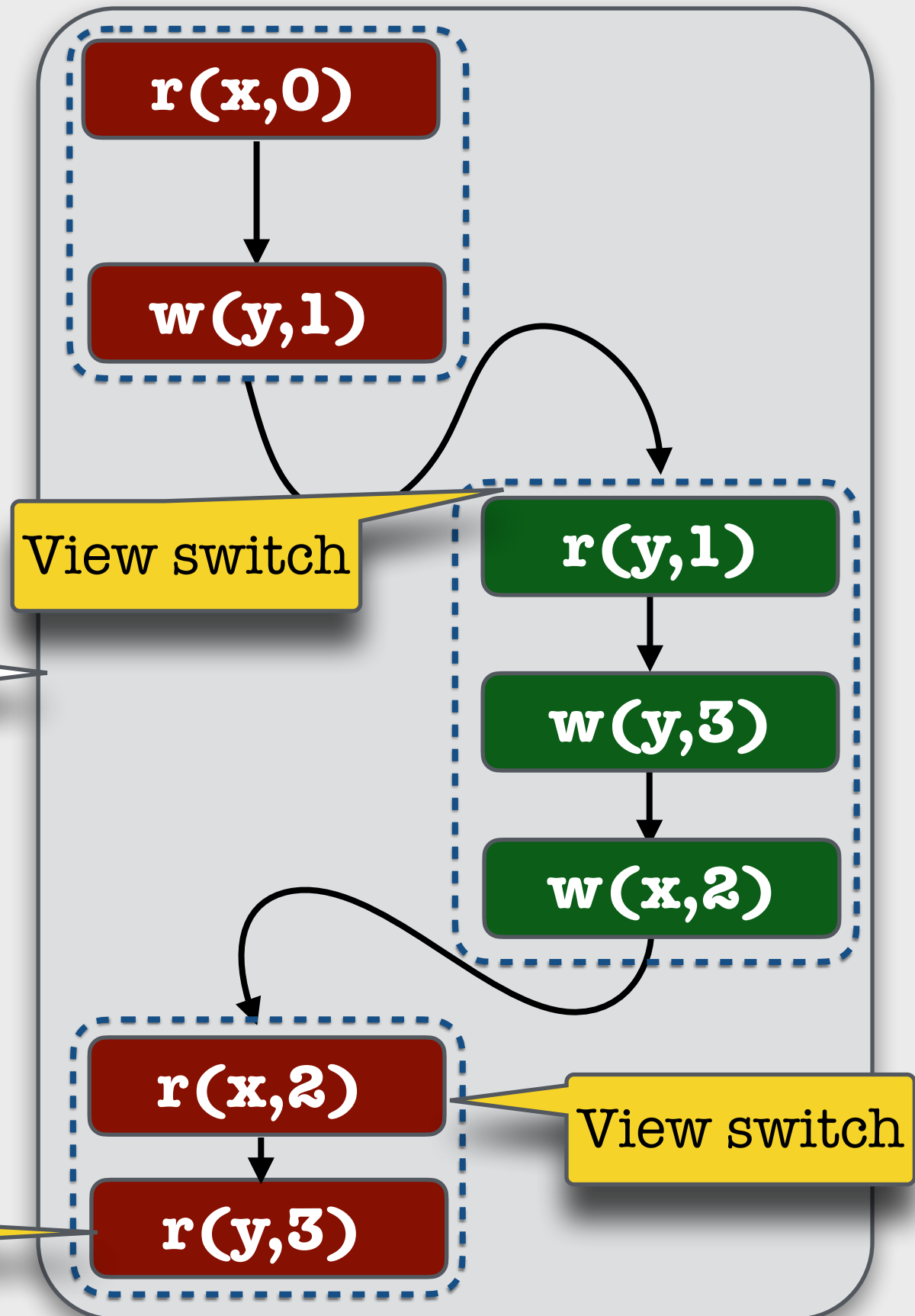


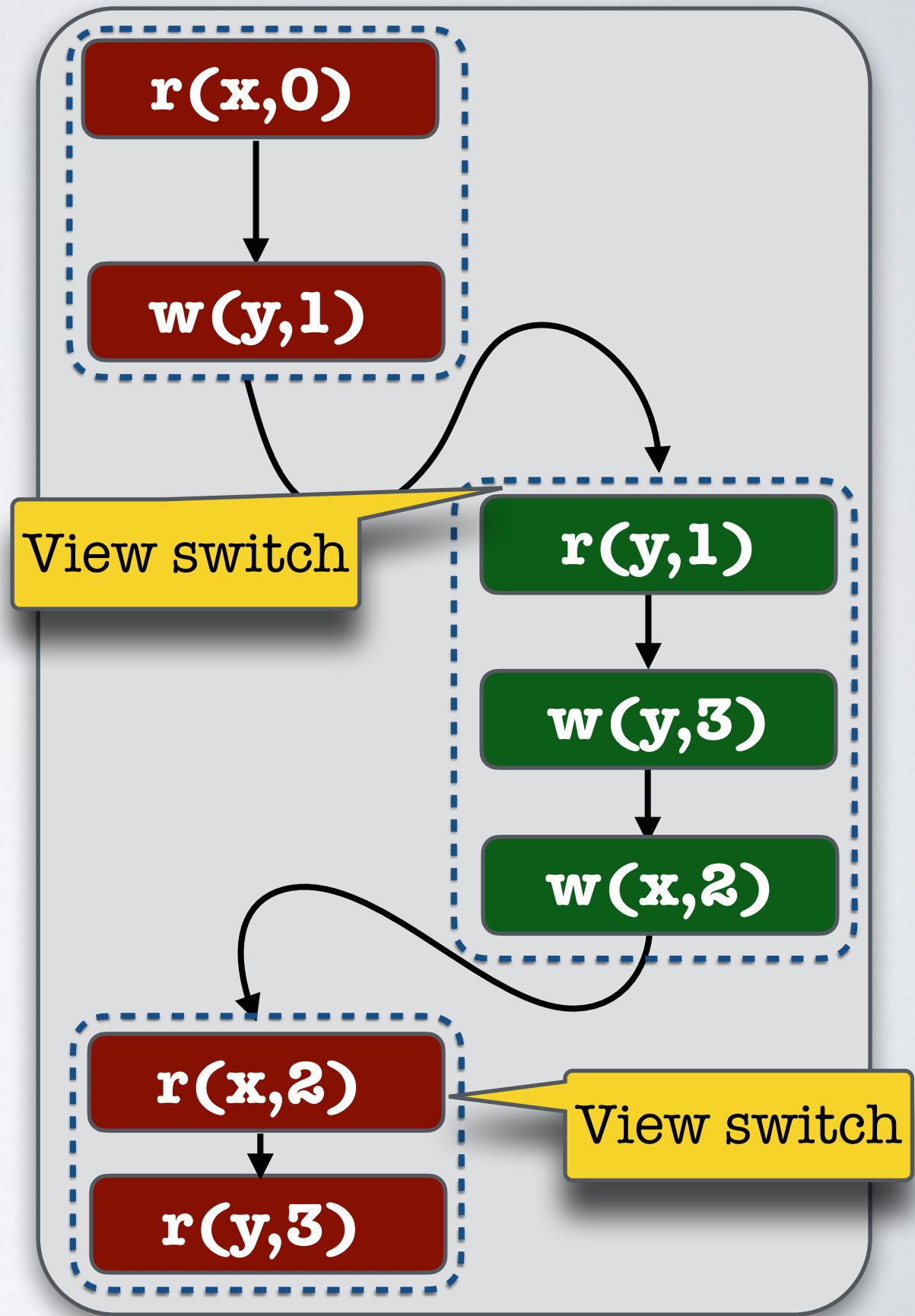
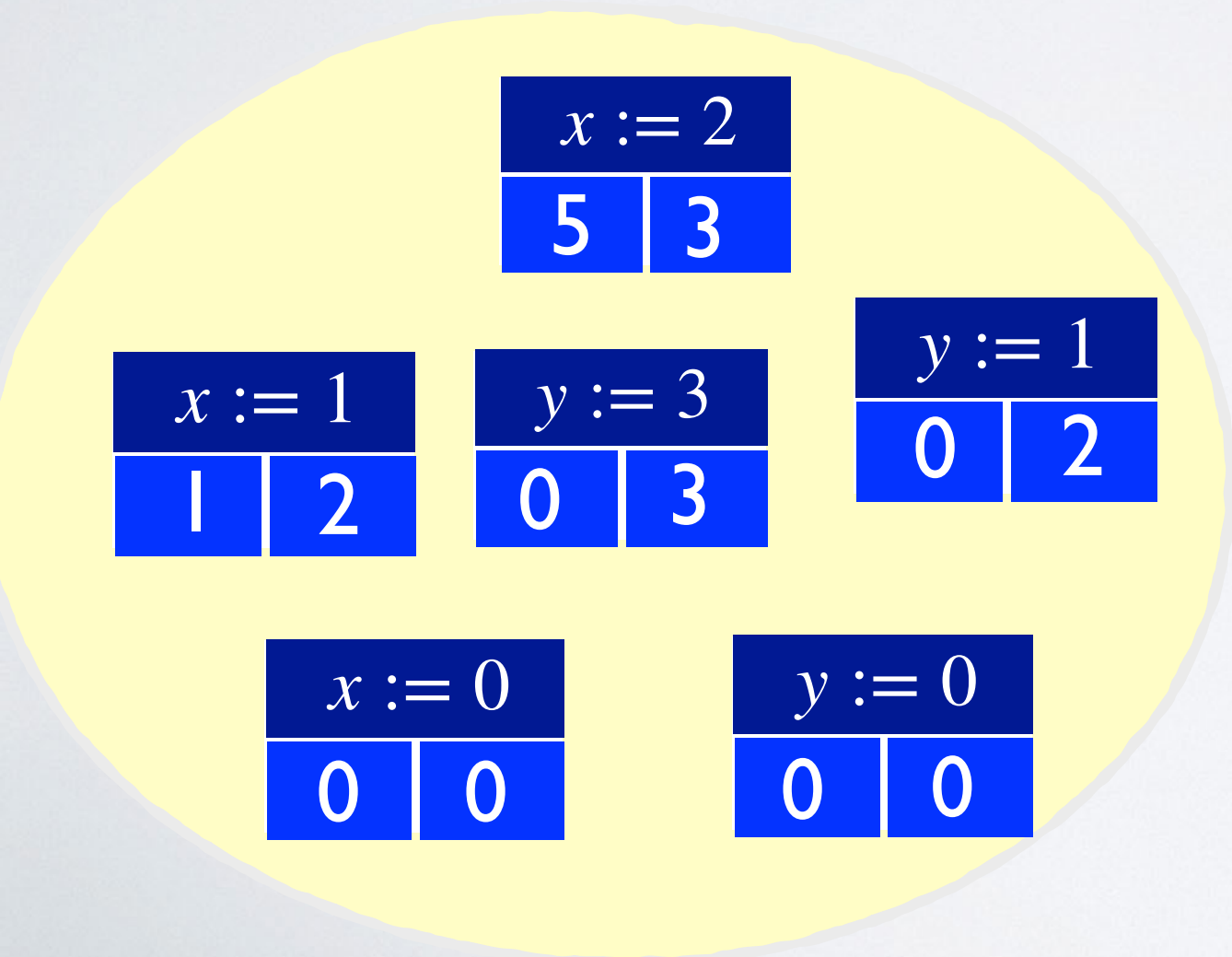
View Switch

A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view

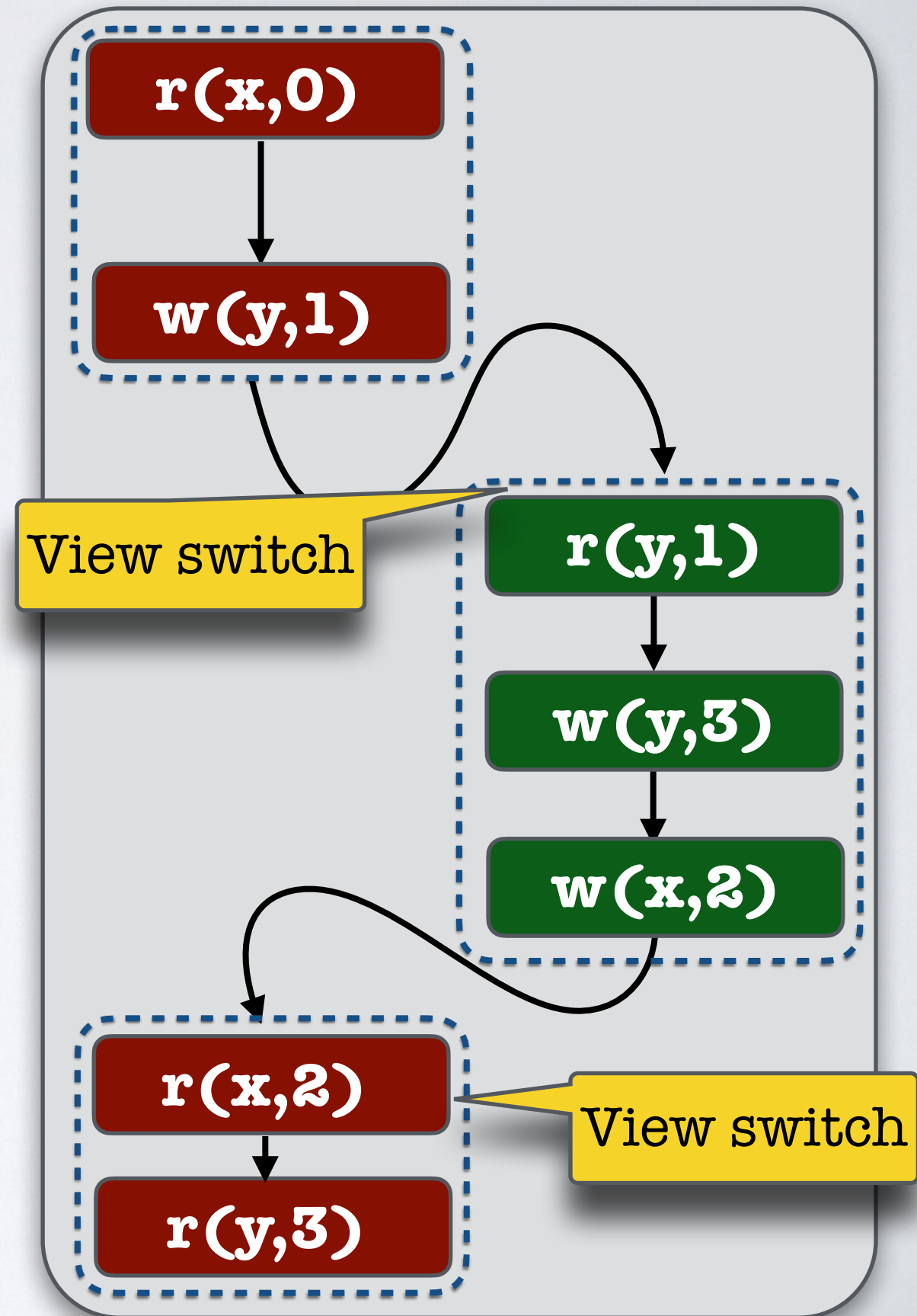
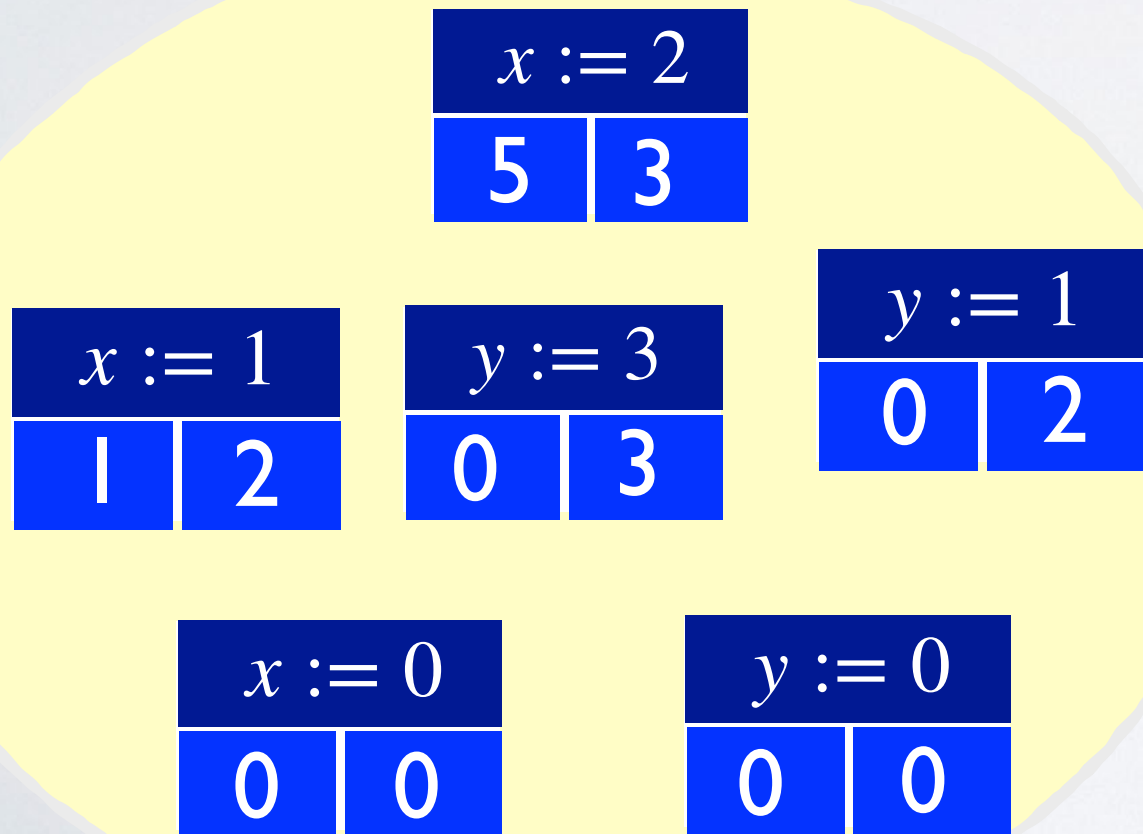
2-bounded run

No View switch



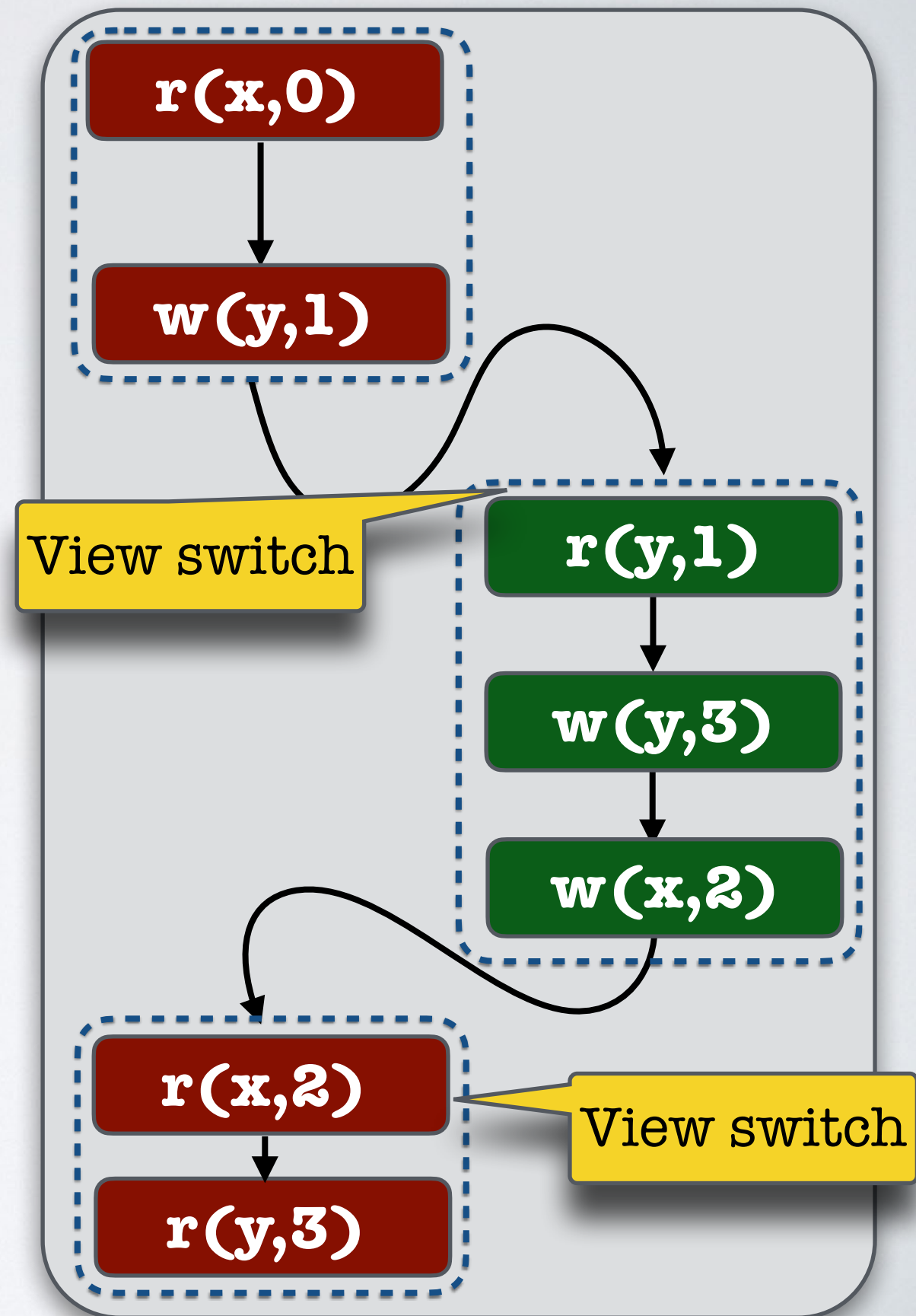
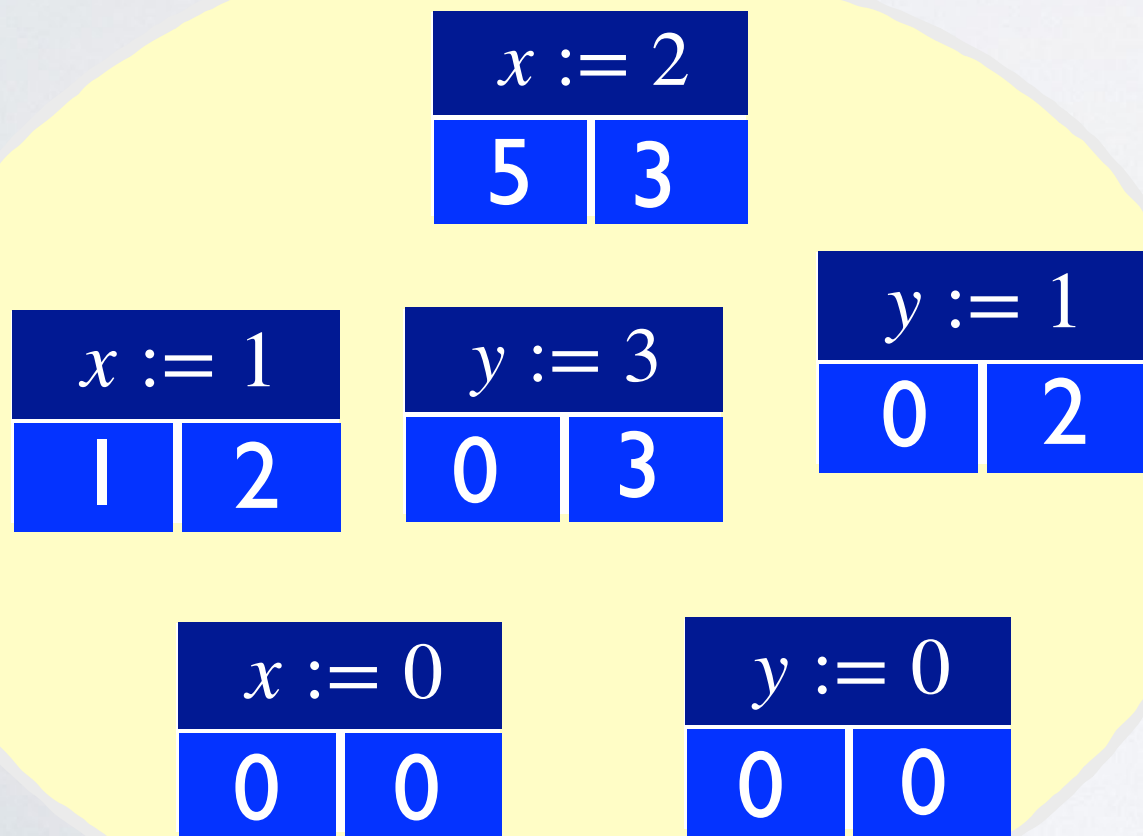


A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view



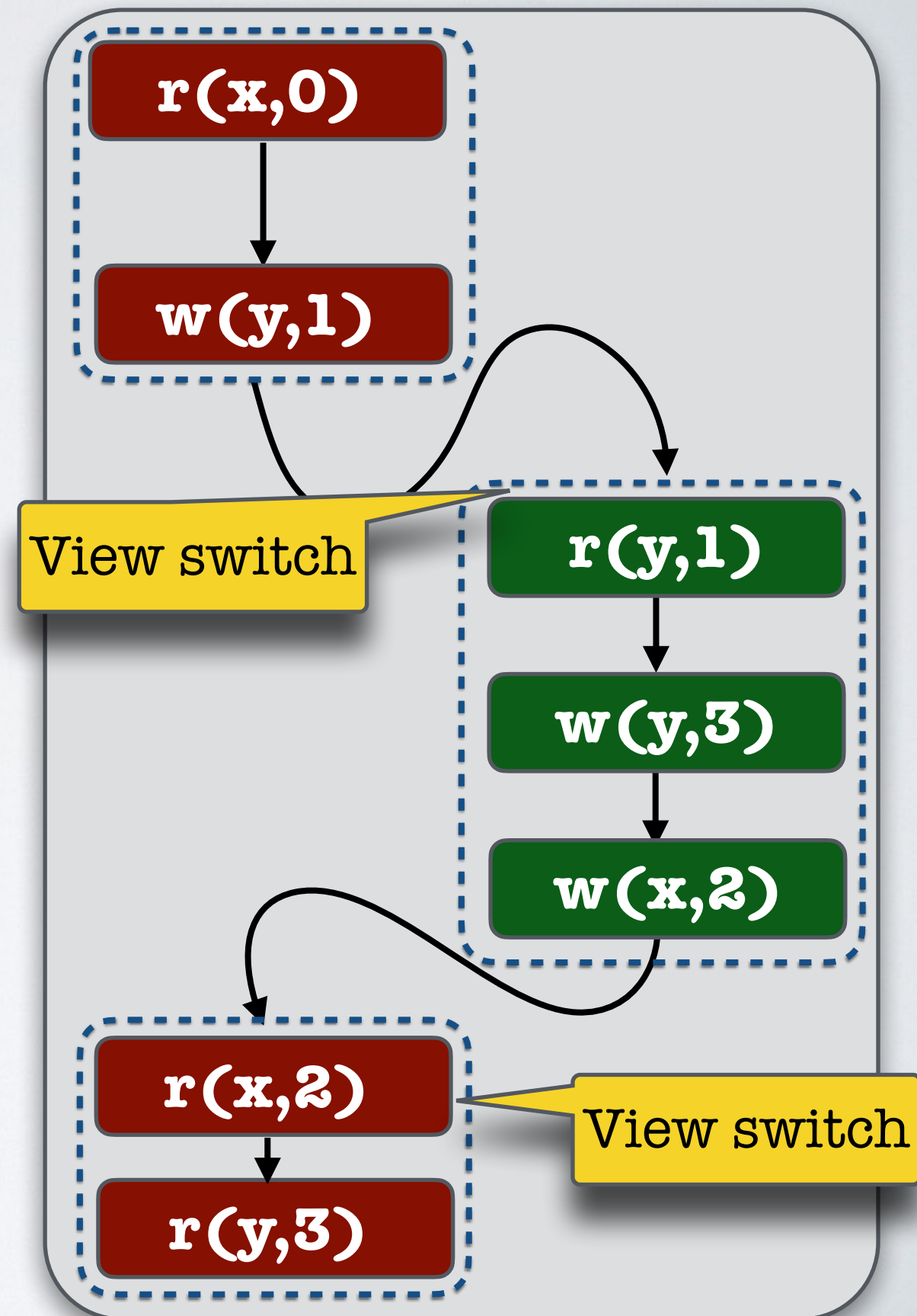
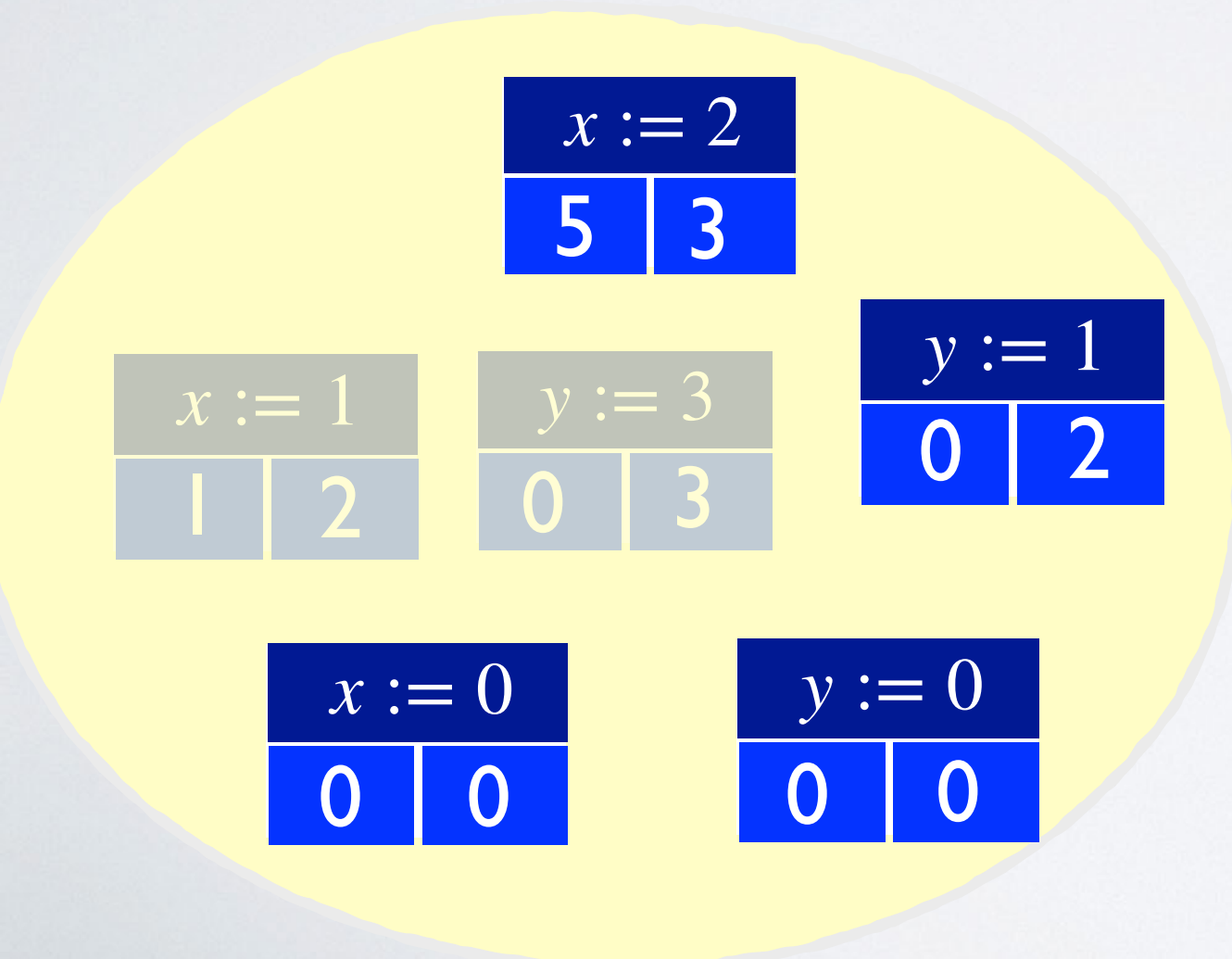
A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view

Bounding the number of **essential** views in the memory



A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view

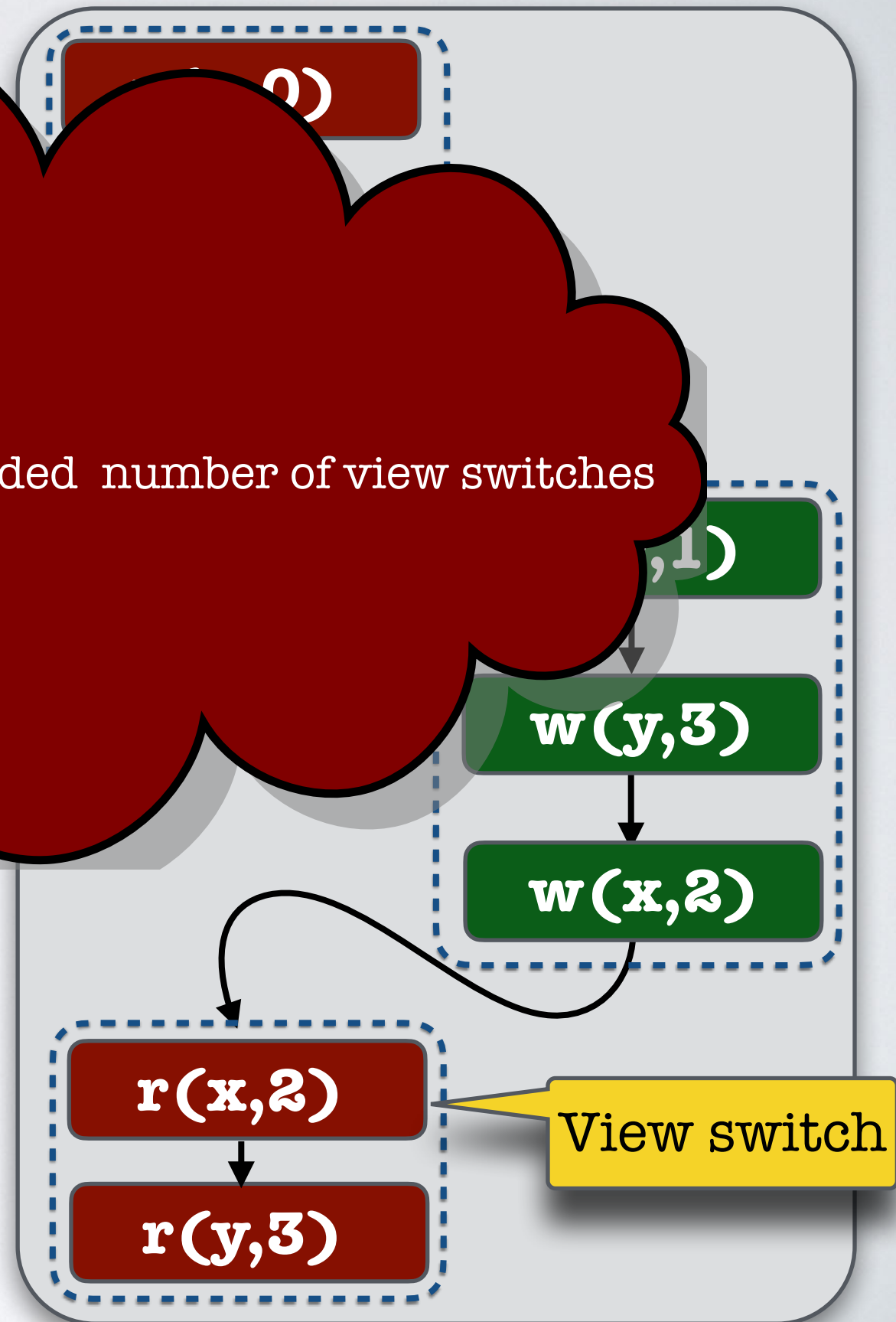
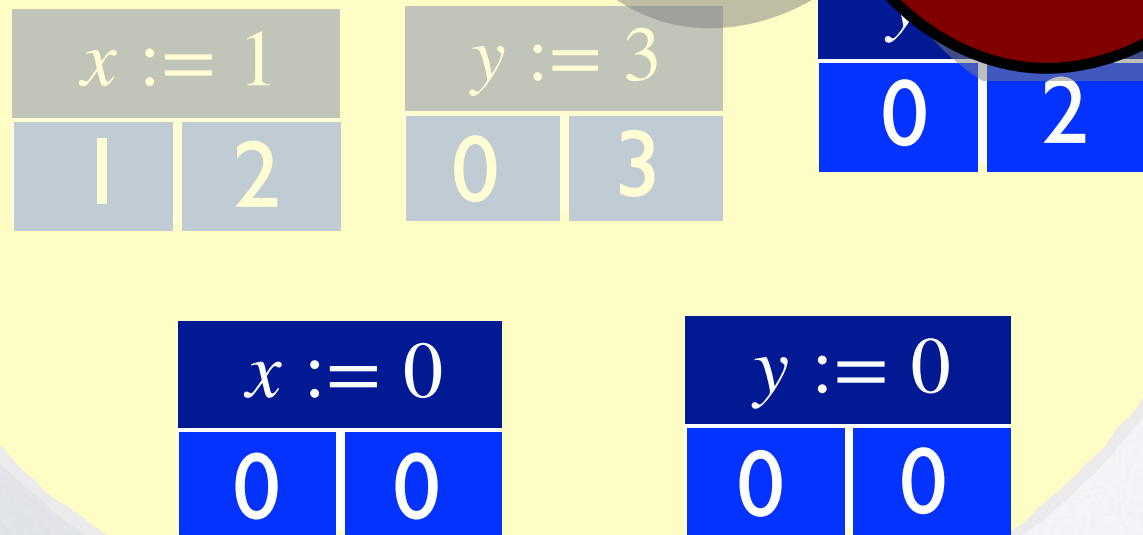
Bounding the number of **essential** views in the memory



A **view-switch** happens when a process **reads** a **value** written by **another** process, and **changes** its view

Boundi
essential

The undecidability proof needed unbounded number of view switches



K-bounded Reachability Problem

K-bounded Reachability Problem

Definition

Reachability problem **restricted** to **K-bounded runs**

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Definition

Reachability problem **restricted** to **K-bounded runs**

**Code-to-code
translation**

Theorem

The K-bounded reachability for RA is **reducible** to
K+n **bounded context reachability** under SC

K-bounded Reachability Problem

Definition

Reachability problem **restricted** to **K-bounded runs**

**Code-to-code
translation**

Theorem

The K-bounded reachability for RA is **reducible** to
K+n **bounded context reachability** under SC

Corollary

The K-bounded reachability for RA is **decidable**
for **finite-state** programs

View bounding under RA to Context bounding under SC

View bounding under RA to Context bounding under SC

- View bound K implies K essential messages

View bounding under RA to Context bounding under SC

- View bound K implies K essential messages
- K time stamps, one per essential message

View bounding under RA to Context bounding under SC

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- Another K time stamps for comparison corresponding to a view change

View bounding under RA to Context bounding under SC

- View bound K implies K essential messages
- K time stamps, one per essential message
- Another K time stamps for comparison corresponding to a view change
- $2K$ time stamps

Per process p , maintain $pView=(pview_x,\dots,pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of **Messages**, each of type $(var, View)$

Per process p , maintain $pView=(pview_x, \dots, pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of **Messages**, each of type $(var, View)$

- On each write $x:=v$ in process p , update $pval(x)$ in $pview_x$

Per process p , maintain $pView=(pview_x, \dots, pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of **Messages**, each of type $(var, View)$

- On each write $x:=v$ in process p , update $pval(x)$ in $pview_x$
- Guess if this will be used in a time stamp comparison. If yes, $pUseful(x)=true$, and update $ptime(x)$

Per process p , maintain $pView=(pview_x,\dots,pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of **Messages**, each of type $(var, View)$

- On each write $x:=v$ in process p , update $pval(x)$ in $pview_x$
- Guess if this will be used in a time stamp comparison. If yes, $pUseful(x)=true$, and update $ptime(x)$
- Guess if this will result in an essential message. If yes, add to **Messages** if $pUseful(x)=true$ for all $pview_x$ in $pView$

Per process p , maintain $pView=(pview_x,\dots,pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of **Messages**, each of type $(var, View)$

- On each write $x:=v$ in process p , update $pval(x)$ in $pview_x$
- Guess if this will be used in a time stamp comparison. If yes, $pUseful(x)=true$, and update $ptime(x)$
- Guess if this will result in an essential message. If yes, add to **Messages** if $pUseful(x)=true$ for all $pview_x$ in $pView$
- Once all essential messages are generated, **Messages** has all necessary data, simply run all processes to completion (**$K+n$ context** switches).

Per process p , maintain $pView=(pview_x,\dots,pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of messages, each of type $(var, View)$

Per process p , maintain $pView=(pview_x, \dots, pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of messages, each of type $(var, View)$

- On each read $r:=x$, guess if this read is view altering. If yes, check if $pUseful(x)$ is true in $pview_x$, and $pUseful(var)$ is true in $pView$ for all var

Per process p , maintain $pView=(pview_x, \dots, pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

Globally, maintain a set of messages, each of type $(var, View)$

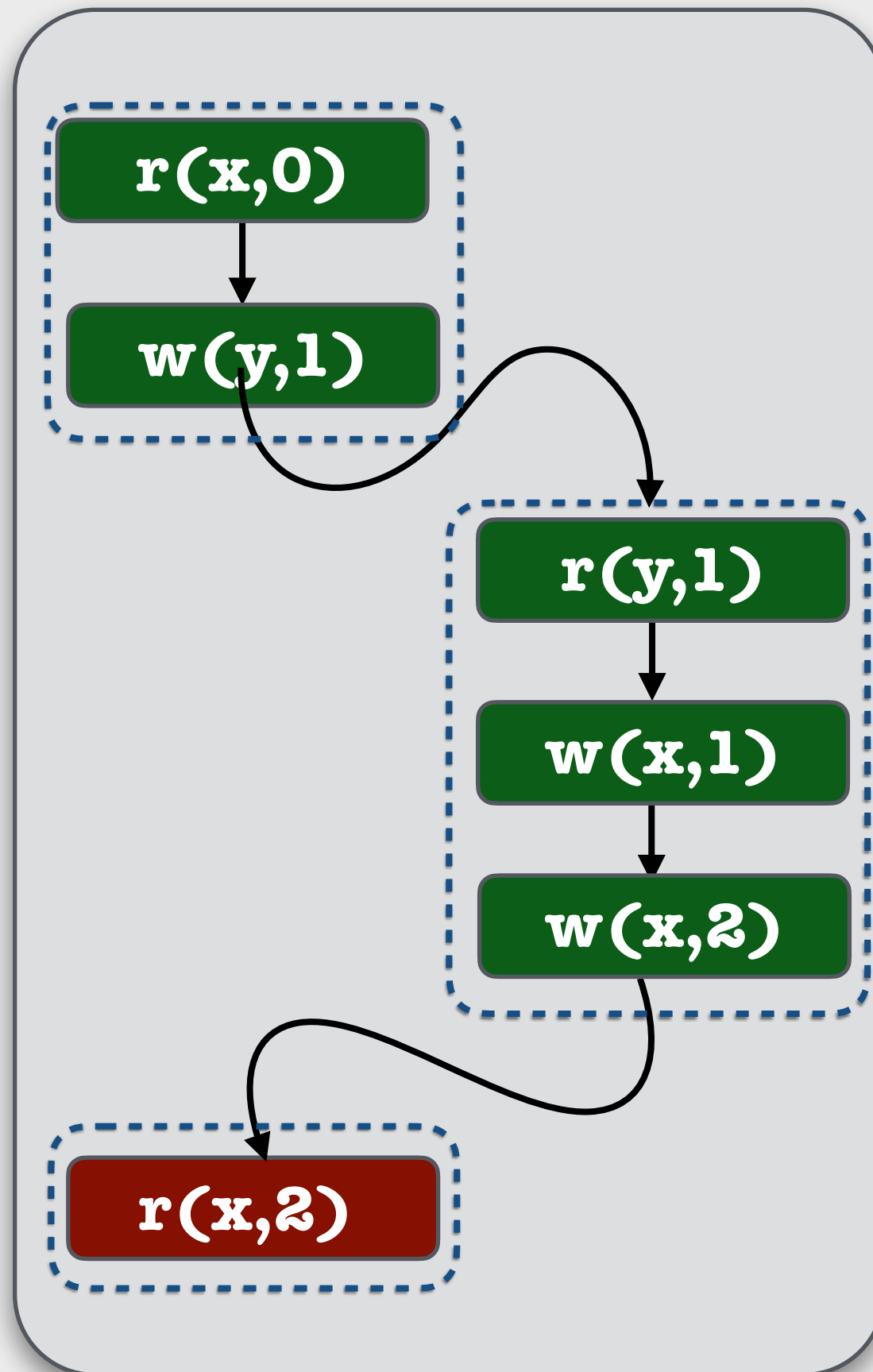
- On each read $r:=x$, guess if this read is view altering. If yes, check if $pUseful(x)$ is true in $pview_x$, and $pUseful(var)$ is true in $pView$ for all var
- Pick up a suitable $m=(x, View)$ from **Messages**

Per process p , maintain $pView=(pview_x,\dots,pview_z)$,
 $pview_x=(ptime(x), pval(x), pUseful(x))$

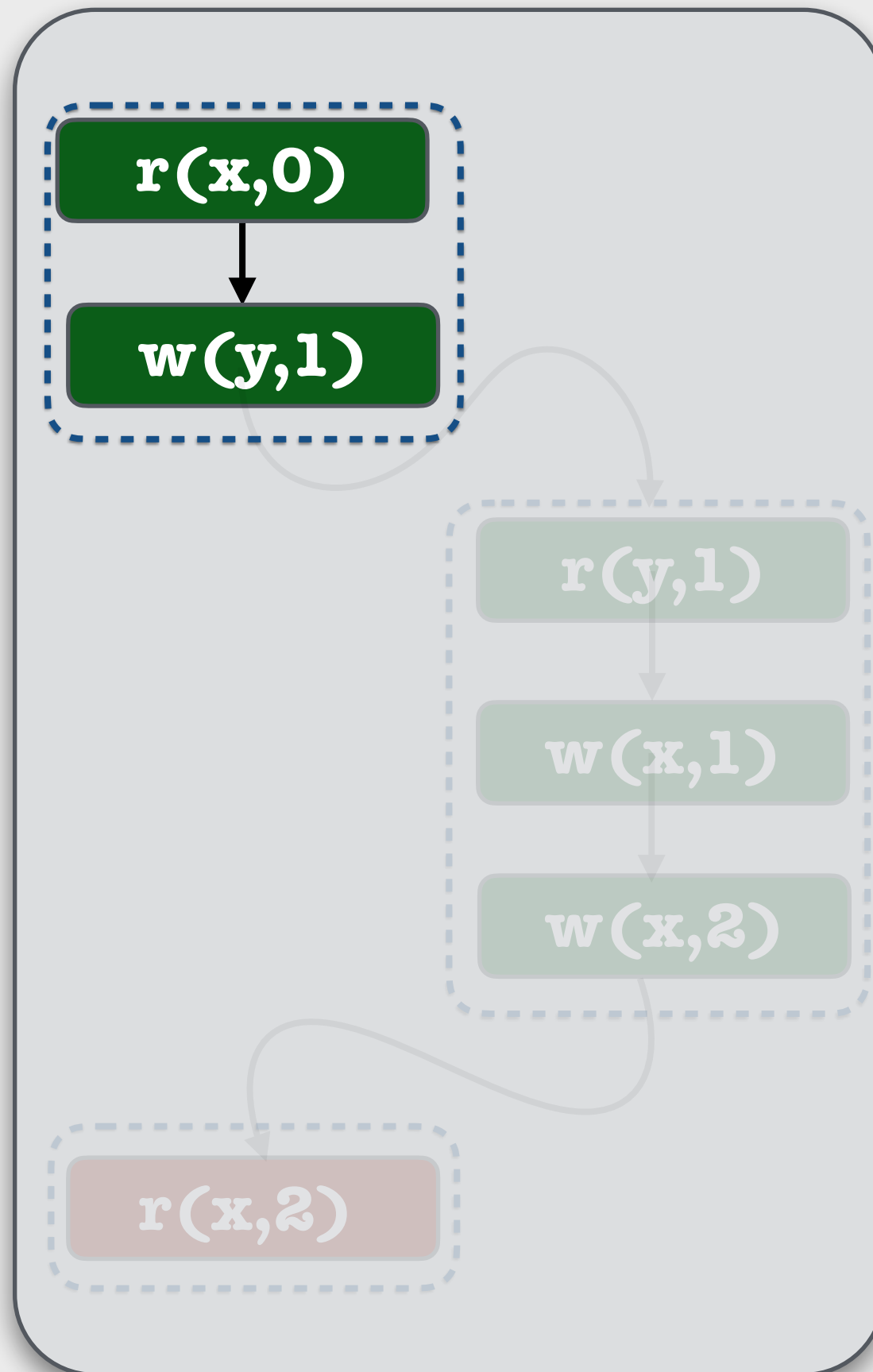
Globally, maintain a set of messages, each of type $(var, View)$

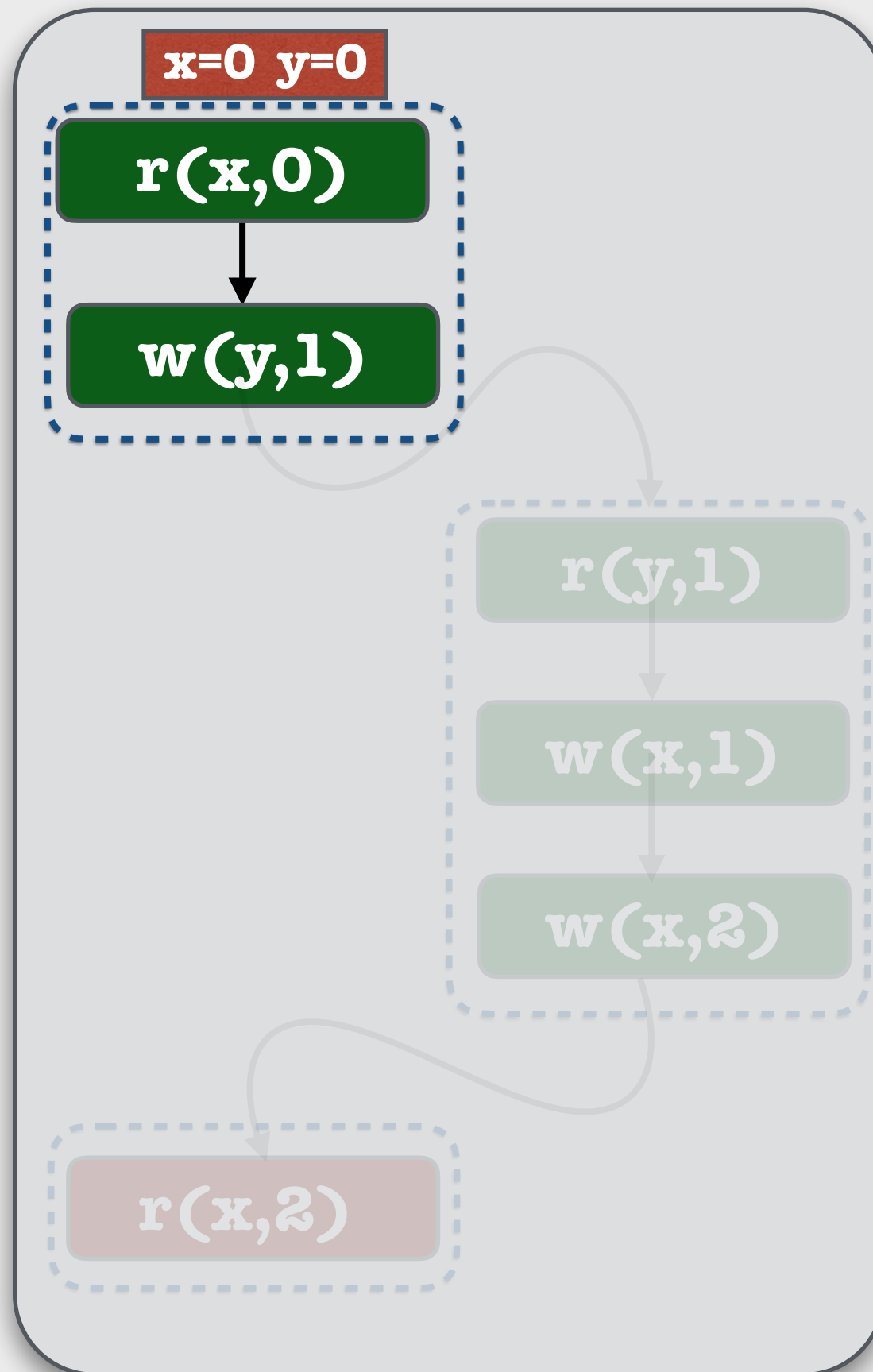
- On each read $r:=x$, guess if this read is view altering. If yes, check if $pUseful(x)$ is true in $pview_x$, and $pUseful(var)$ is true in $pView$ for all var
- Pick up a suitable $m=(x, View)$ from **Messages**
- Update $ptime(var), pval(var)$ in $pView$ if $ptime(var)$ is at most $mtime(var)$

Guess essential messages,
run processes generating
them in order

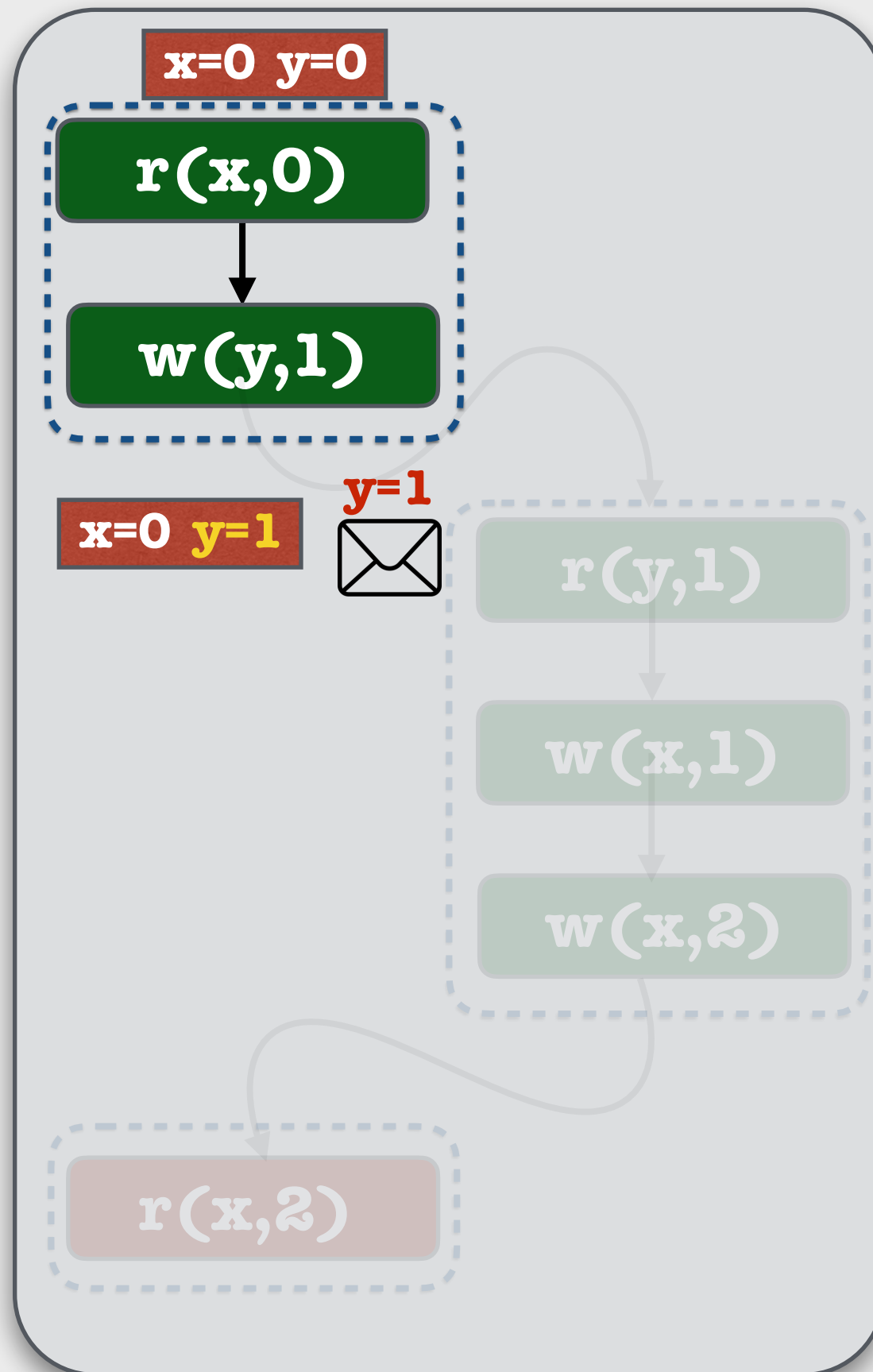


Guess essential messages,
run processes generating
them in order



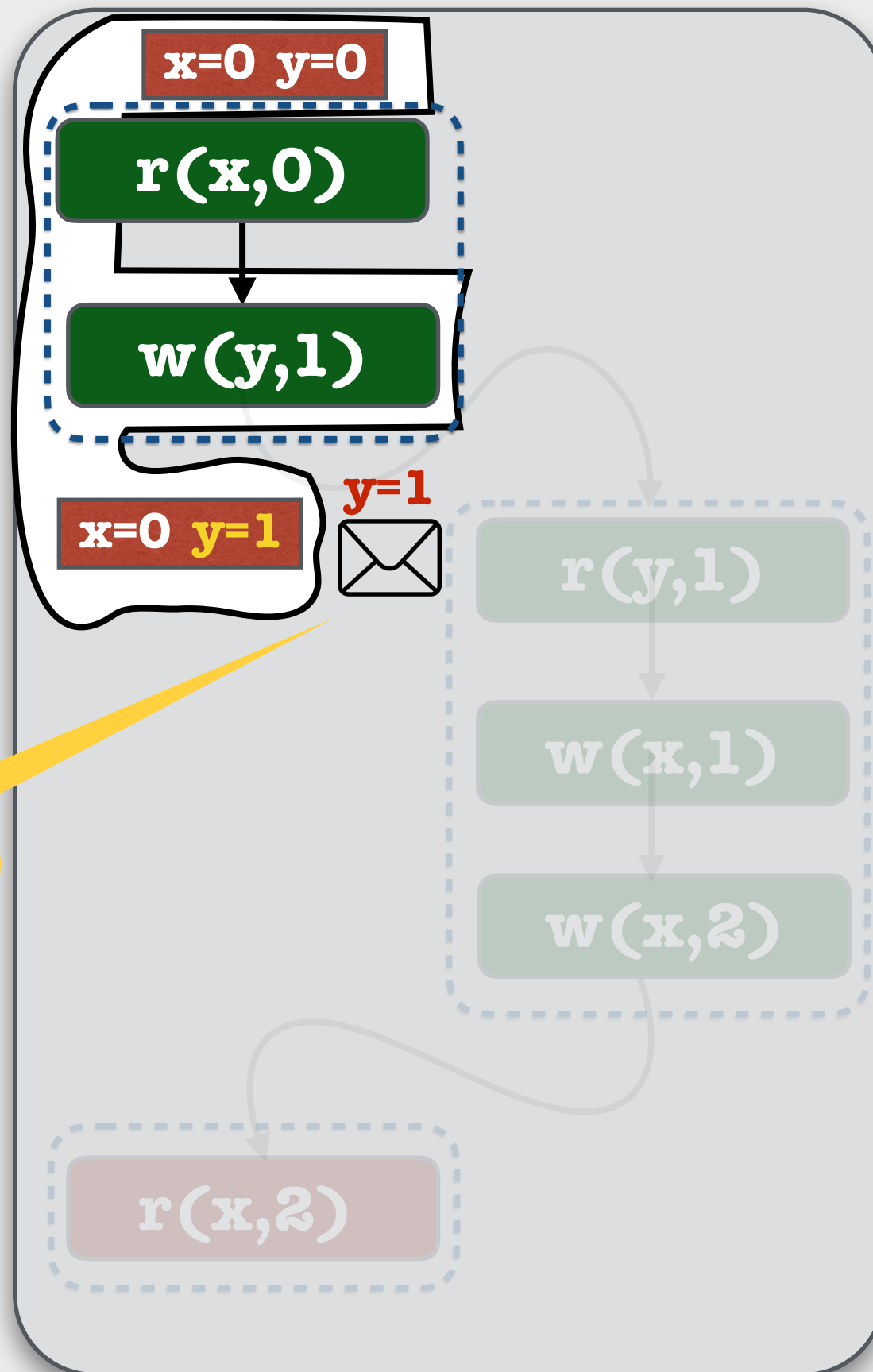


Guess essential messages,
run processes generating
them in order



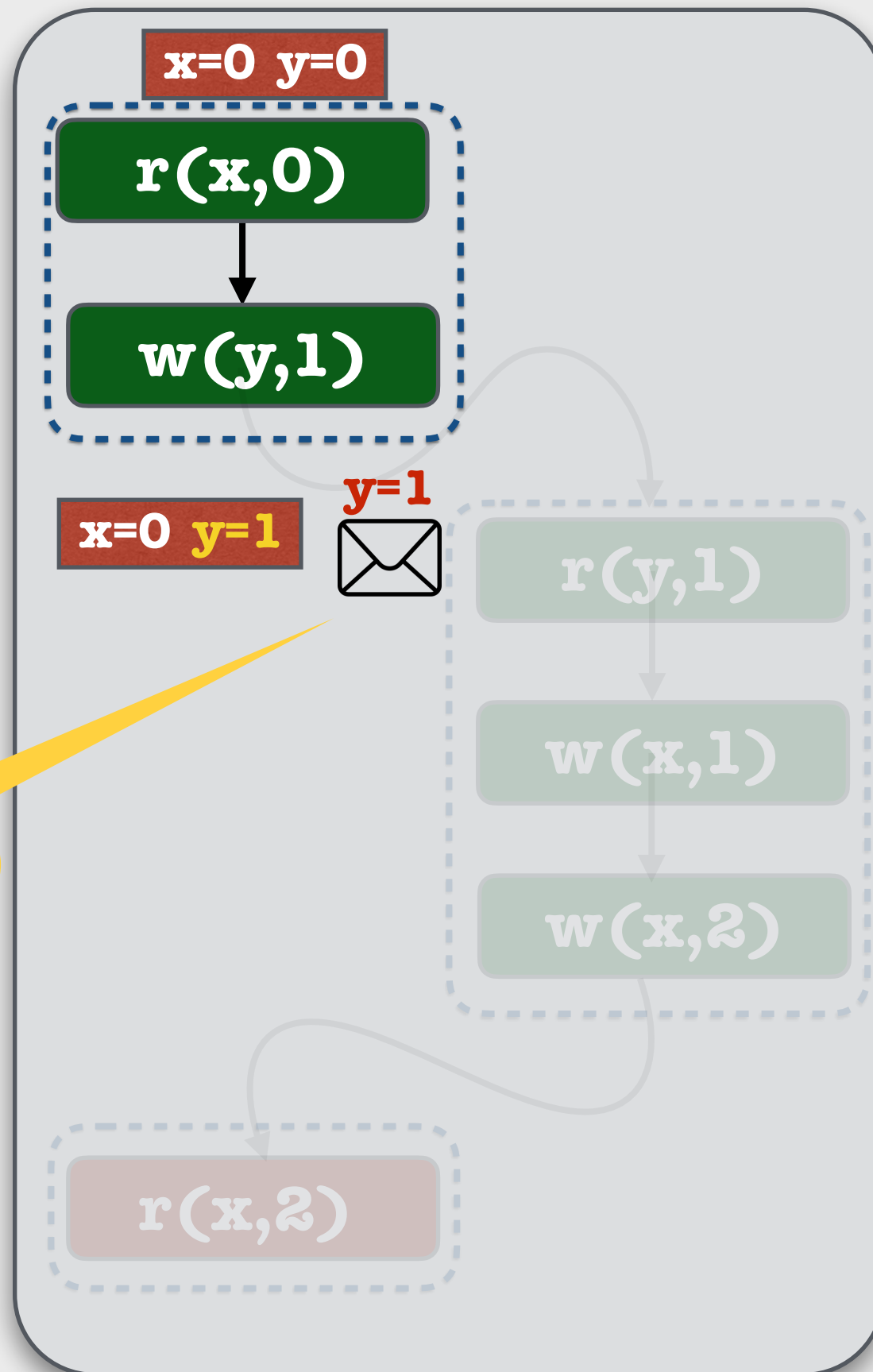
Guess essential messages, run processes generating them in order

Guess essential messages,
run processes generating
them in order



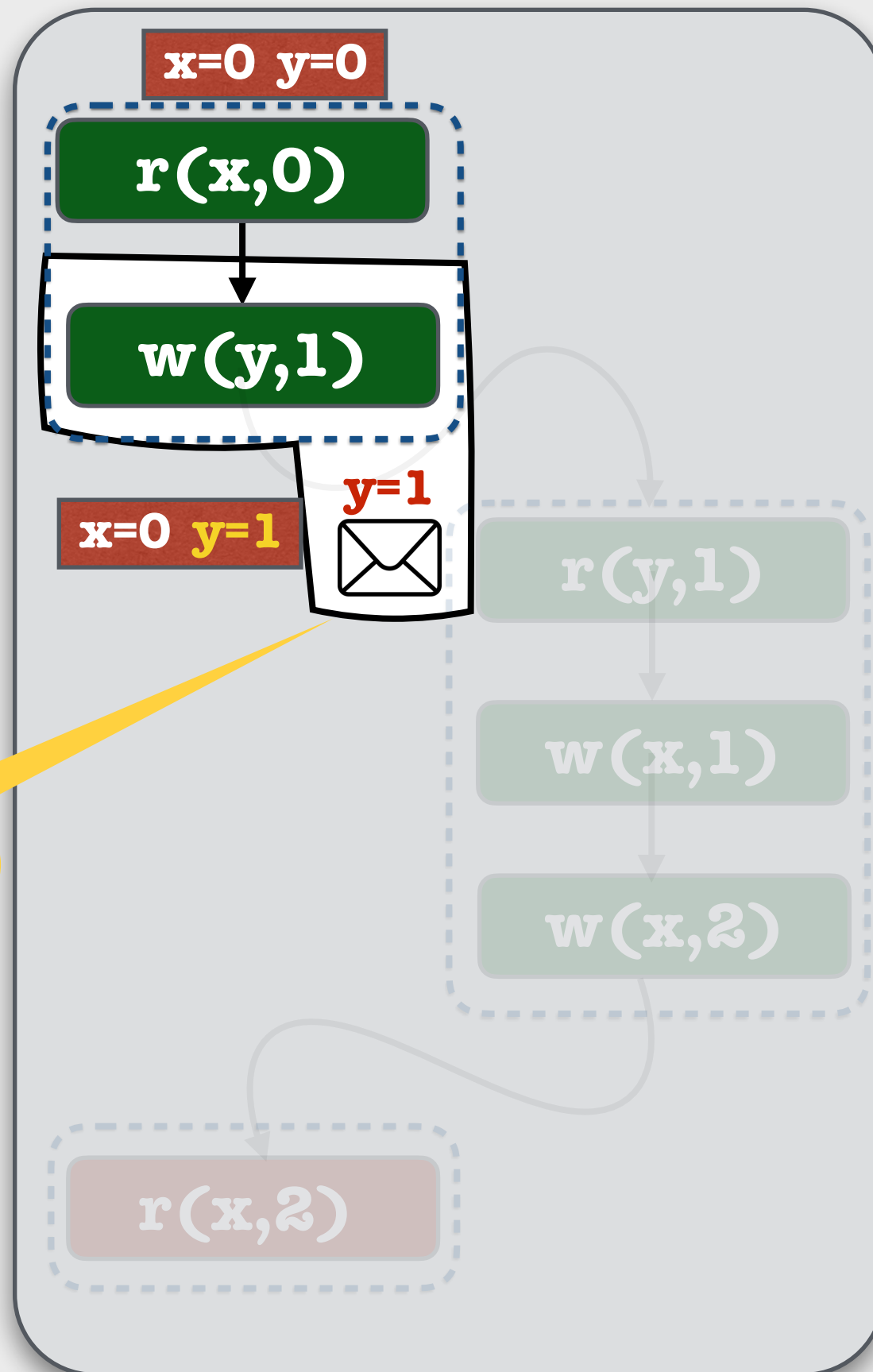
First essential
message

Guess essential messages,
run processes generating
them in order

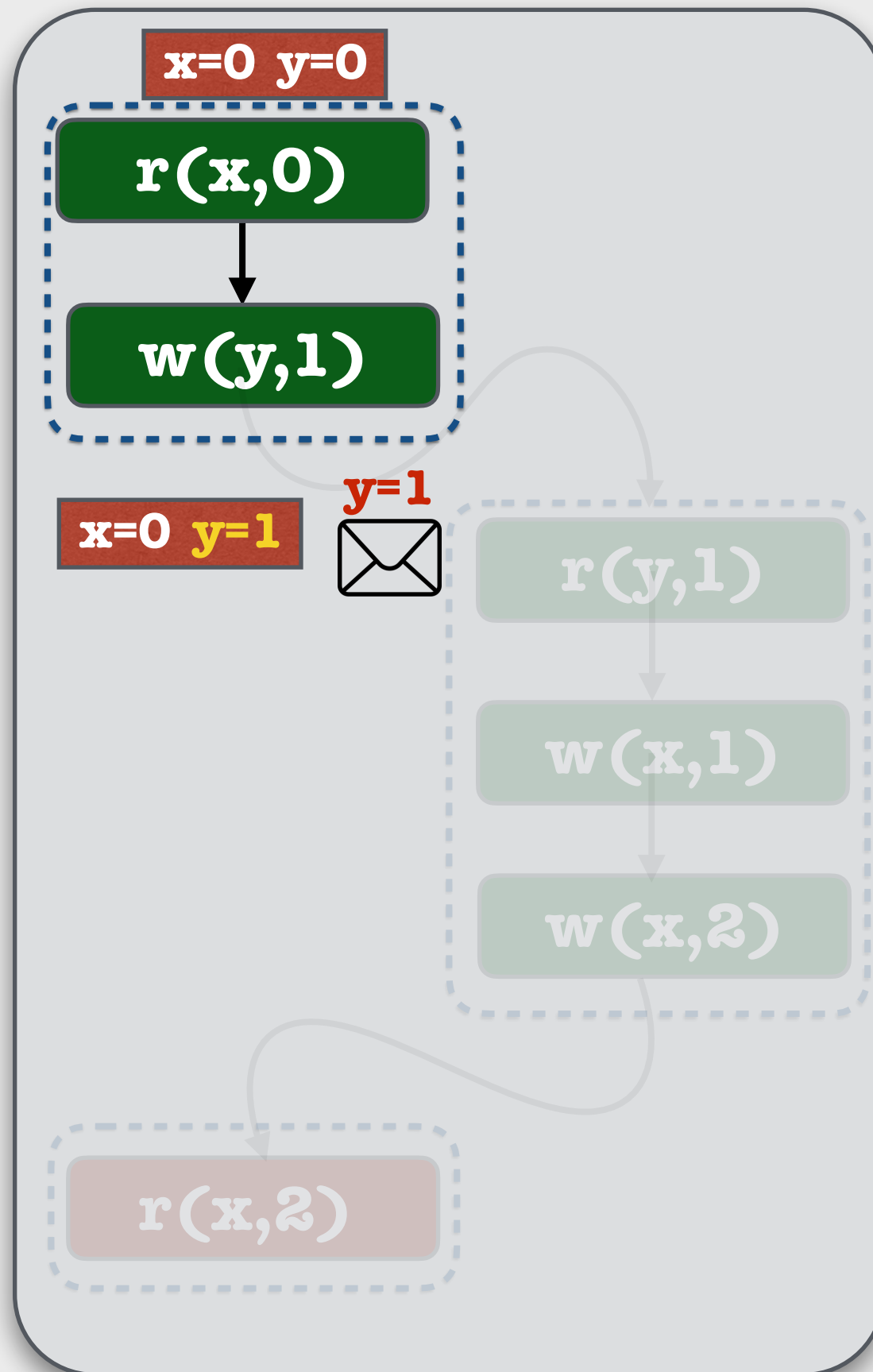


First essential
message

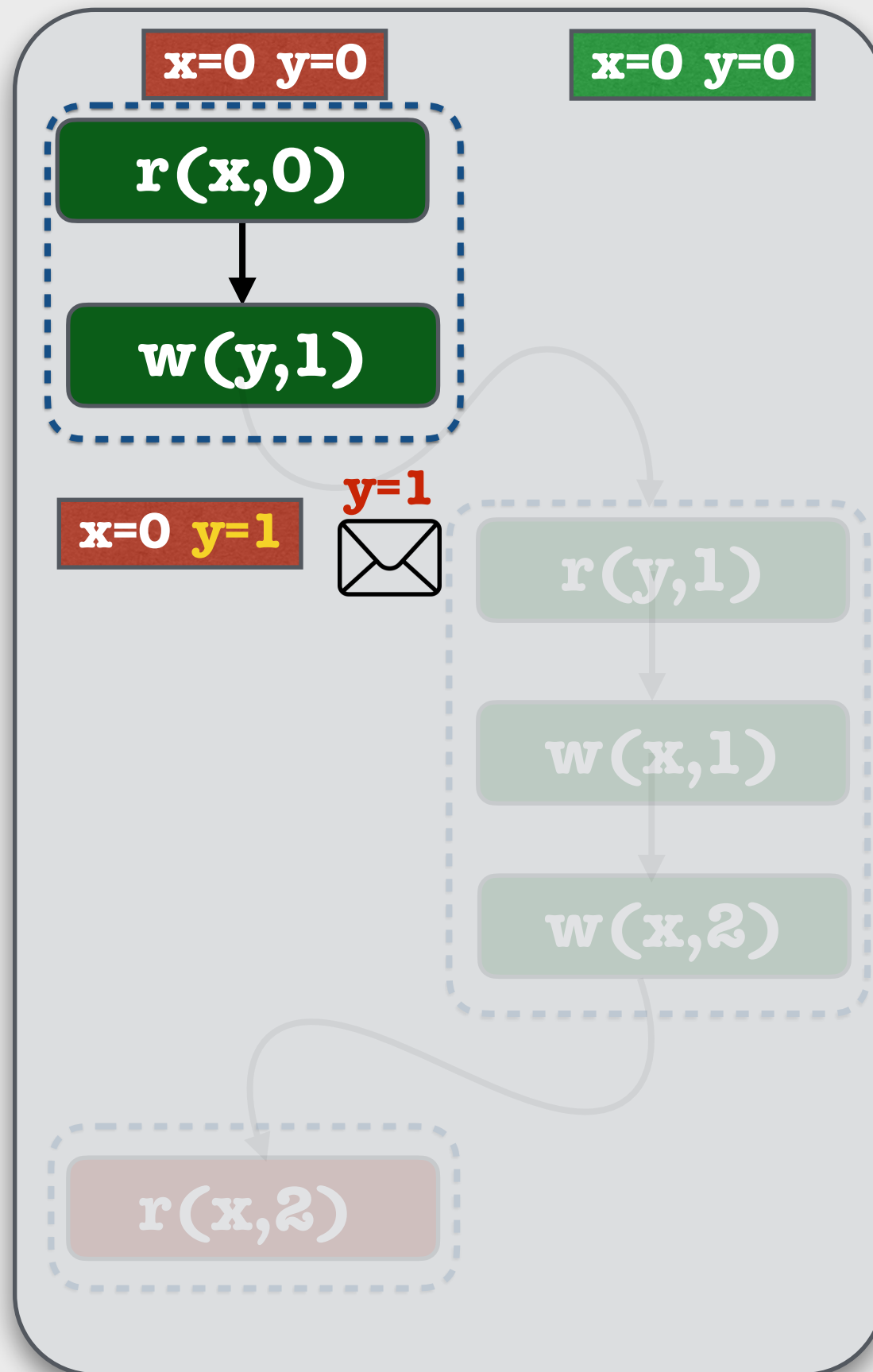
Guess essential messages,
run processes generating
them in order



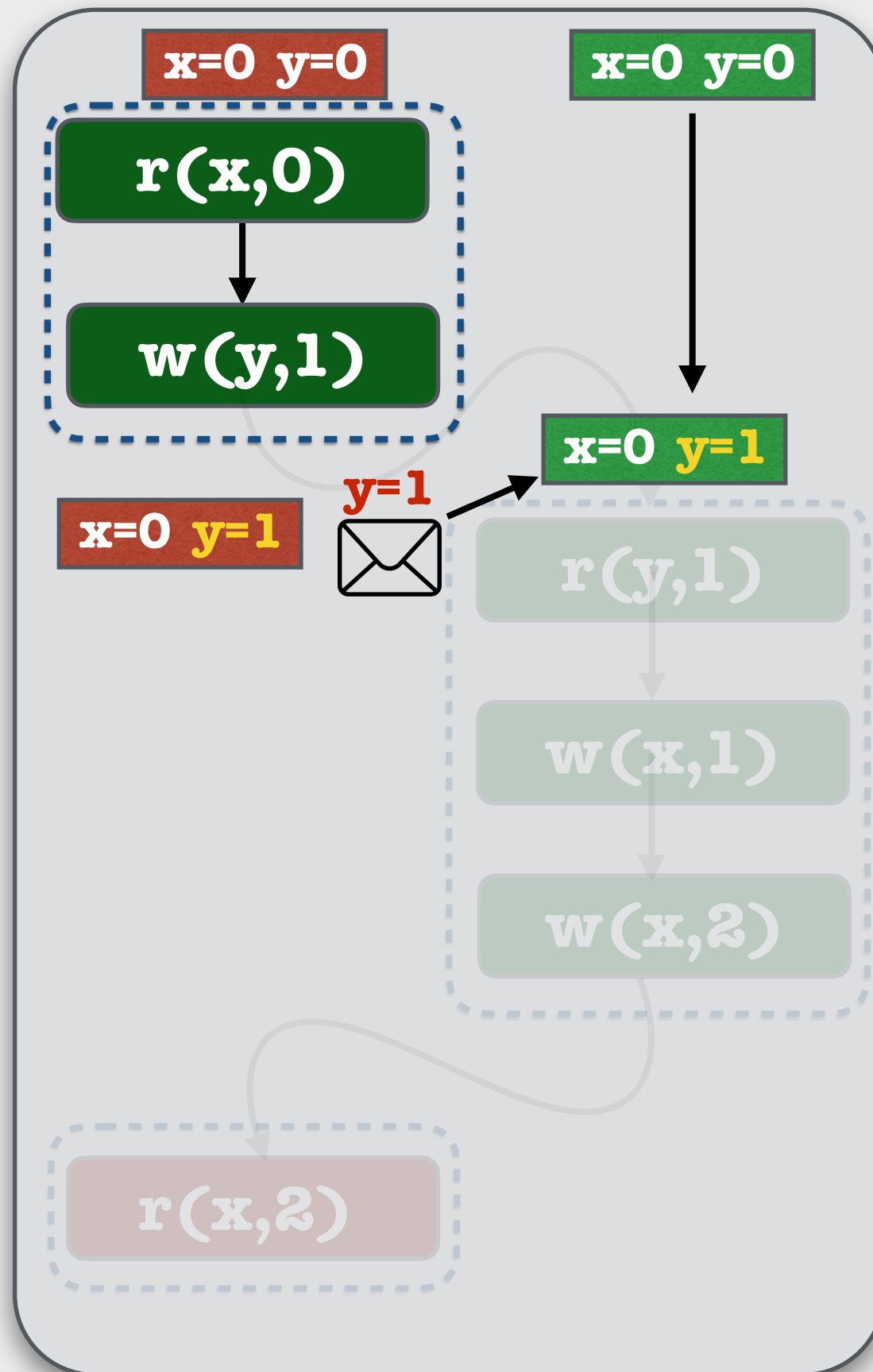
First essential
message



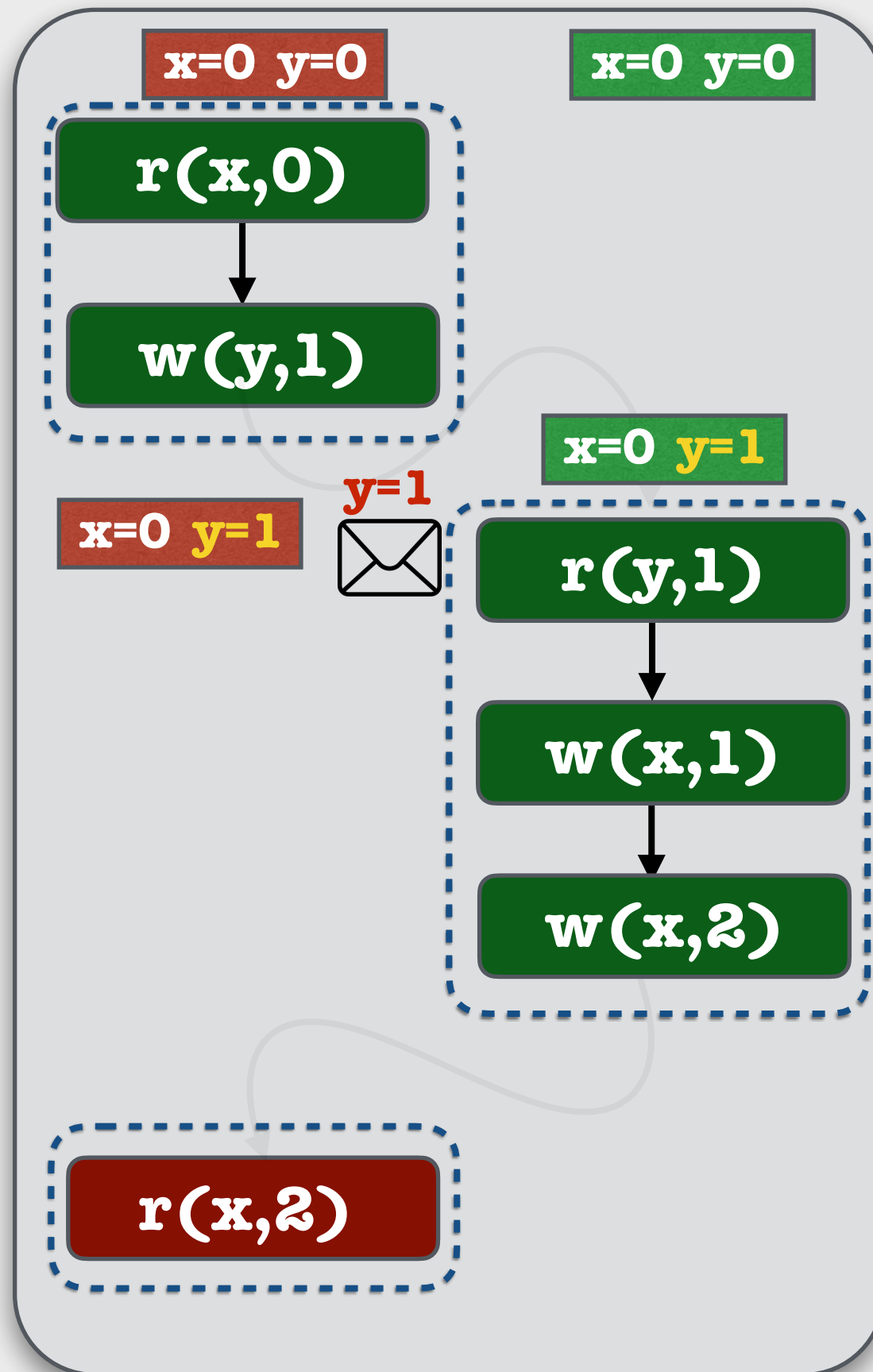
Guess essential messages, run processes generating them in order



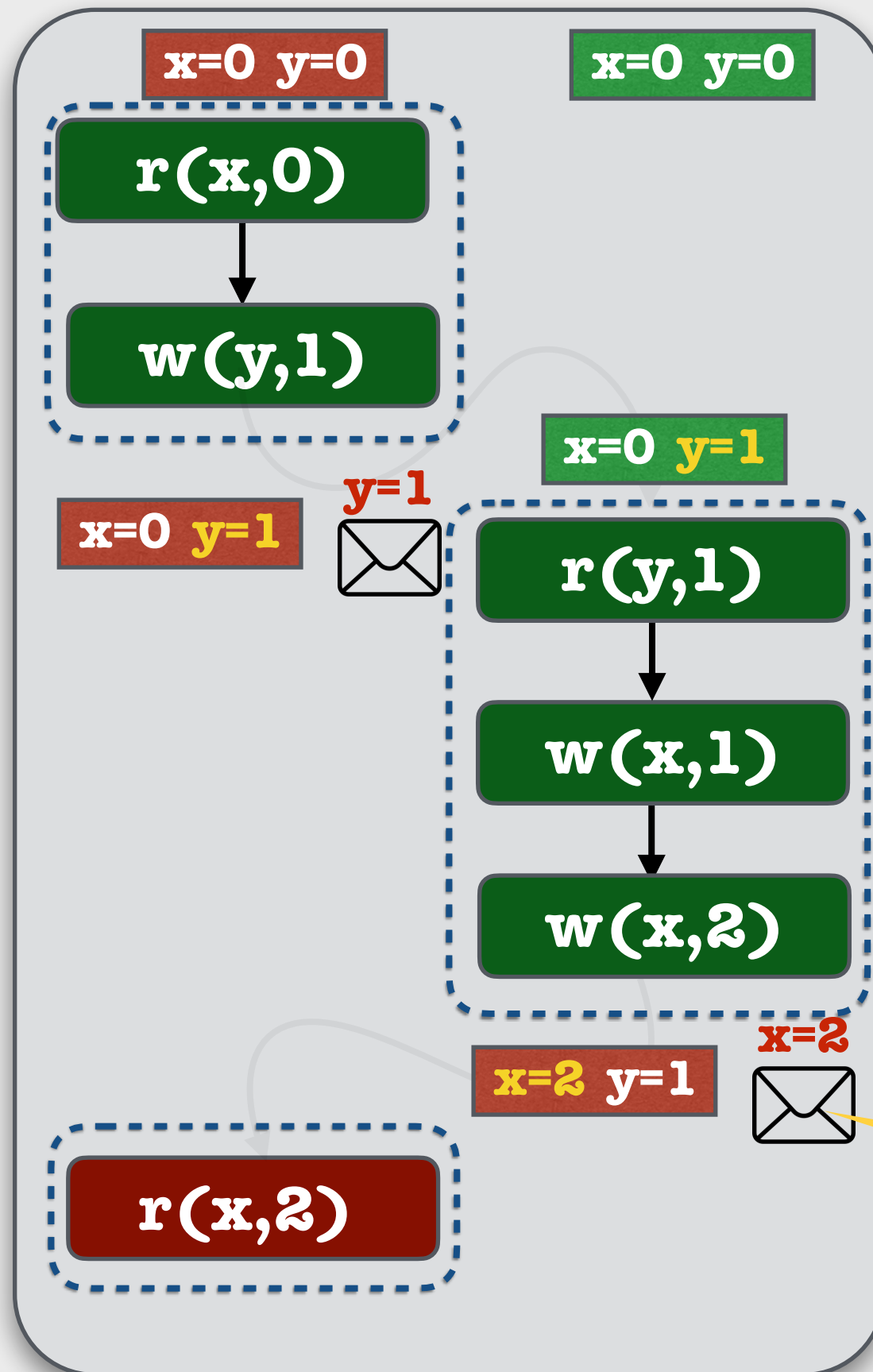
Guess essential messages, run processes generating them in order



Guess essential messages, run processes generating them in order

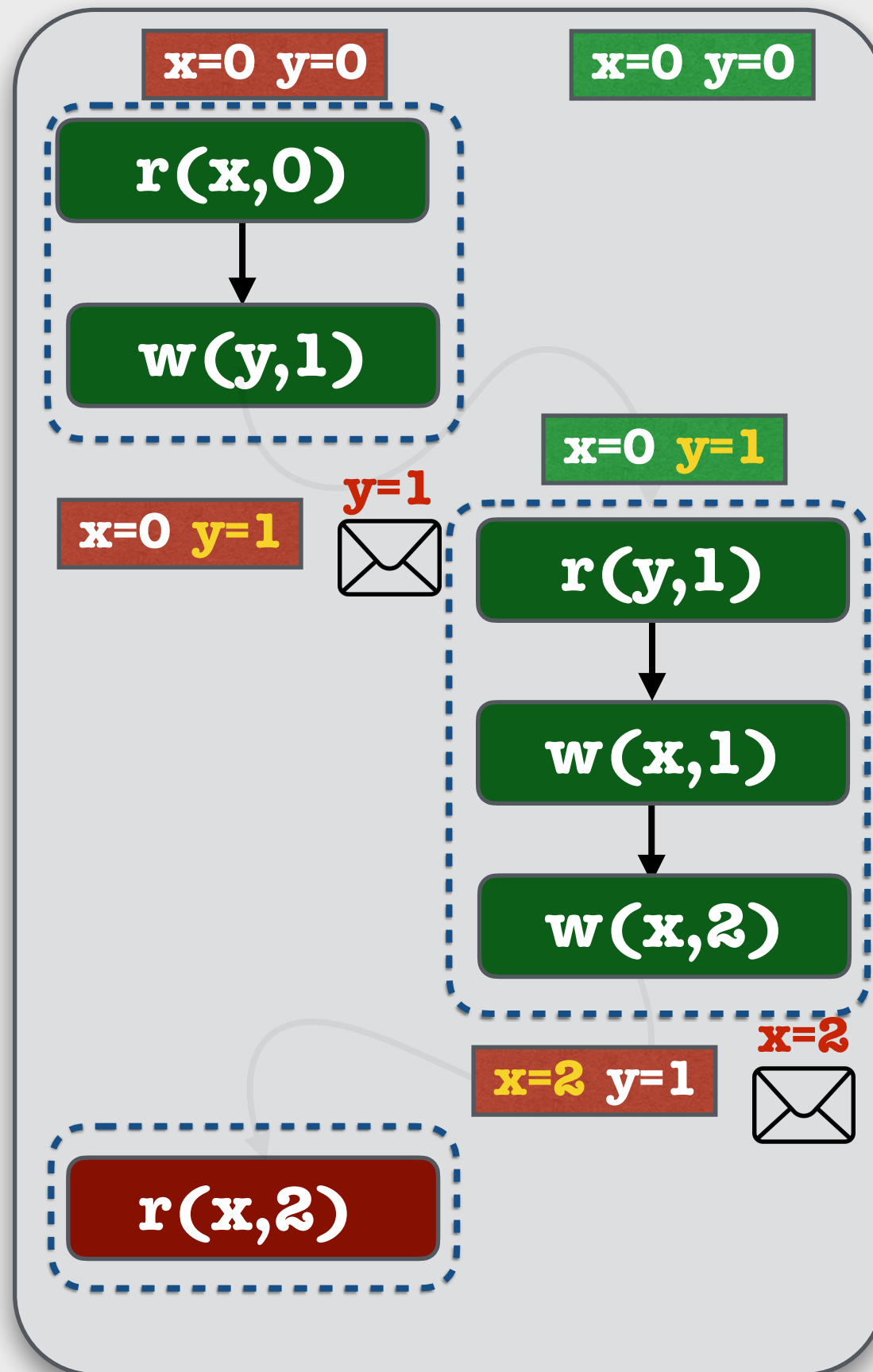


Guess essential messages, run processes generating them in order

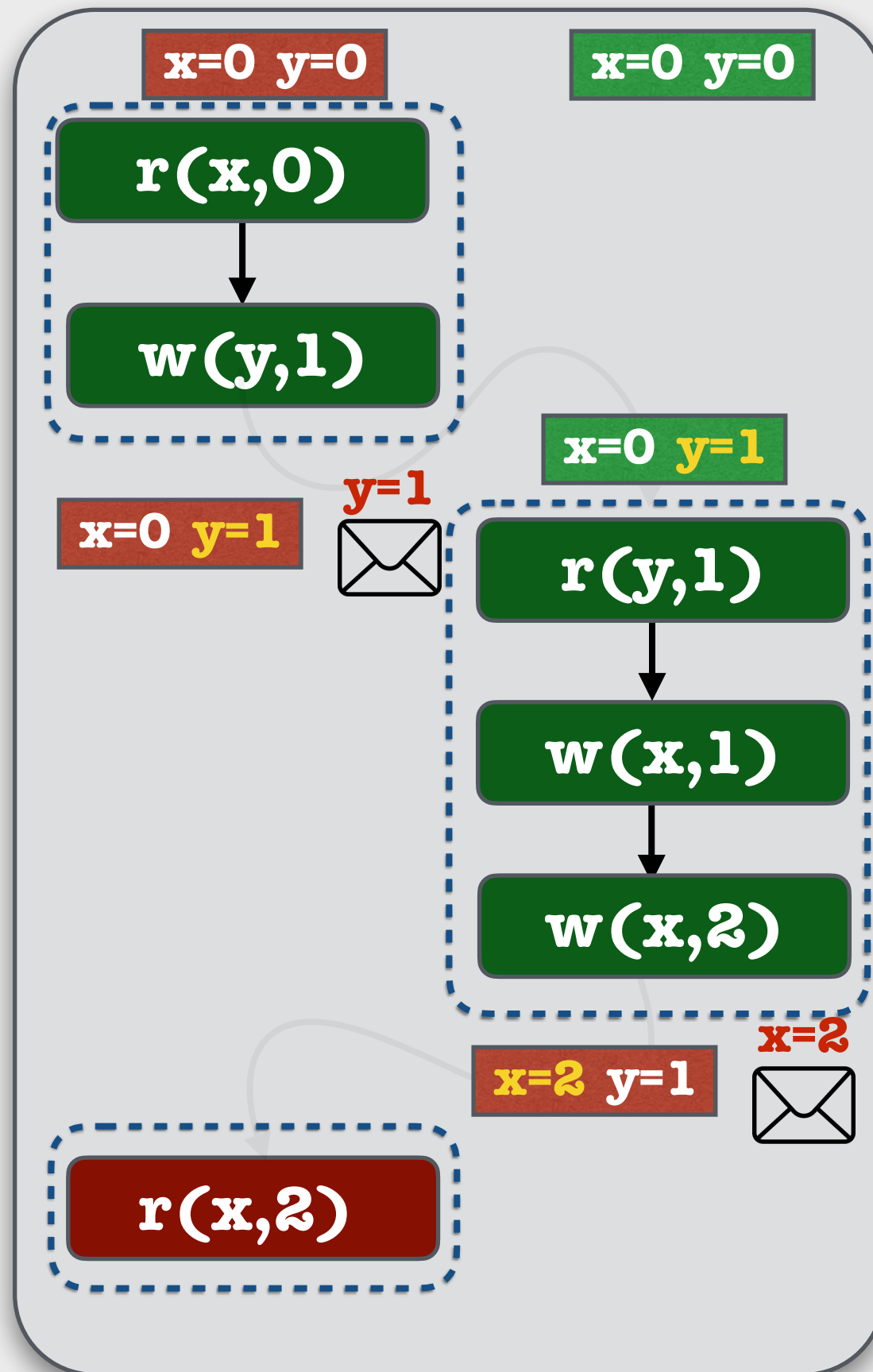


Guess essential messages, run processes generating them in order

second essential message

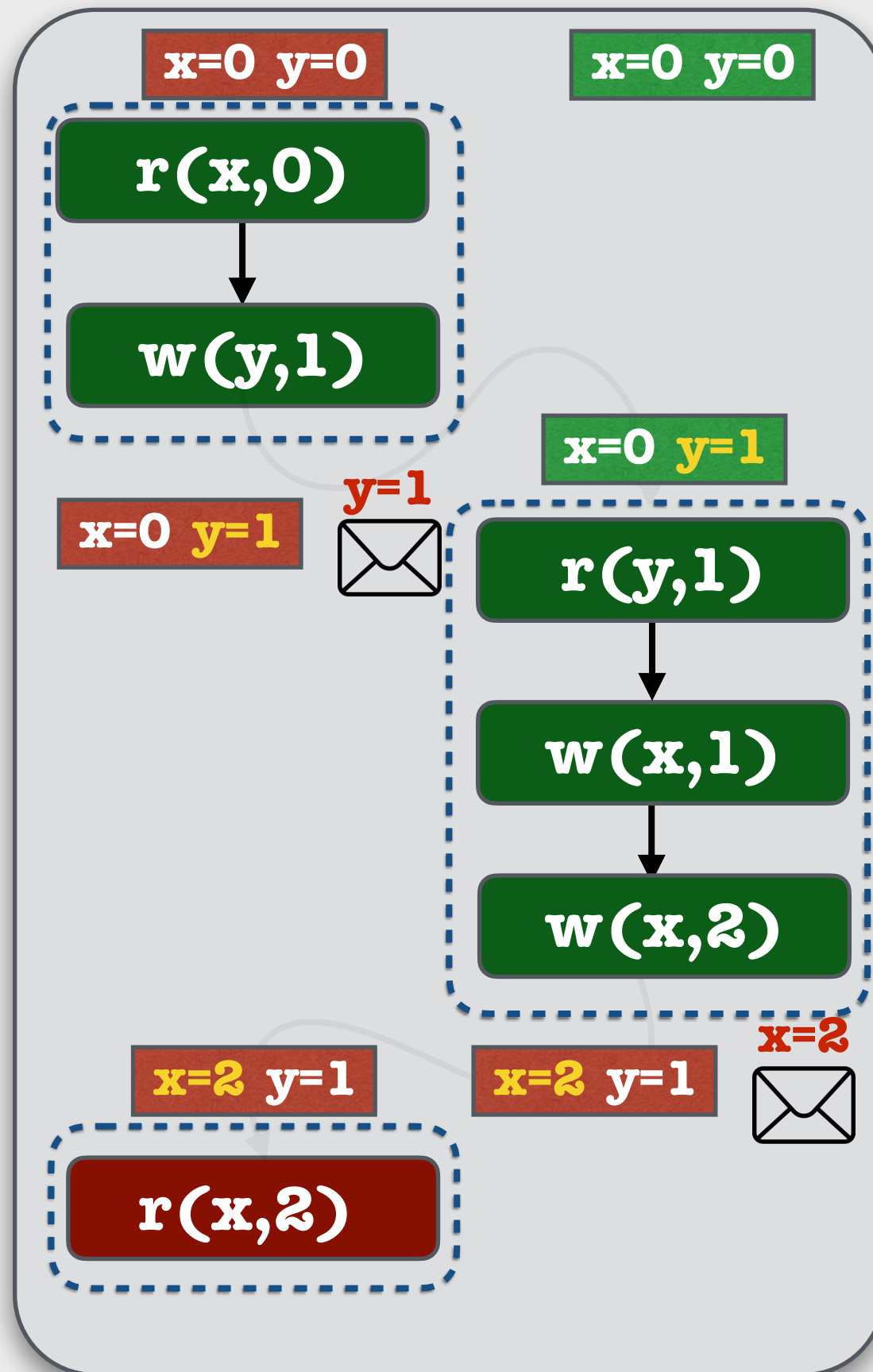


Guess essential messages, run processes generating them in order



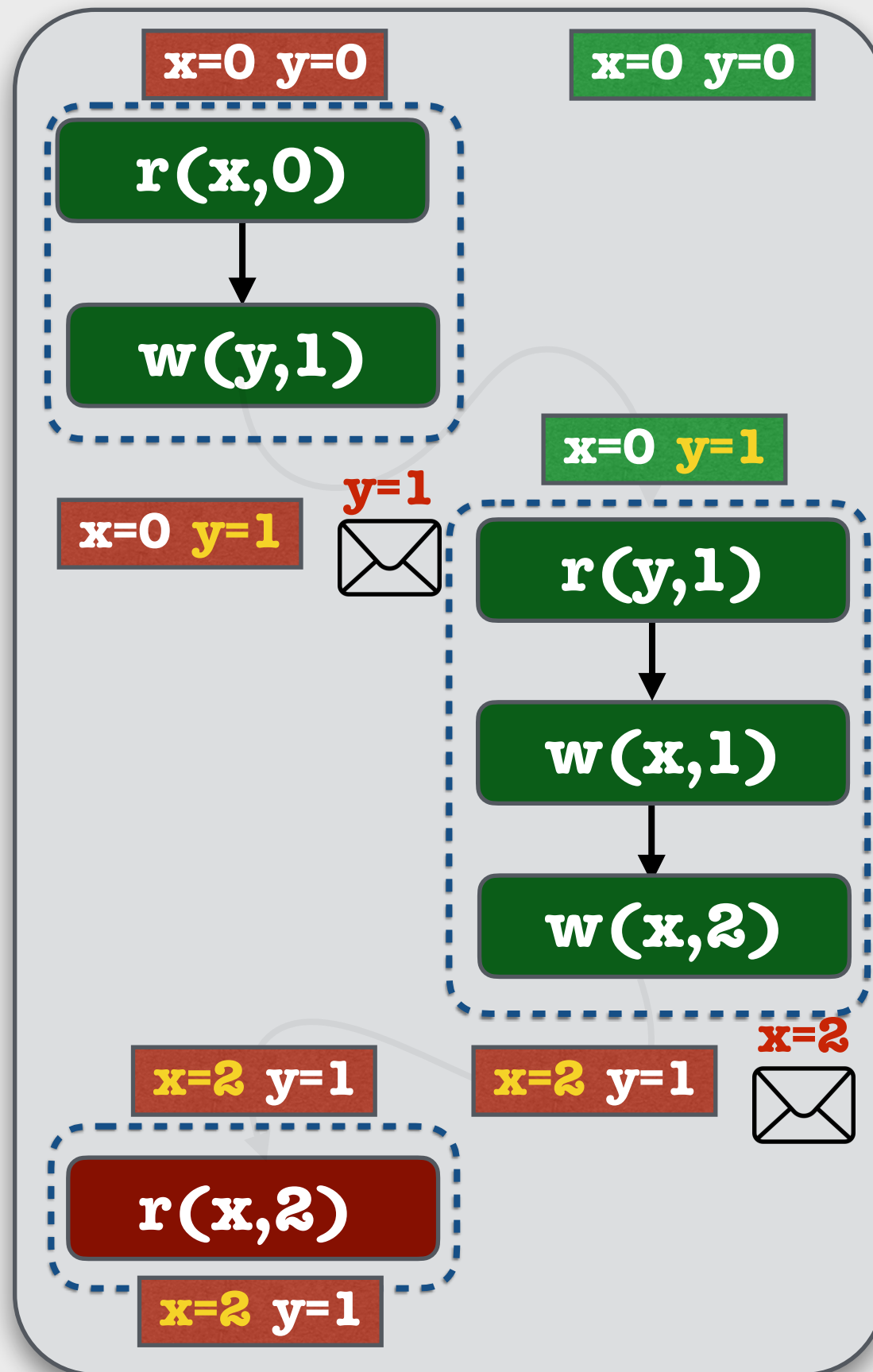
Guess essential messages, run processes generating them in order

Once all essential messages are generated, run all processes to completion



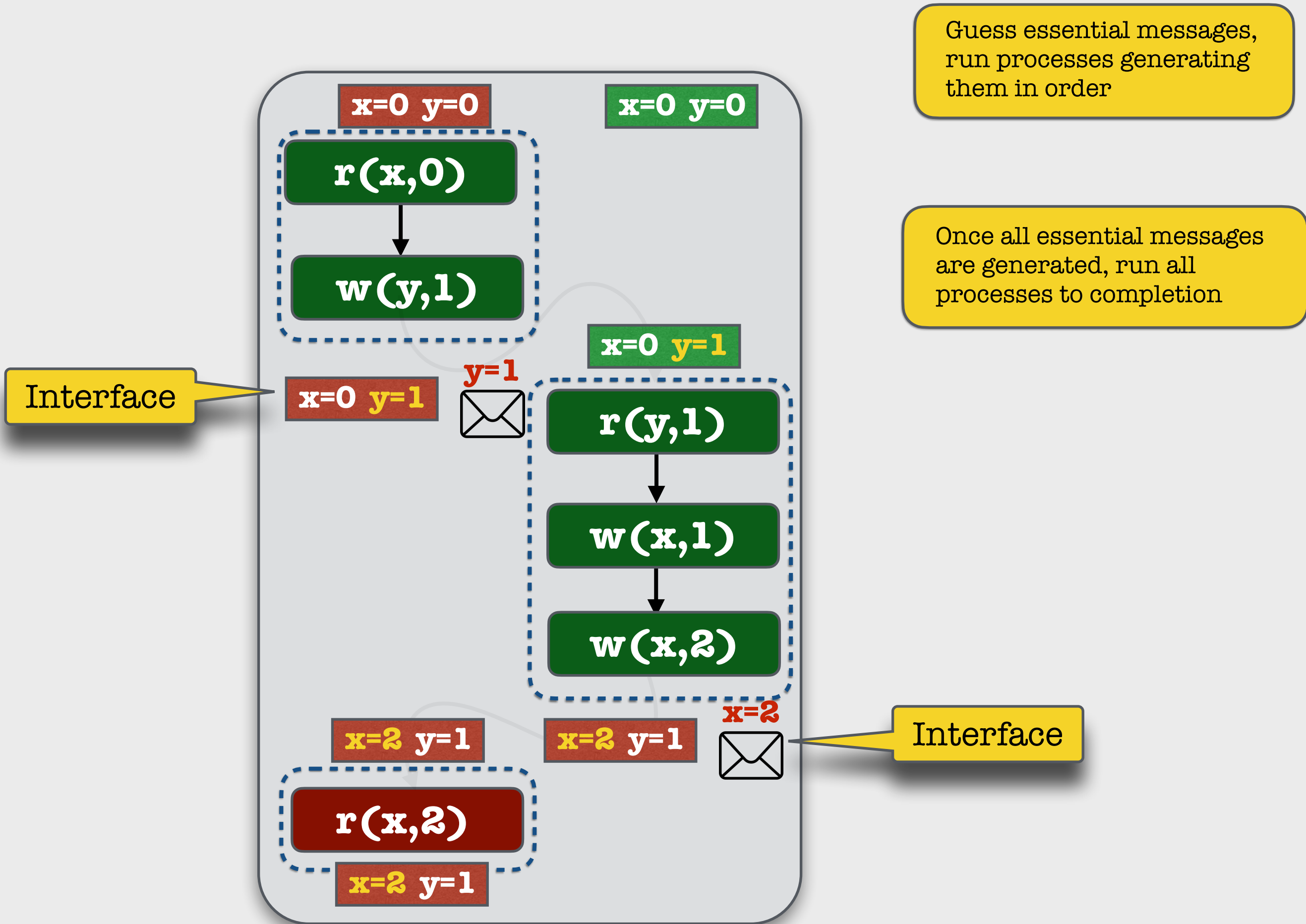
Guess essential messages, run processes generating them in order

Once all essential messages are generated, run all processes to completion



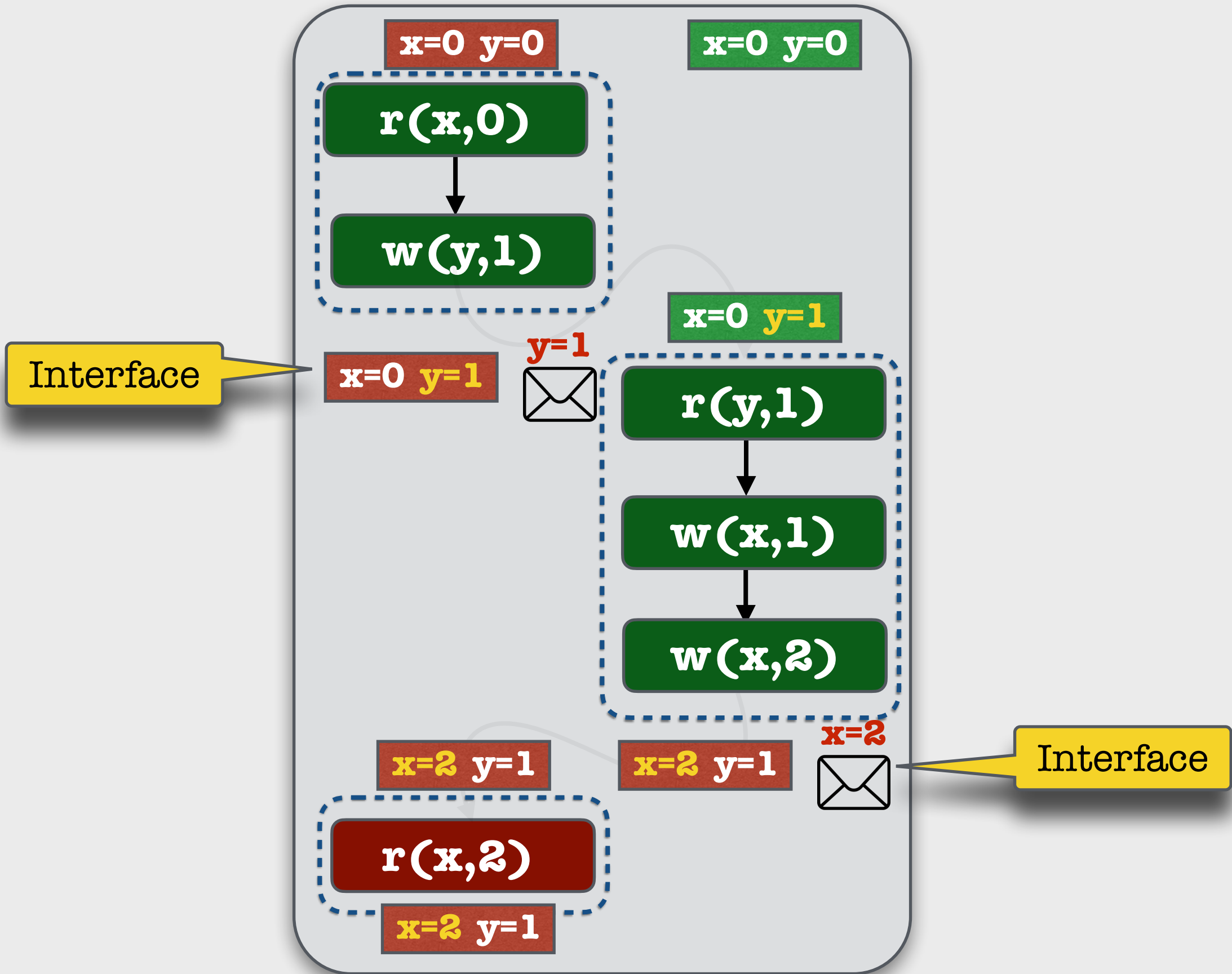
Guess essential messages, run processes generating them in order

Once all essential messages are generated, run all processes to completion



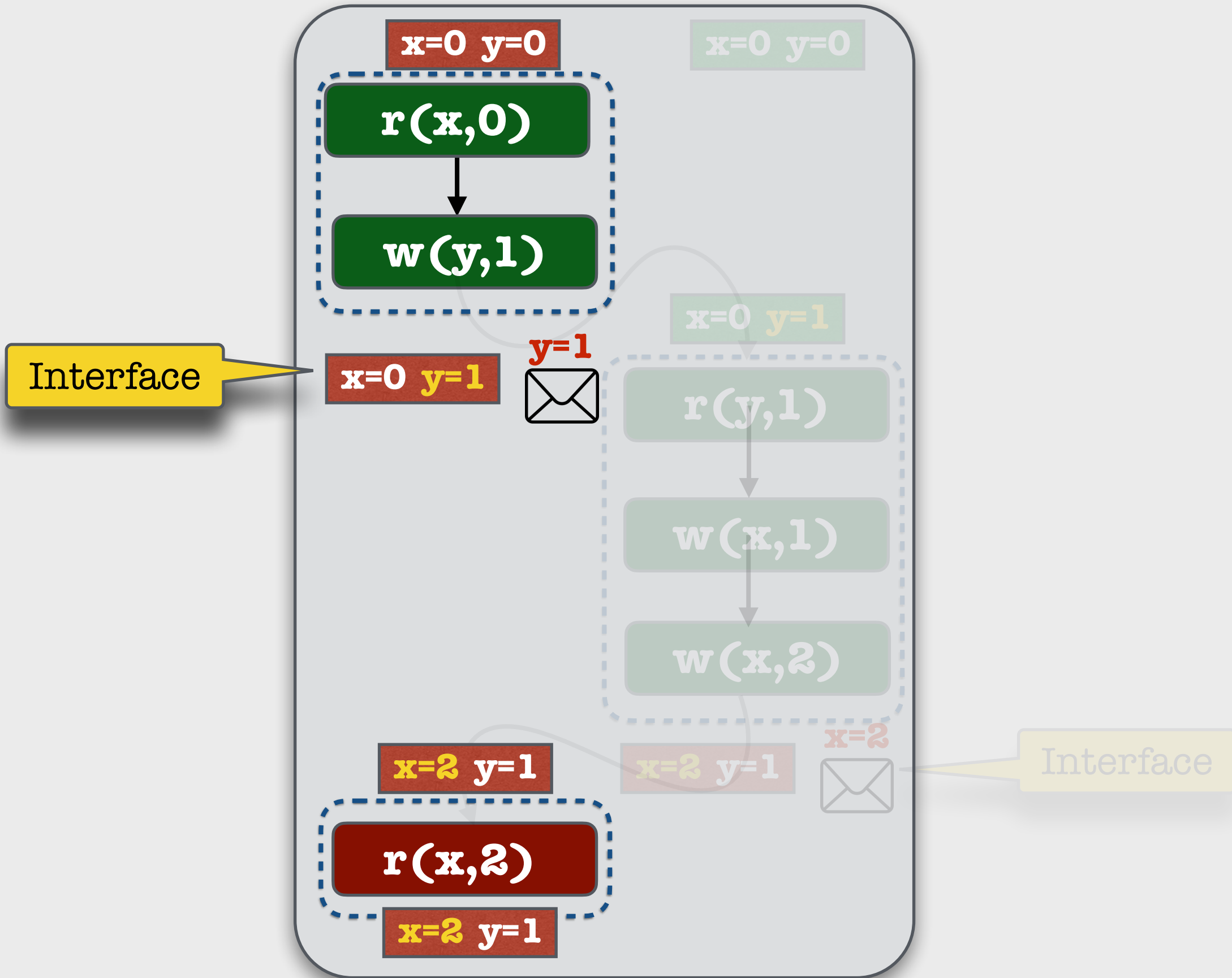
Locality

Validation



Locality

Validation

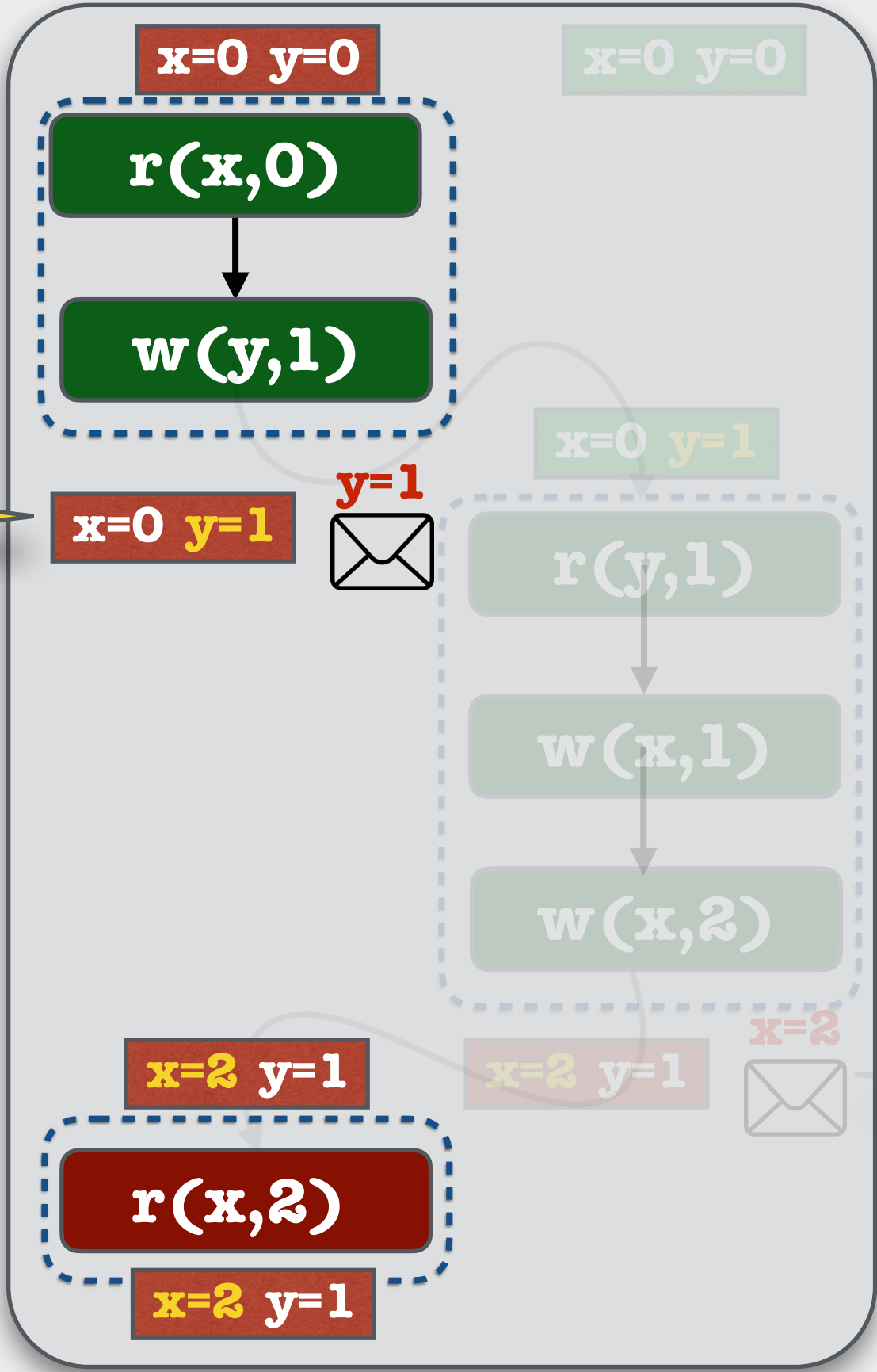


Locality

Validation

Simulate **P1** under **SC**

Interface

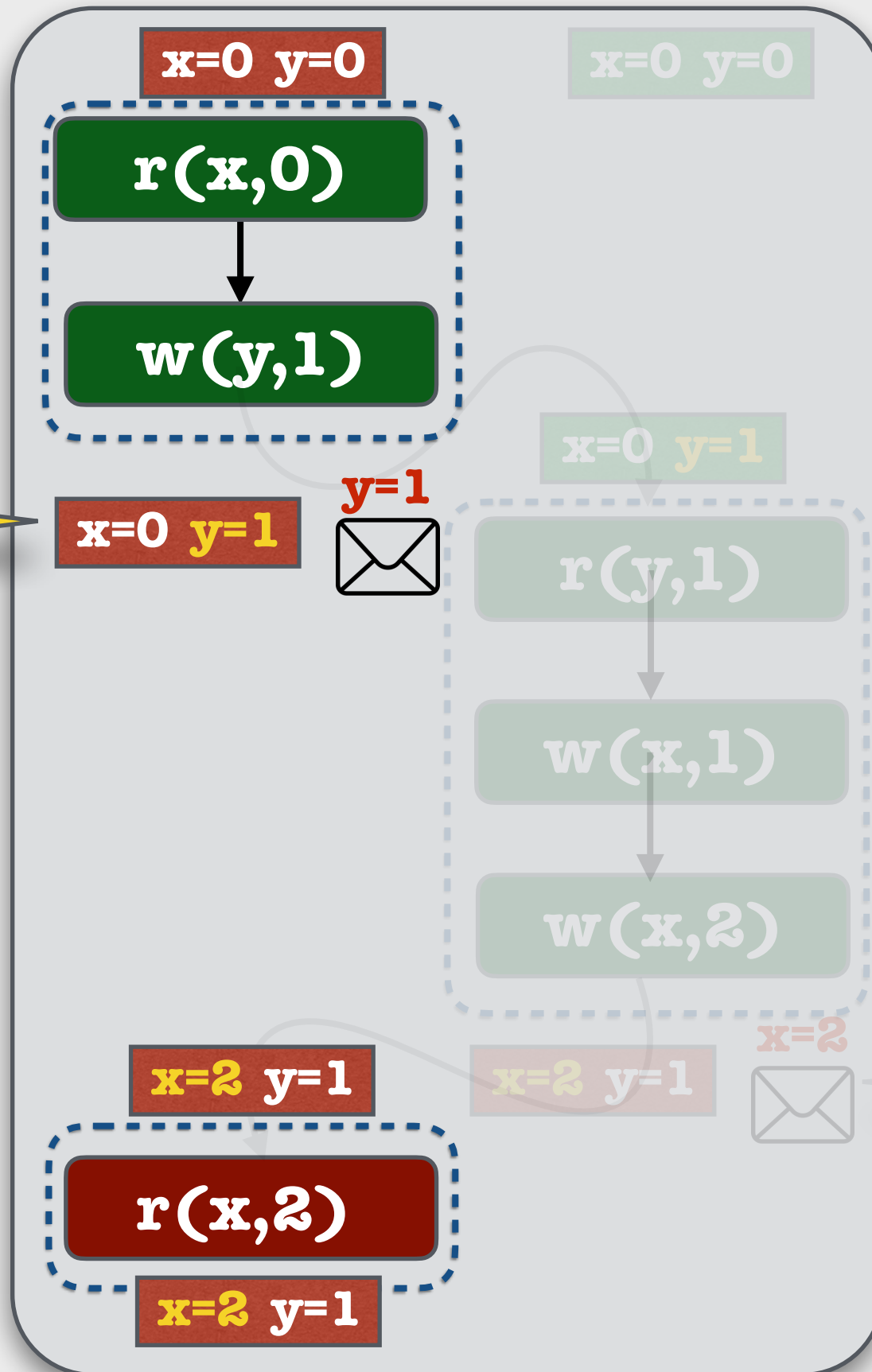


Interface

Locality

Validation

Interface



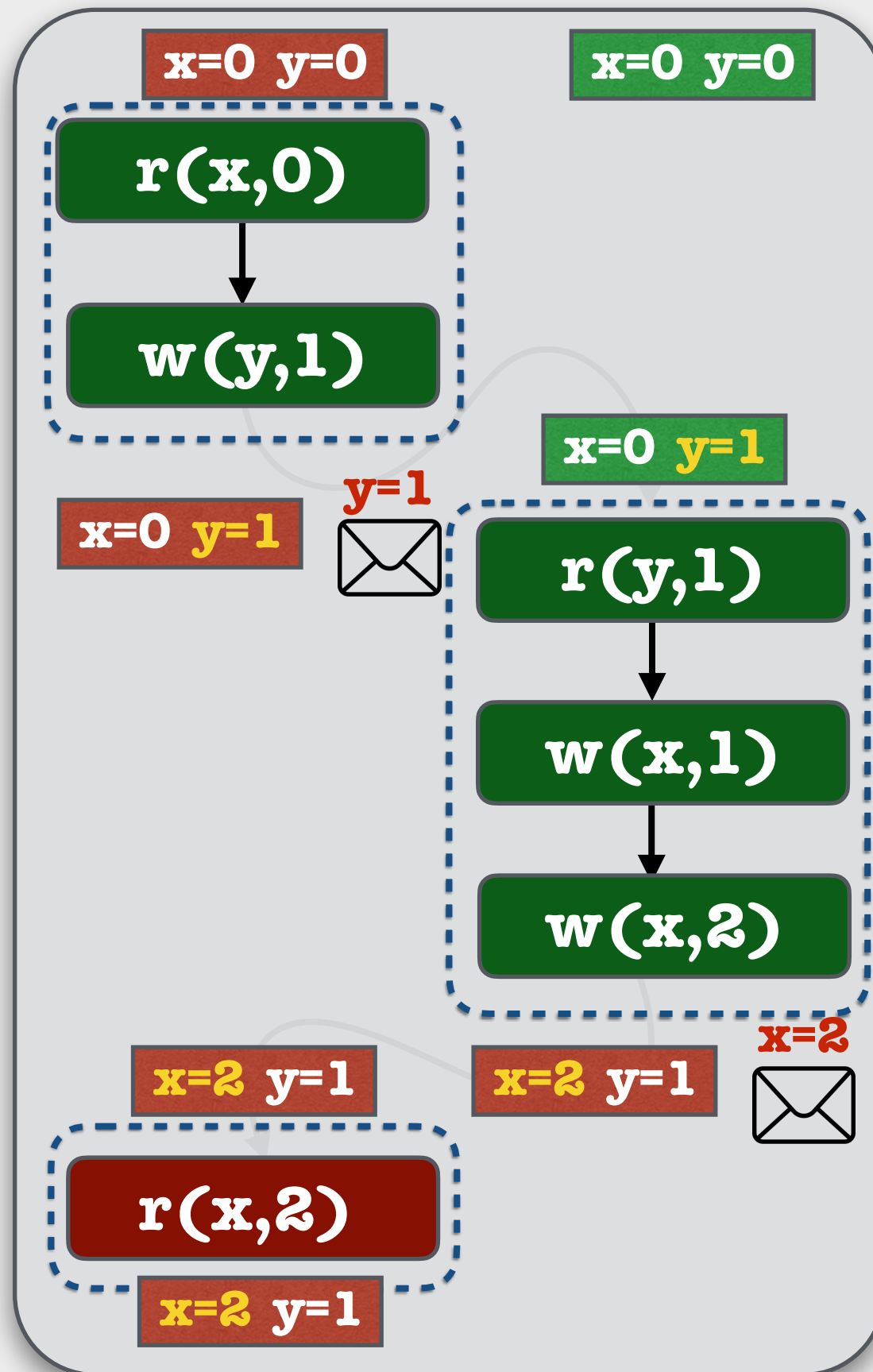
Simulate **P1** under **SC**

Local and global data structures to simulate views and the memory pool

Interface

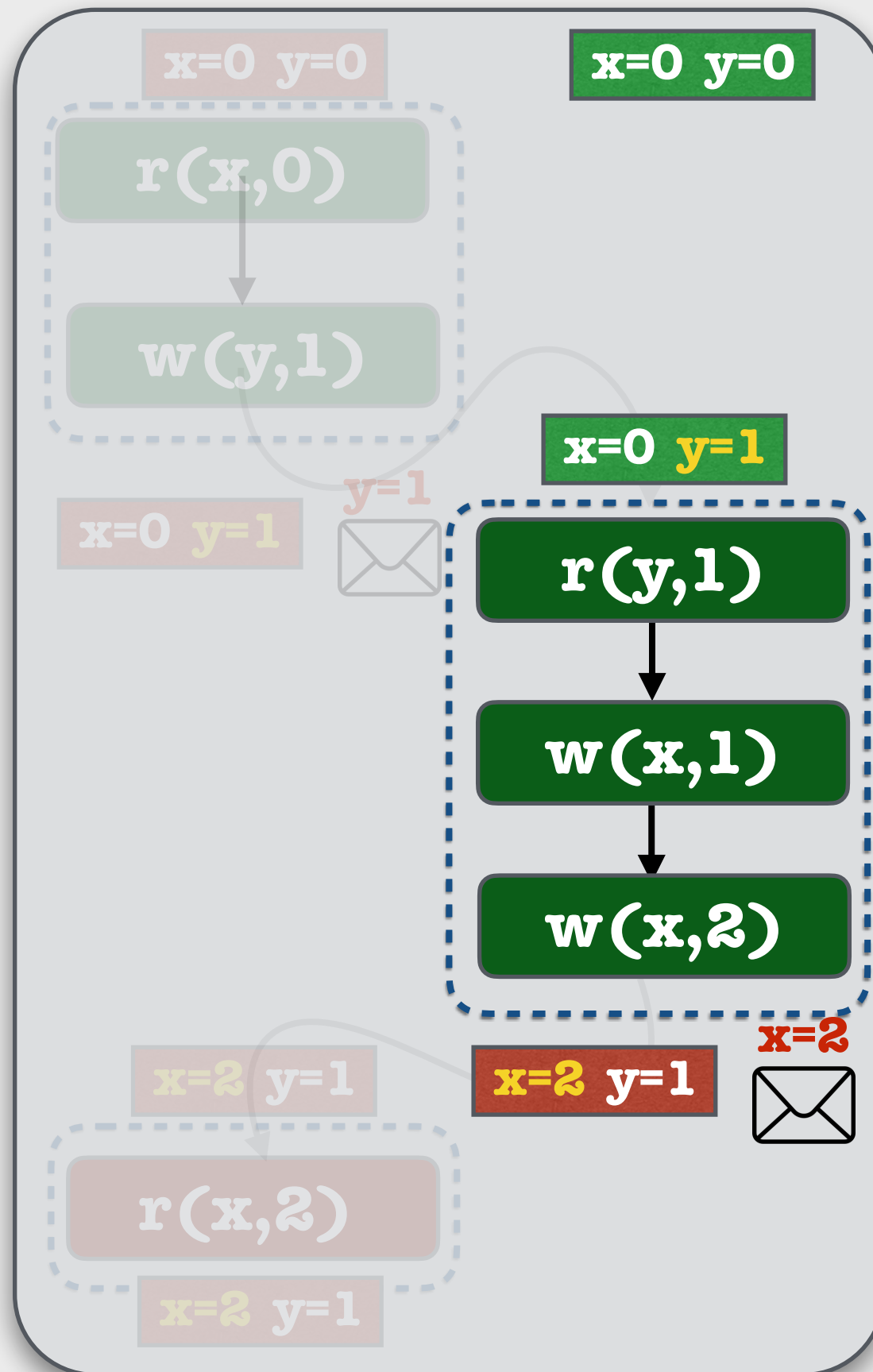
Locality

Validation



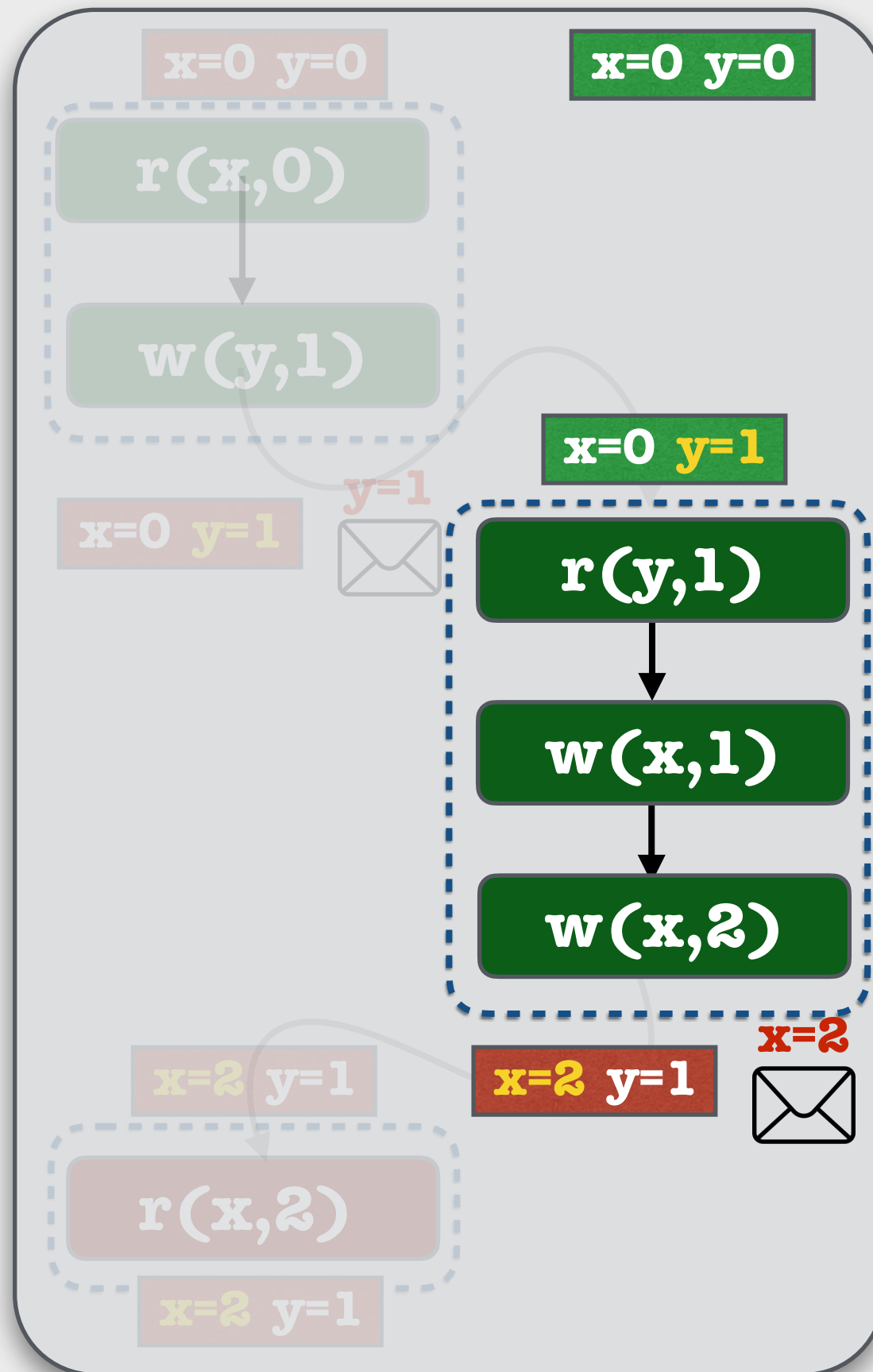
Locality

Validation



Locality

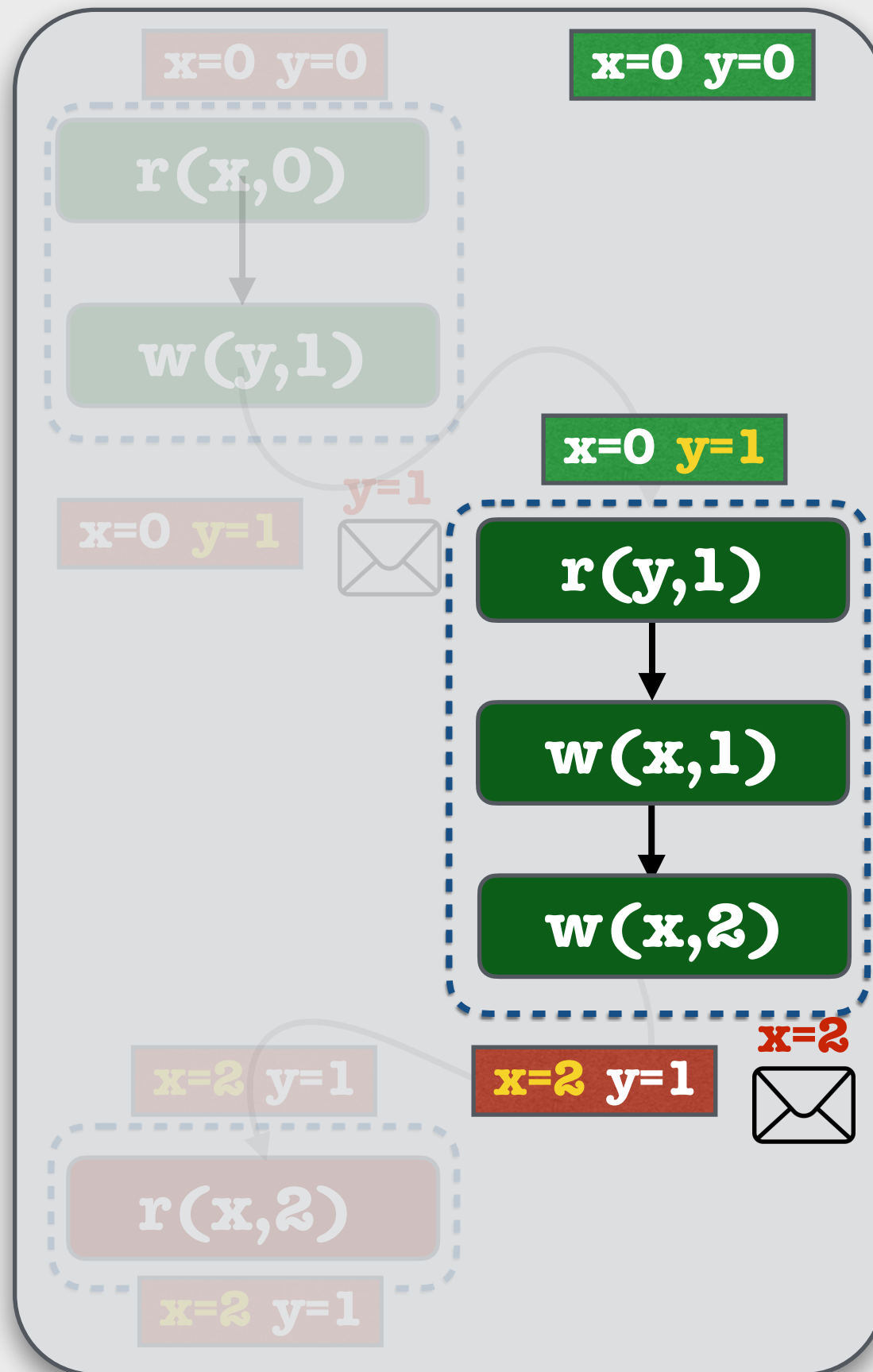
Validation



Simulate **P2** under **SC**

Locality

Validation

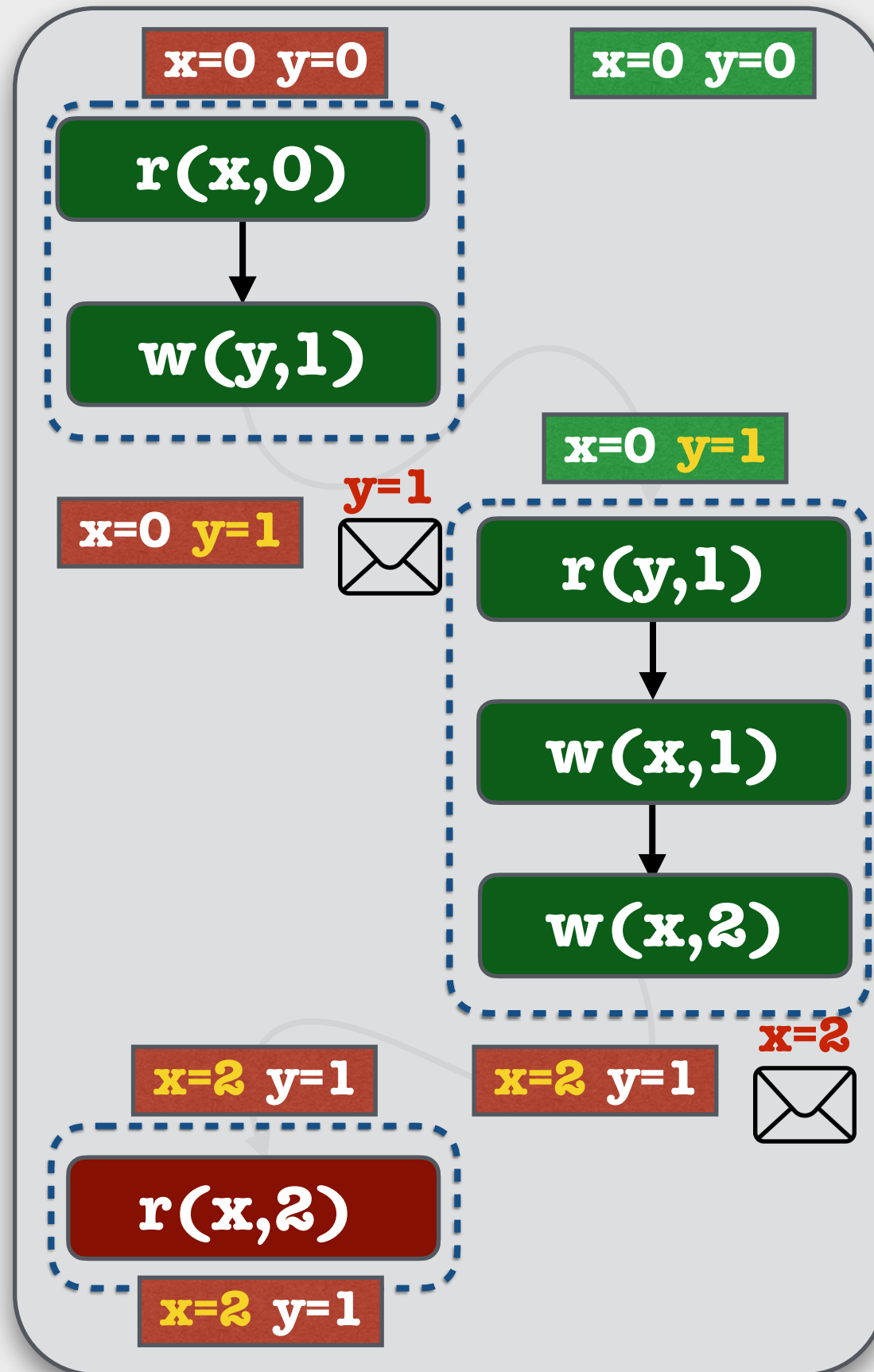


Simulate **P2** under **SC**

Local and global data structures to simulate views and the memory pool

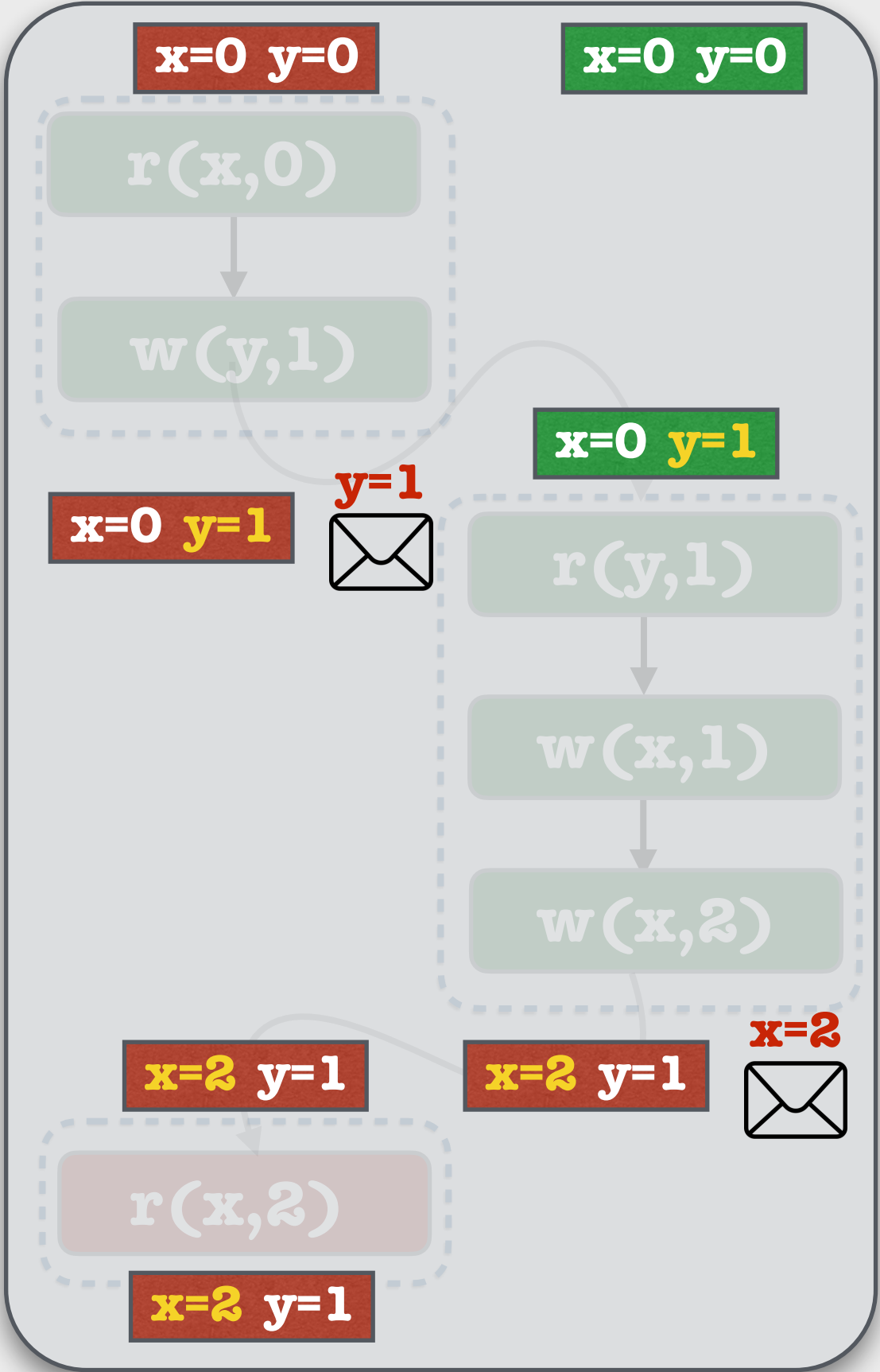
Locality

Validation



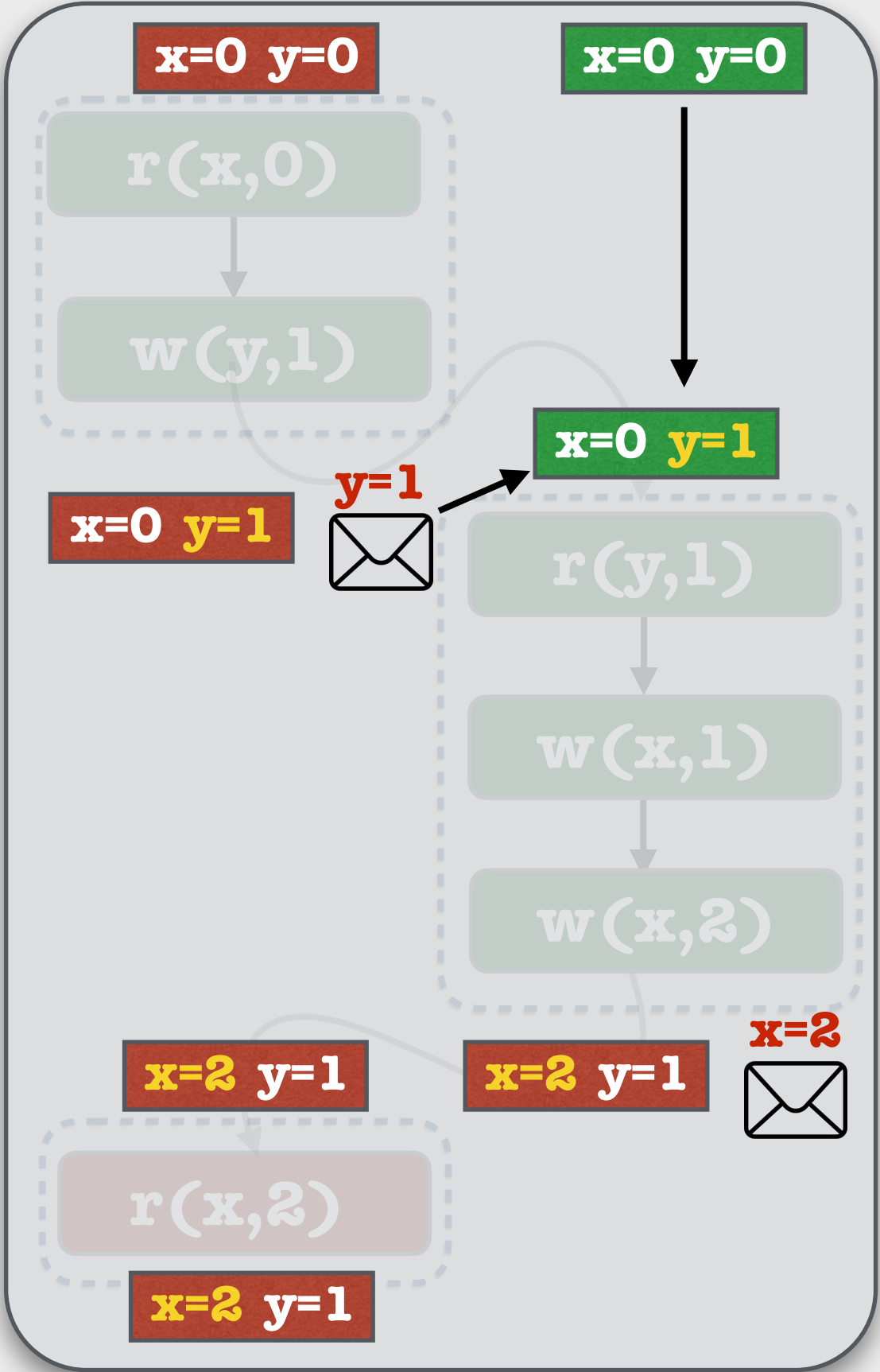
Locality

Validation



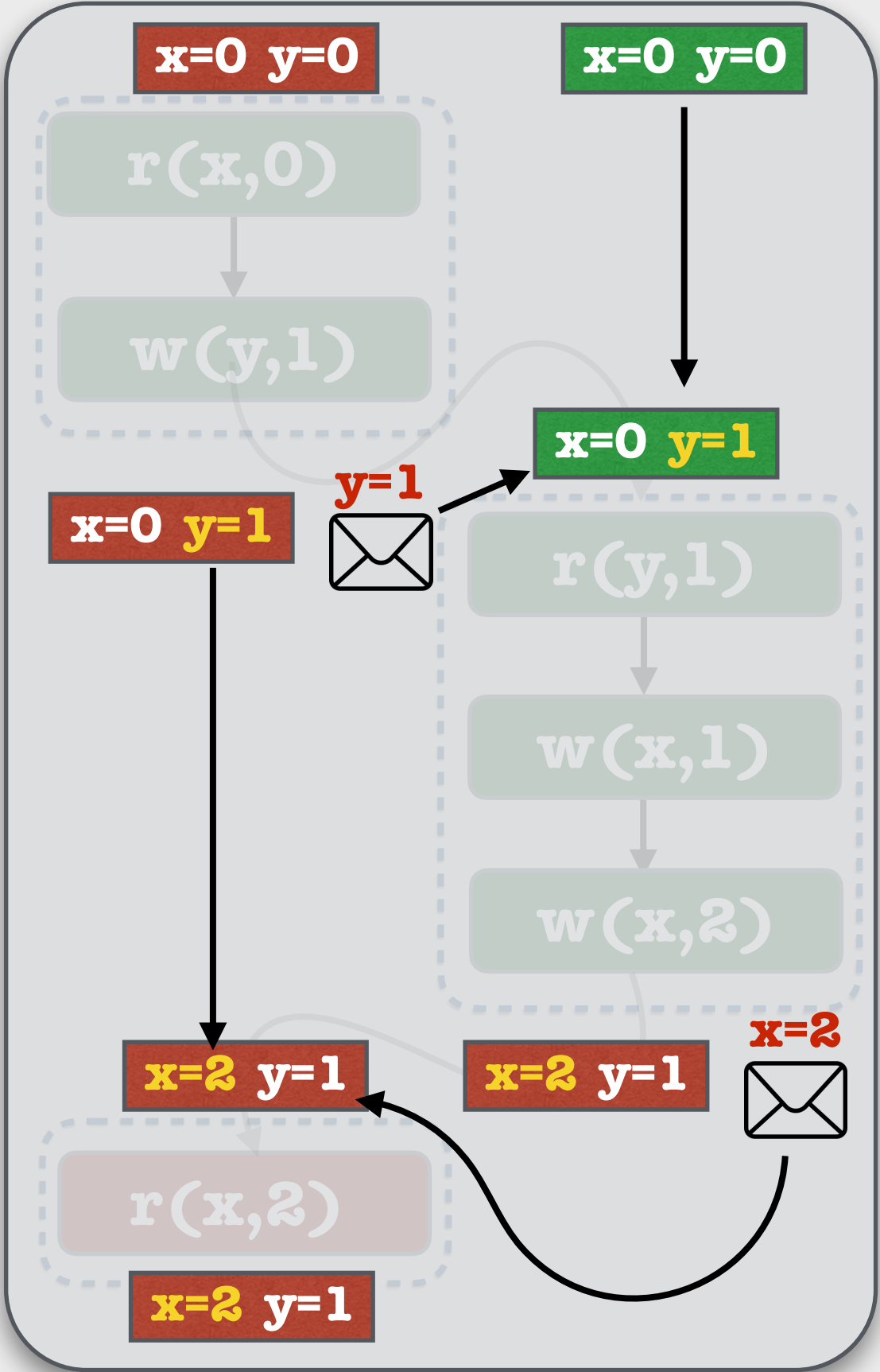
Locality

Validation



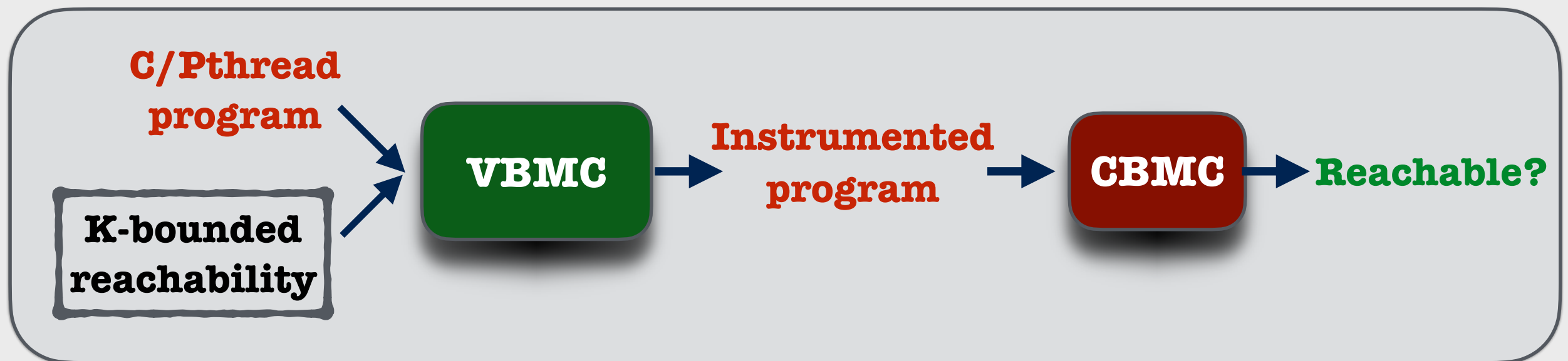
Locality

Validation



View Bounded Model Checker (VBMC)

- Using CBMC as backend model checker

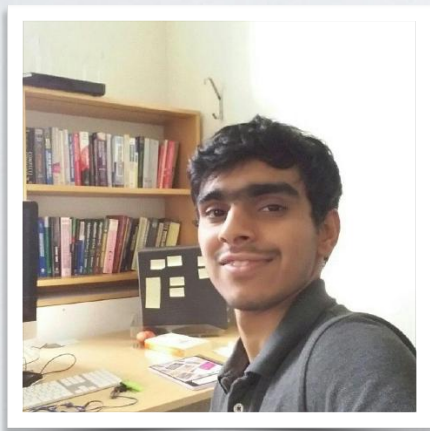
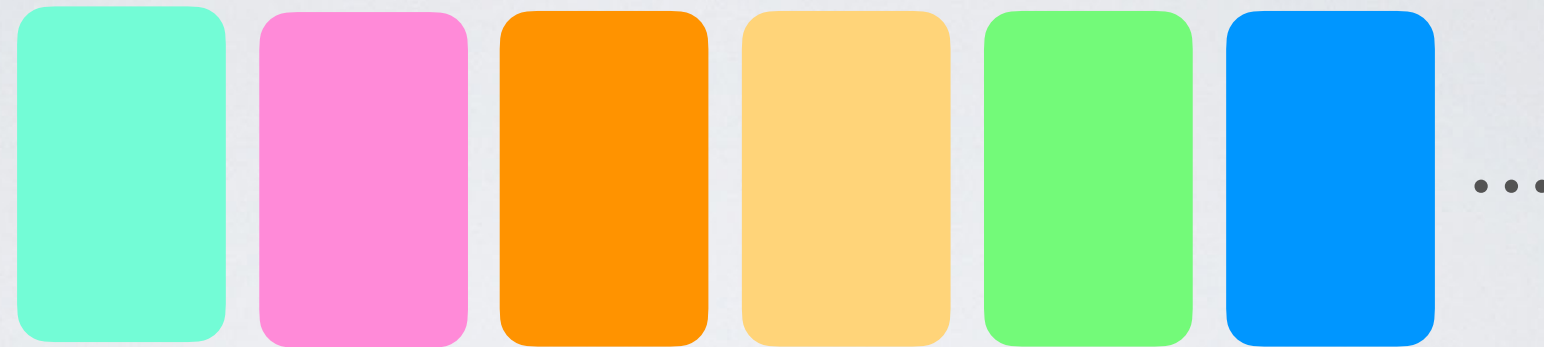


View Bounded Model Checker (VBMC)

- ◆ Tested with **4004 litmus tests** [Sarkar et al. 2011]:
 - **Same results** as **Herd** [Alglave et al. 2014]
- ◆ Tested on **concurrent benchmarks**:
 - **Few number of contexts** sufficient for bug detection under RA
 - Catches isolated bugs faster than state of the art SMC tools **Tracer**, **RCMC** and **CDSChecker**

Parameterized Reachability in RA

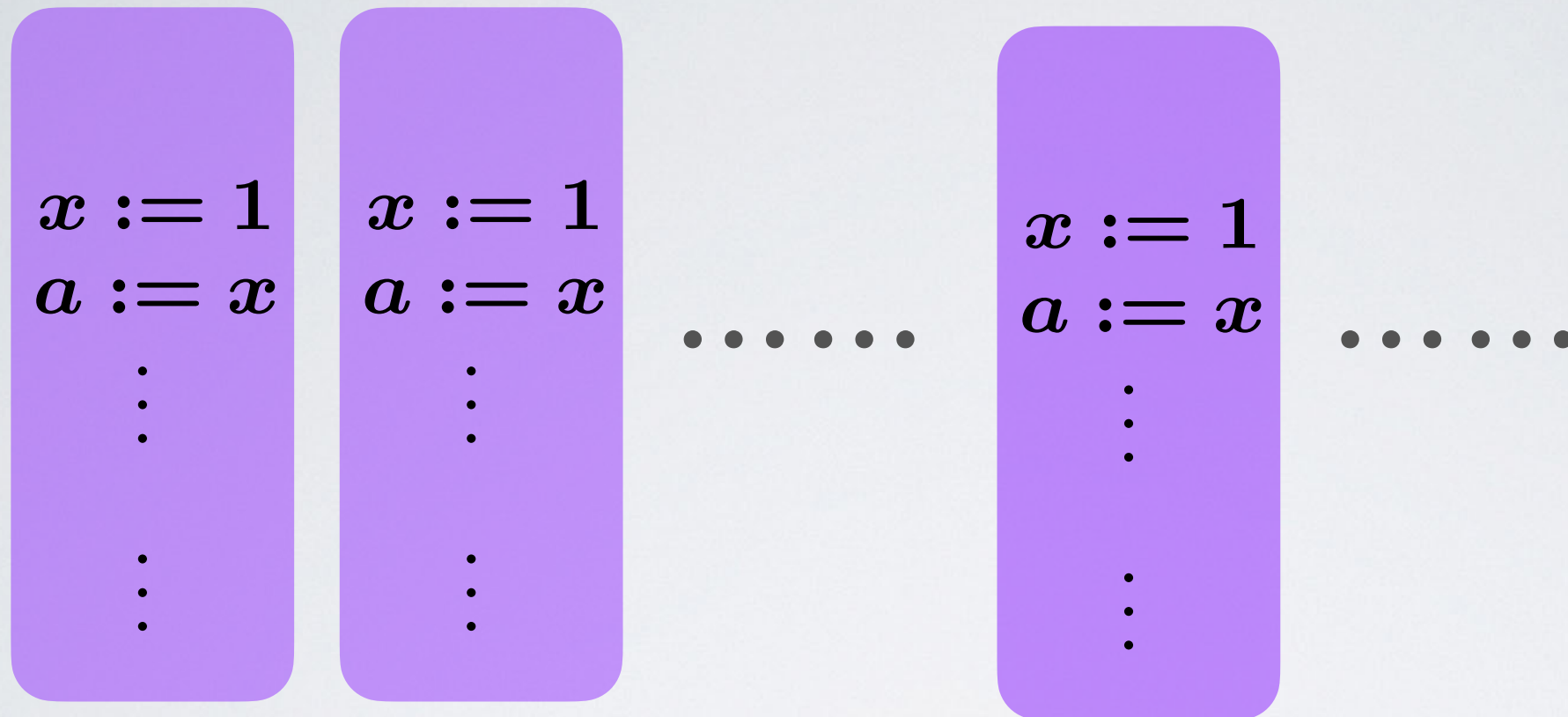
Parameterized Reachability in RA



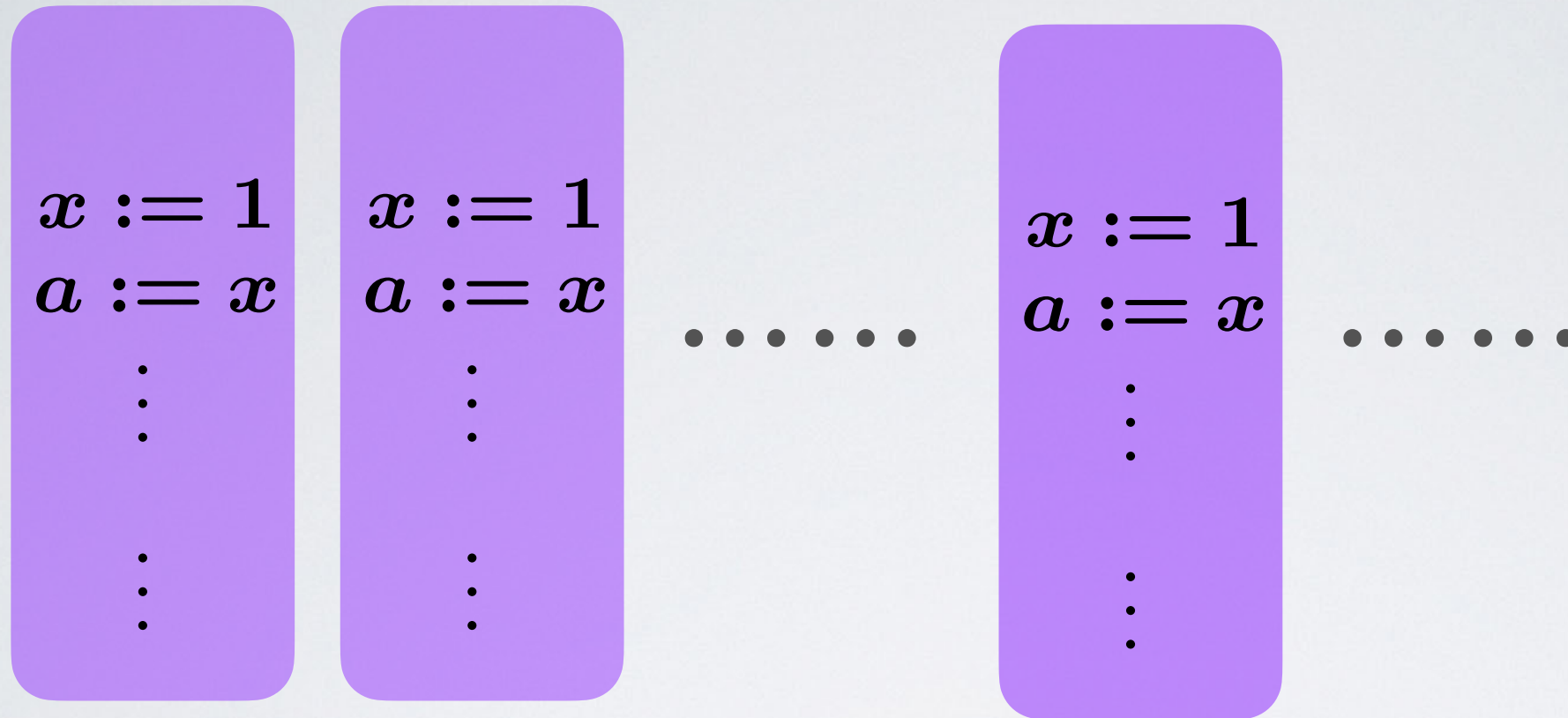
PODC 2022

Parameterized Reachability

Parameterized Reachability

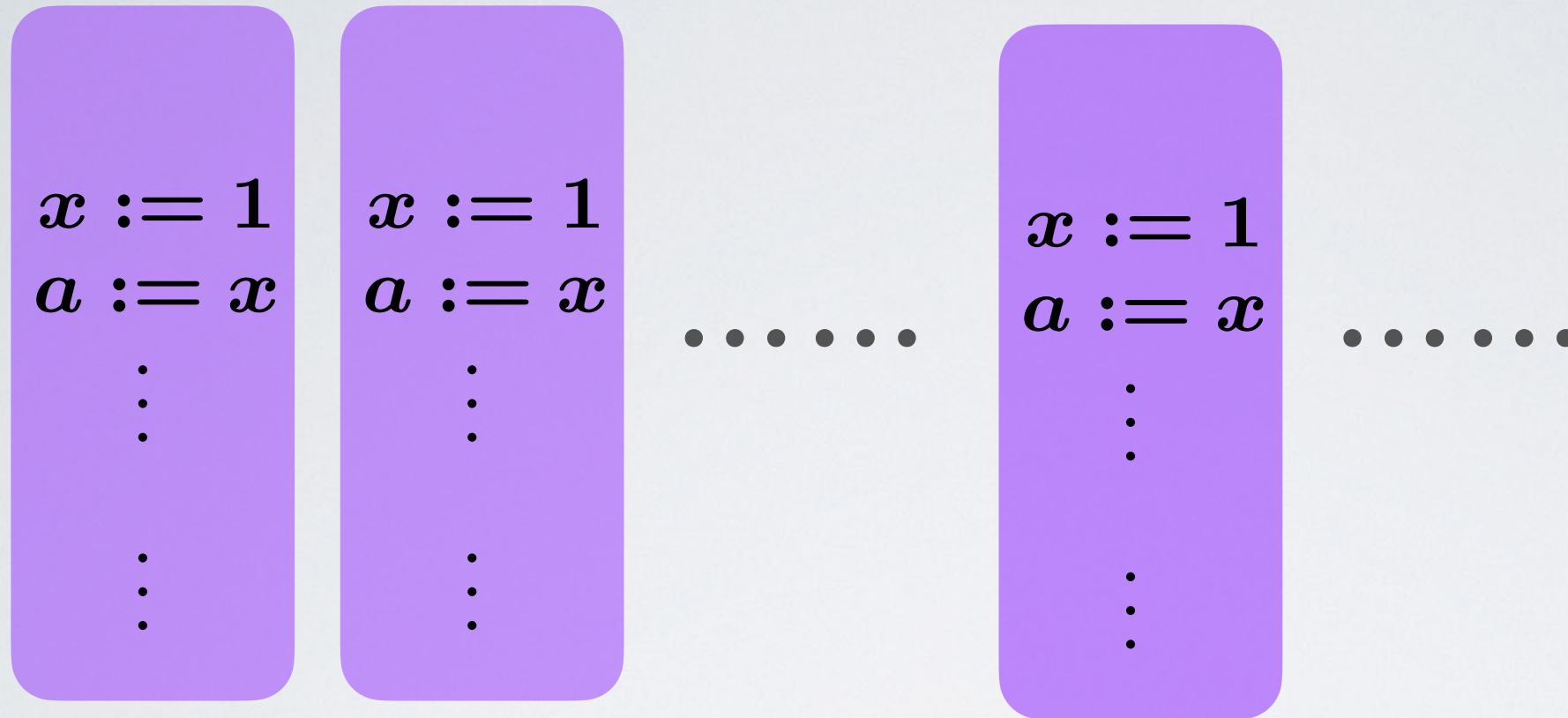


Parameterized Reachability



Unboundedly many

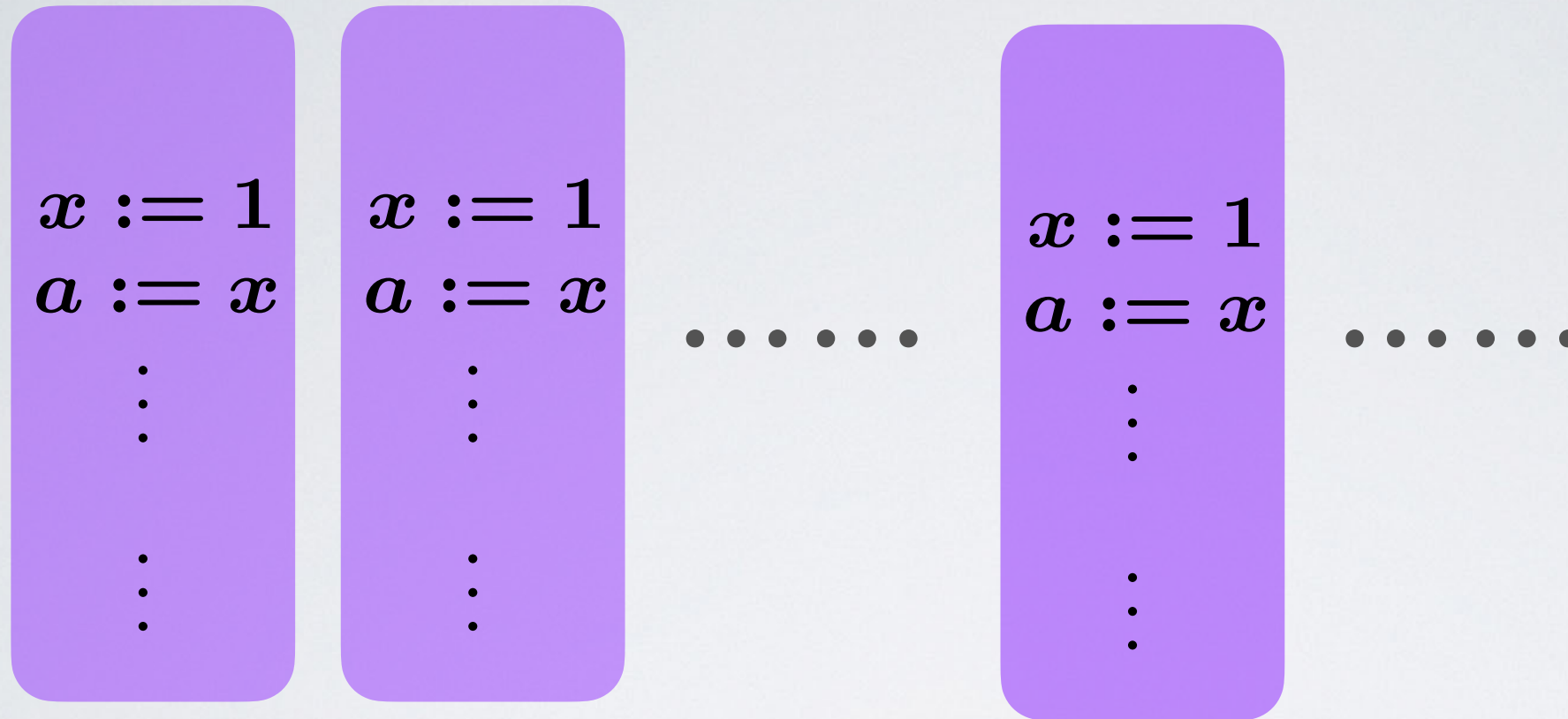
Parameterized Reachability



Unboundedly many

Identical threads

Parameterized Reachability

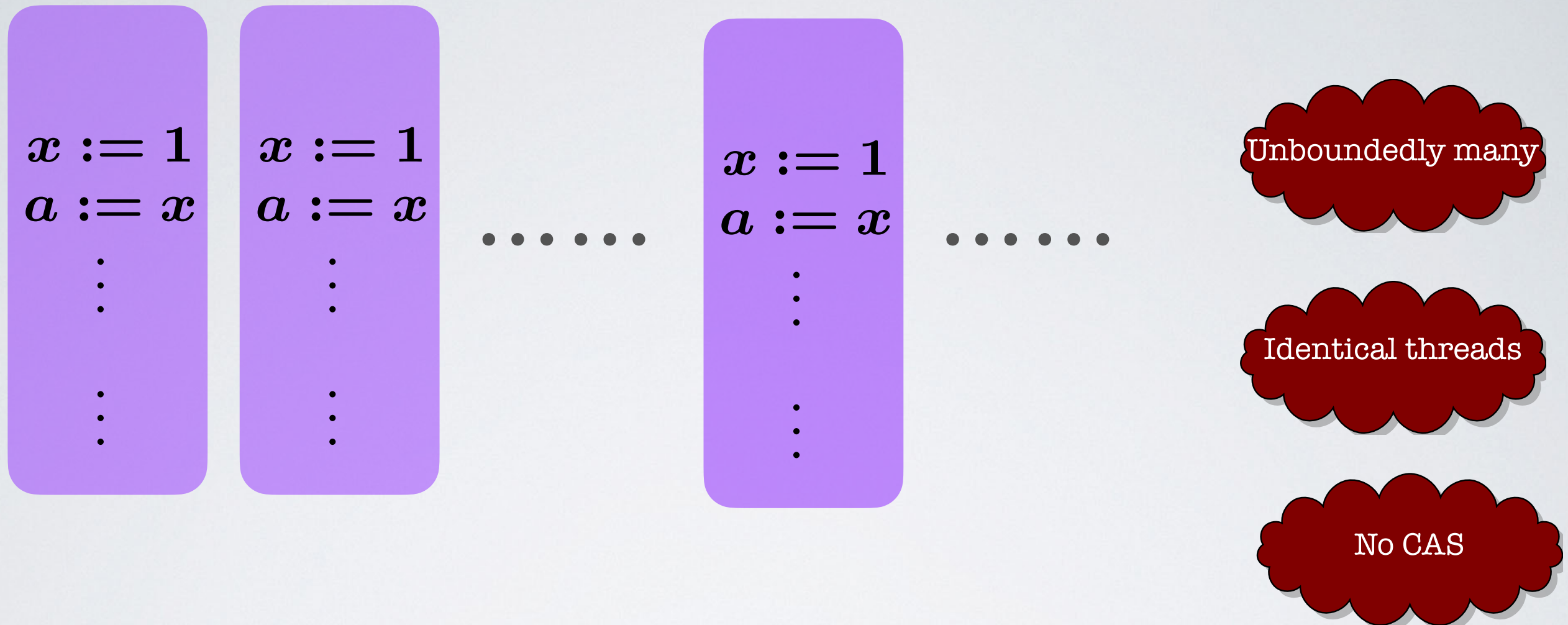


Unboundedly many

Identical threads

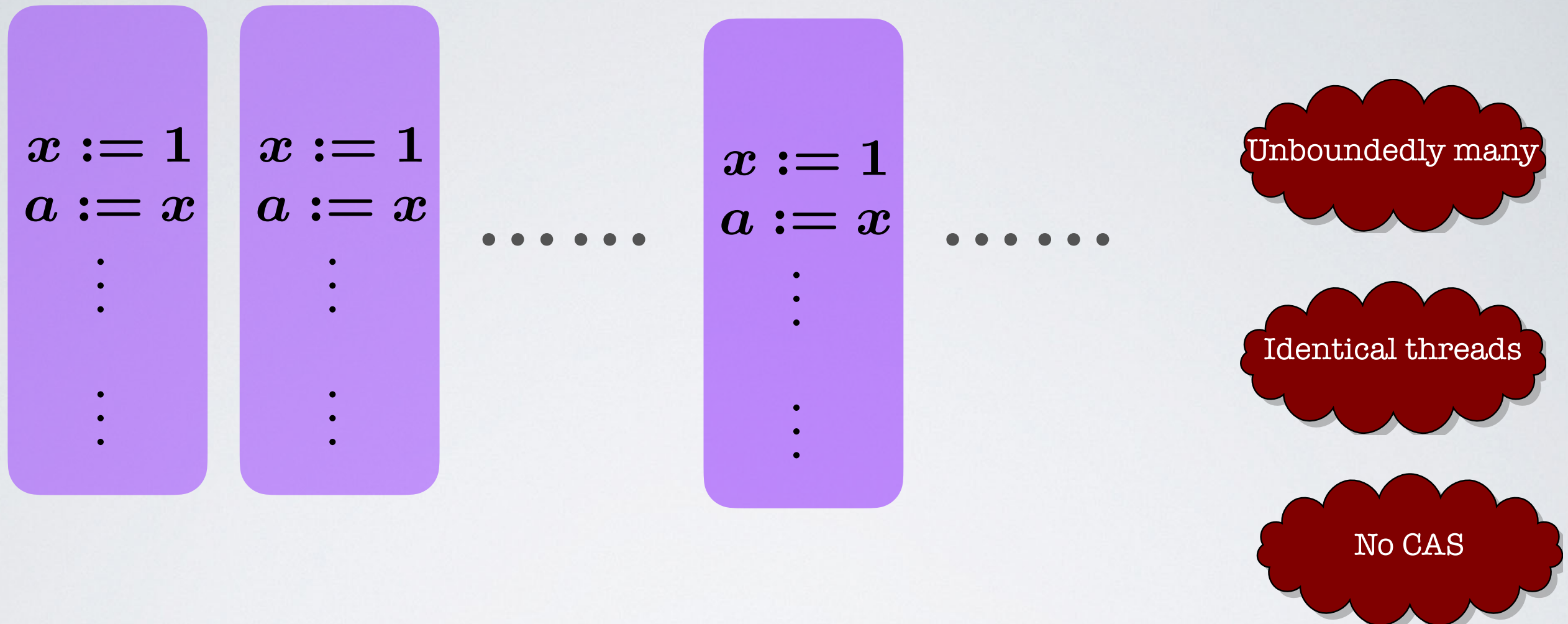
No CAS

Parameterized Reachability



Allowing CAS operations render state reachability **undecidable** for parameterized **RA**, even with **acyclic**, identical threads

Parameterized Reachability



Allowing CAS operations render state reachability **undecidable** for parameterized **RA**, even with **acyclic**, identical threads

Simulate the non parameterized setting

Simplified Semantics

Simplified Semantics

$x := 1$
 $a := x$
⋮
⋮

$x := 1$
 $a := x$
⋮
⋮

...

$x := 1$
 $a := x$
⋮
⋮

.....

$x := 1$
 $a := x$
⋮
⋮

$x := 1$
 $a := x$
⋮
⋮

...

Simplified Semantics

generates $(x, 1, t)$

$x := 1$
 $a := x$
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots
 \vdots

.....

$x := 1$
 $a := x$
 \vdots
 \vdots

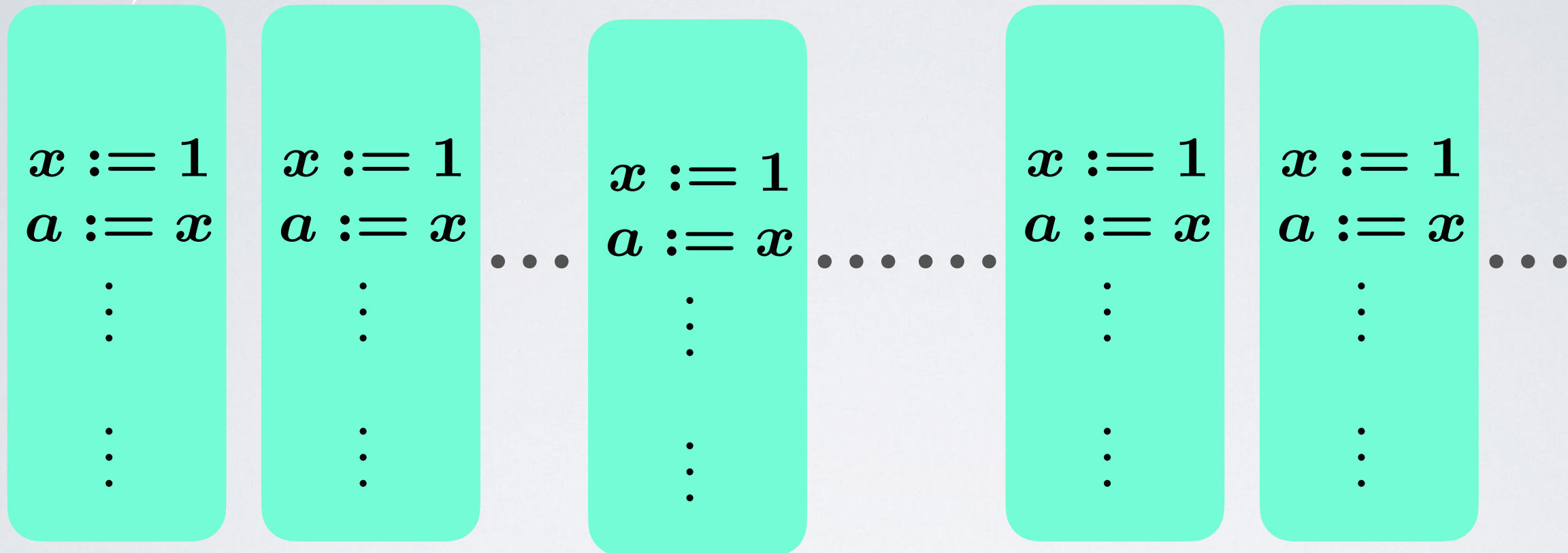
$x := 1$
 $a := x$
 \vdots
 \vdots

...

unbounded, identical, no CAS

Simplified Semantics

generates (x, l, t)



generates
 $(x, l, t+i)$

unbounded, identical, no CAS

Simplified Semantics

generates (x, l, t)

$x := 1$
 $a := x$
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots

...

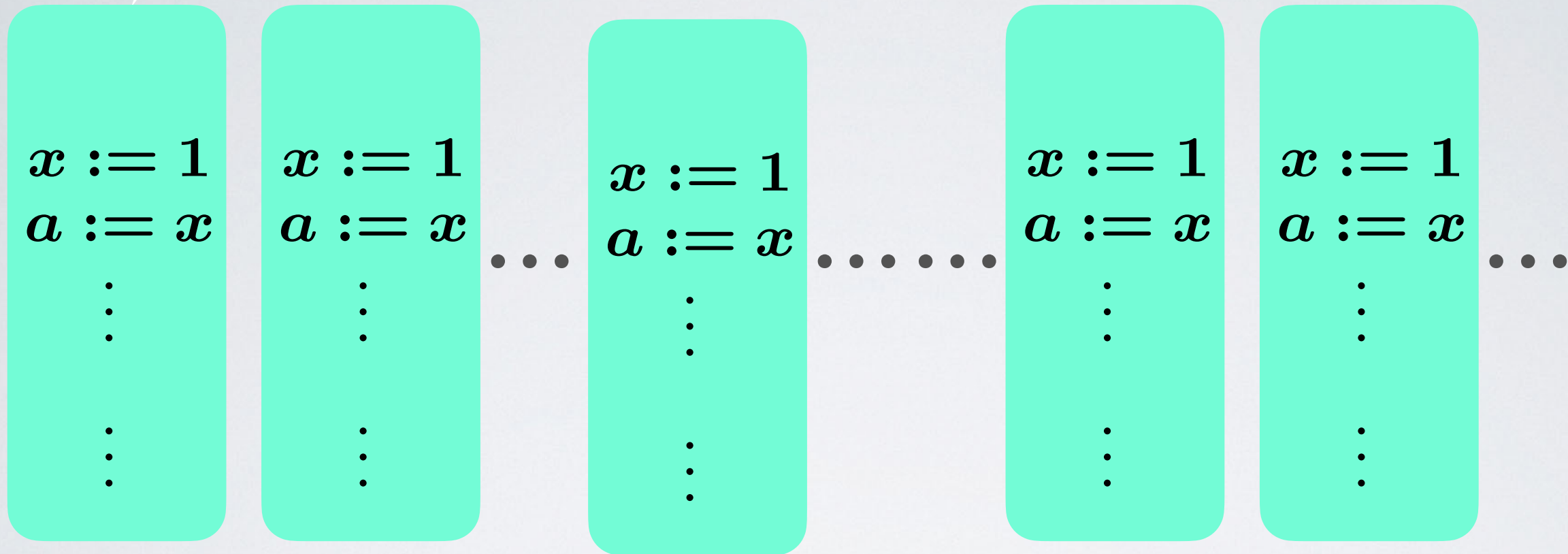
generates
 $(x, l, t+i)$

generates
 $(x, l, t+i+j)$

unbounded, identical, no CAS

Simplified Semantics

generates (x, l, t)



generates
 $(x, l, t+i)$

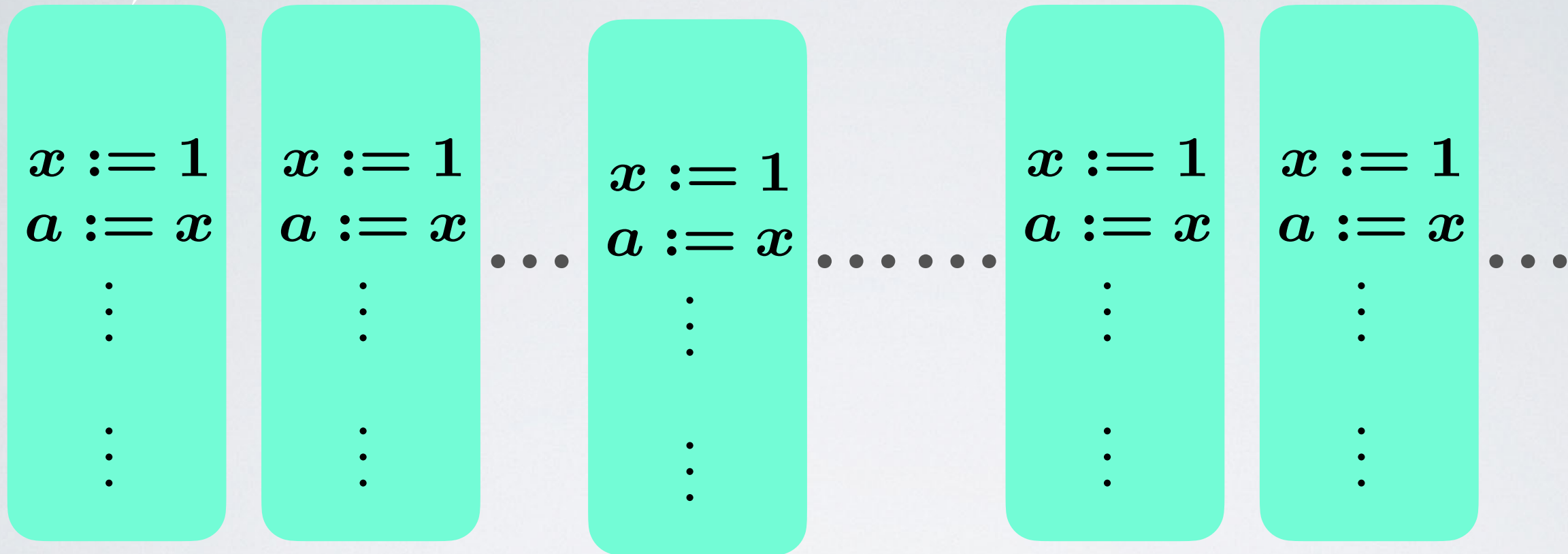
generates
 $(x, l, t+i+j)$

...

unbounded, identical, no CAS

Simplified Semantics

generates (x, l, t)



generates
 $(x, l, t+i)$

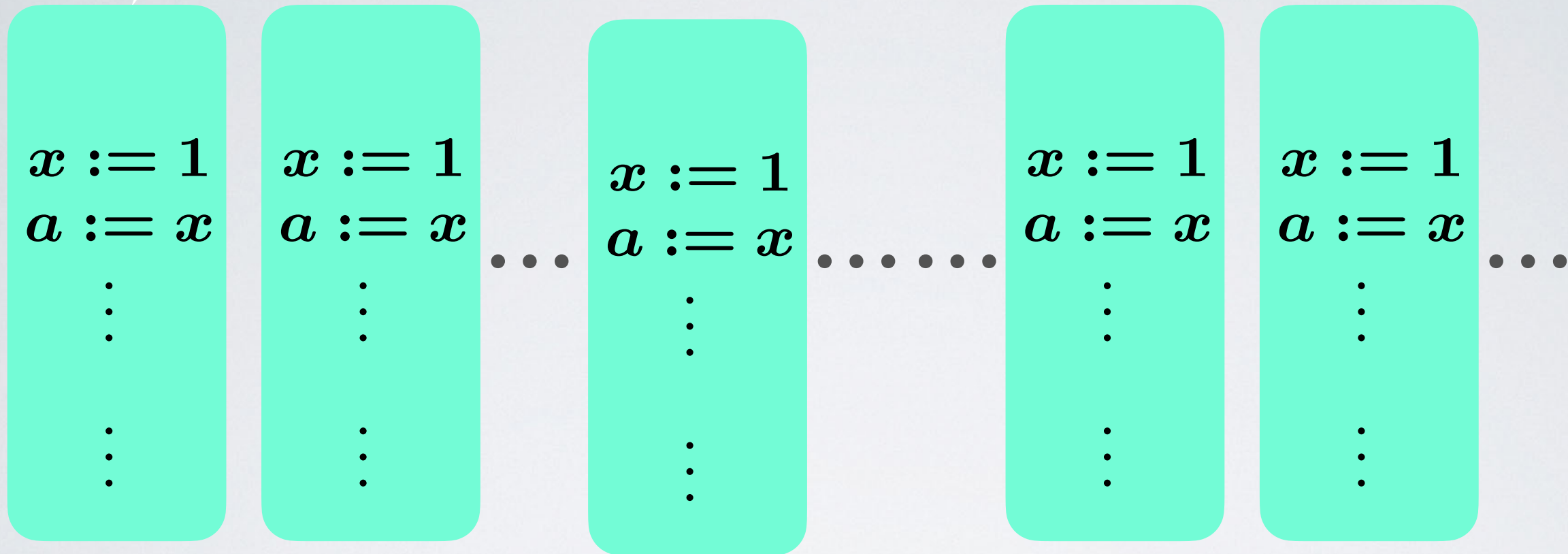
generates
 $(x, l, t+i+j)$

generates
 $(x, l, t+i+j+k)$

unbounded, identical, no CAS

Simplified Semantics

generates (x, l, t)



generates
 $(x, l, t+i)$

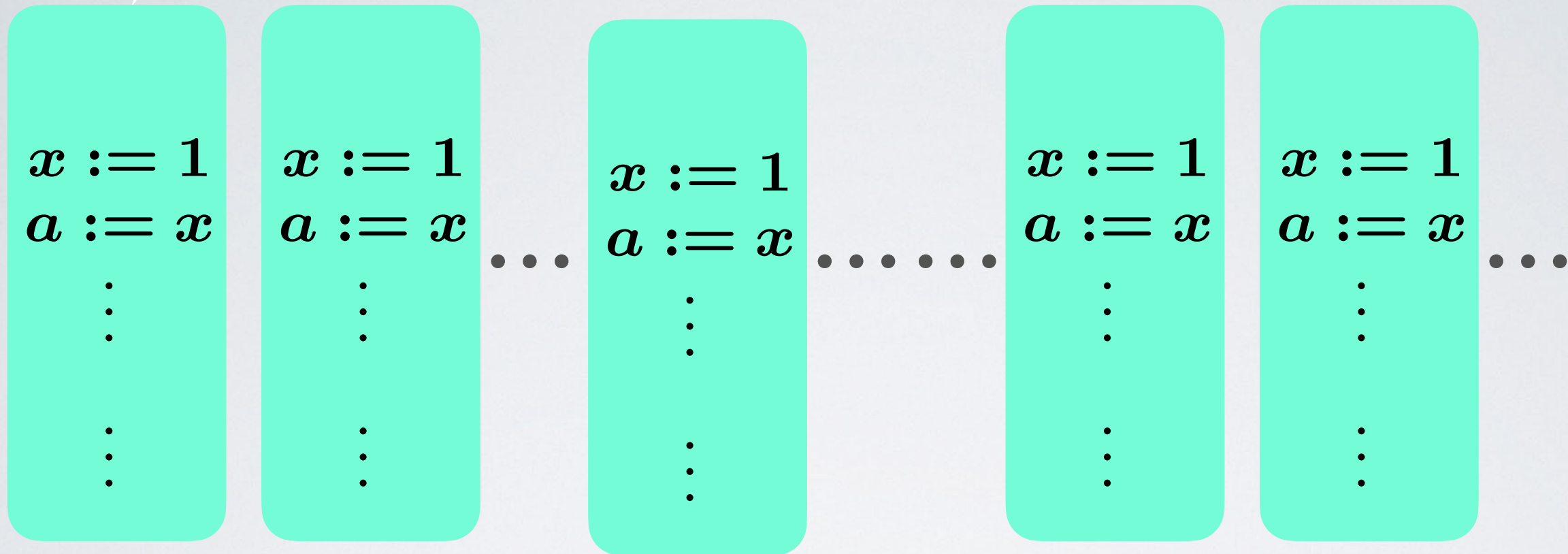
generates
 $(x, l, t+i+j)$

generates
 $(x, l, t+i+j+k)$

unbounded, identical, no CAS

Simplified Semantics

generates (x, l, t)



generates
 $(x, l, t+i)$

generates
 $(x, l, t+i+j)$

generates
 $(x, l, t+i+j+k)$

unbounded, identical, no CAS

If (x, l) can be read, it can be read arbitrary many times

Simplified Semantics

generates (x, l, t)

$x := 1$
 $a := x$
⋮
⋮
⋮

$x := 1$
 $a := x$
⋮
⋮
⋮

⋯

$x := 1$
 $a := x$
⋮
⋮
⋮

⋯

Infinite Supply Property

$x := 1$
 $a := x$
⋮
⋮
⋮

$x := 1$
 $a := x$
⋮
⋮
⋮

⋯

generates
 $(x, l, t+i)$

generates
 $(x, l, t+i+j)$

⋯

generates
 $(x, l, t+i+j+k)$

⋯

unbounded, identical, no CAS

If (x, l) can be read, it can be read arbitrary many times

Simplified Semantics

generates (x, l, t)

$x := 1$

$a := x$

No recency check needed

$x := 1$

$a := x$

$x := 1$

$a := x$

Infinite Supply Property

unbounded, identical, no CAS

generates
 $(x, l, t+i)$

generates
 $(x, l, t+i+j)$

generates
 $(x, l, t+i+j+k)$

If (x, l) can be read, it can be read arbitrary many times

Simplified Semantics

generates (x, l, t)

$x := 1$

$a := x$

⋮

⋮

$x := 1$

$a := x$

⋮

⋮

$x := 1$

$a := x$

⋮

⋮

No recency check needed

generates
 $(x, l, t+i)$

generates
 $(x, l, t+i+j)$

⋮

generates
 $(x, l, t+i+j+k)$

⋮

Infinite Supply Property

Abstraction of timestamps

unbounded, identical, no CAS

If (x, l) can be read, it can be read arbitrary many times

Simplified Semantics

Simplified Semantics

$x := 1$
 $a := x$
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots
 \vdots

.....

\vdots
 $c := y$
 \vdots

\vdots
 $z := 11$
 \vdots

...

$x := 2$
 $b := x$
 \vdots

unbounded, identical no CAS

acyclic, distinguished
has CAS

Simplified Semantics

Maintain relative ordering
with distinguished threads

$x := 1$
 $a := x$
⋮
⋮

$x := 1$
 $a := x$
⋮
⋮

...

$x := 1$
 $a := x$
⋮
⋮

.....

⋮
 $c := y$
⋮

⋮
 $z := 11$
⋮

...

$x := 2$
 $b := x$
⋮

unbounded, identical no CAS

acyclic, distinguished
has CAS

Simplified Semantics

Maintain relative ordering
with distinguished threads

$x := 1$
 $a := x$
⋮
⋮

$x := 1$
 $a := x$
⋮
⋮

...

$x := 1$
 $a := x$
⋮
⋮

.....

unbounded, identical no CAS

env timestamps $\{0^+, 1^+, 2^+, \dots\}$
dis timestamps $\{0, 1, 2, \dots\}$
 $0 < 0^+ < 1 < 1^+ < 2 \dots$

⋮
⋮
 $c := y$
⋮
⋮

⋮
⋮
 $z := 11$
⋮
⋮

...

$x := 2$
 $b := x$
⋮
⋮

acyclic, distinguished
has CAS

Simplified Semantics

Maintain relative ordering with distinguished threads

$x := 1$
 $a := x$
⋮
⋮

$x := 1$
 $a := x$
⋮
⋮

...

$x := 1$
 $a := x$
⋮
⋮

.....

env timestamps $\{0^+, 1^+, 2^+, \dots\}$
dis timestamps $\{0, 1, 2, \dots\}$
 $0 < 0^+ < 1 < 1^+ < 2 \dots$

⋮
 $c := y$
⋮

⋮
 $z := 11$
⋮

...

$x := 2$
 $b := x$
⋮

unbounded, identical no CAS

acyclic, distinguished
has CAS

Concrete time stamps

0 2 3 5 7 8 10 11 13

Simplified Semantics

Maintain relative ordering with distinguished threads

$x := 1$
 $a := x$
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots
 \vdots

.....

env timestamps $\{0^+, 1^+, 2^+, \dots\}$
 dis timestamps $\{0, 1, 2, \dots\}$
 $0 < 0^+ < 1 < 1^+ < 2 \dots$

\vdots
 \vdots
 $c := y$
 \vdots
 \vdots

\vdots
 \vdots
 $z := 11$
 \vdots
 \vdots

...

$x := 2$
 $b := x$
 \vdots

unbounded, identical no CAS

acyclic, distinguished
has CAS

Concrete time stamps

0 2 3 5 7 8 10 11 13

Abstract time stamps

0 1 1⁺ 2 2⁺ 2⁺ 3 3⁺ 3⁺

Simplified Semantics

Maintain relative ordering with distinguished threads

env timestamps $\{0^+, 1^+, 2^+, \dots\}$
 dis timestamps $\{0, 1, 2, \dots\}$
 $0 < 0^+ < 1 < 1^+ < 2 \dots$

$x := 1$
 $a := x$
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots

.....

$c := y$

\vdots
 $z := 11$
 \vdots

...

$x := 2$
 $b := x$
 \vdots

#timestamps=0(#(dis timestamps))

unbounded, identical, non-cyclic, distinguished
 has CAS

Concrete time stamps

0 2 3 5 7 8 10 11 13

Abstract time stamps

0 1 1⁺ 2 2⁺ 2⁺ 3 3⁺ 3⁺

$x := 1$
 $a := x$
 \vdots
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots
 \vdots
 \vdots

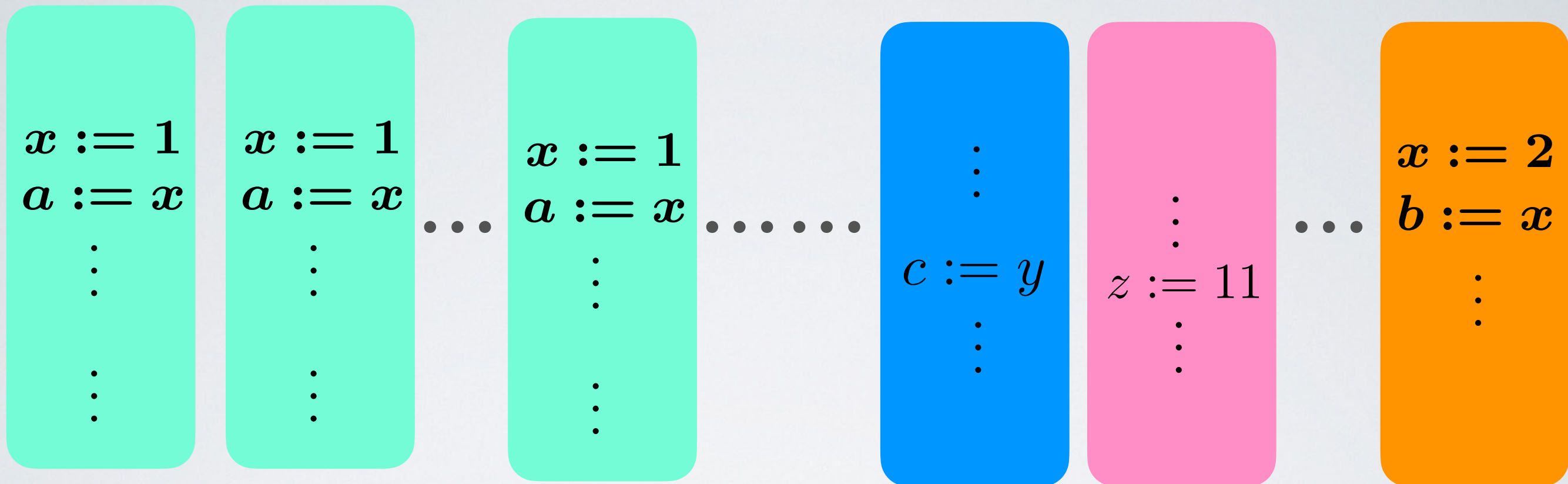
.....

\vdots
 $c := y$
 \vdots

\vdots
 $z := 11$
 \vdots

...

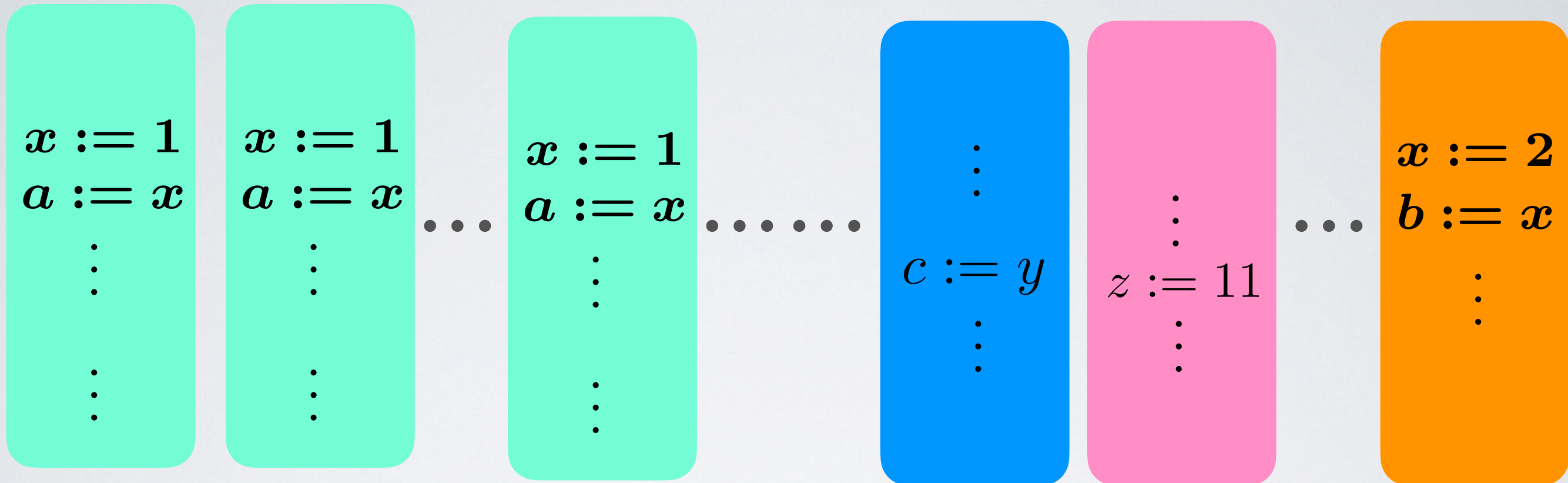
$x := 2$
 $b := x$
 \vdots



unbounded, identical
no CAS

acyclic, distinguished
has CAS

QBF Sat



unbounded, identical
no CAS

acyclic, distinguished
has CAS

QBF Sat

\leq

$x := 1$
 $a := x$
 \vdots
 \vdots
 \vdots

$x := 1$
 $a := x$
 \vdots
 \vdots
 \vdots

...

$x := 1$
 $a := x$
 \vdots
 \vdots
 \vdots

.....

\vdots
 $c := y$
 \vdots
 \vdots

\vdots
 $z := 11$
 \vdots
 \vdots

...

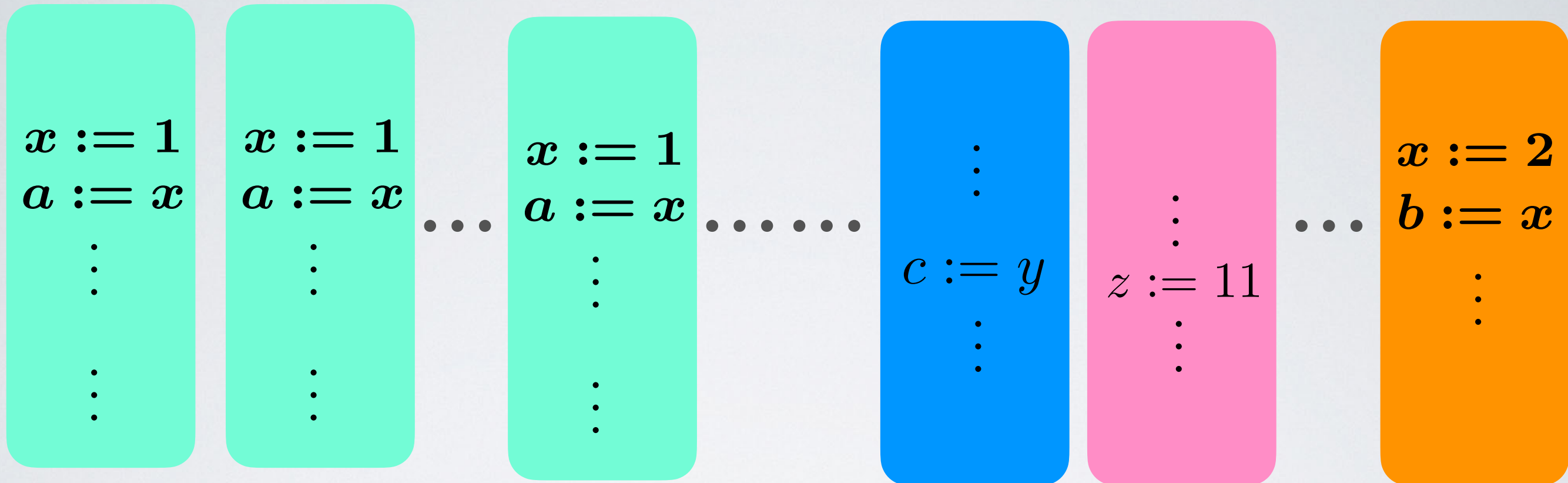
$x := 2$
 $b := x$
 \vdots
 \vdots

unbounded, identical
no CAS

acyclic, distinguished
has CAS

QBF Sat

\leq



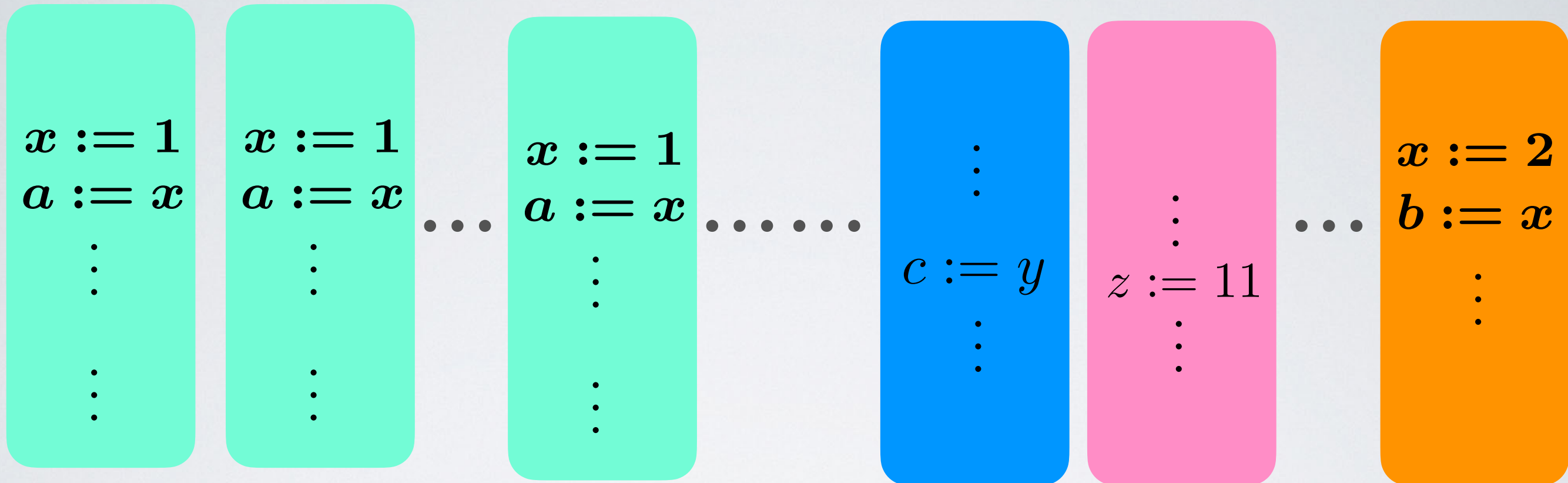
unbounded, identical
no CAS

acyclic, distinguished
has CAS

\geq

QBF Sat

\leq



unbounded, identical
no CAS

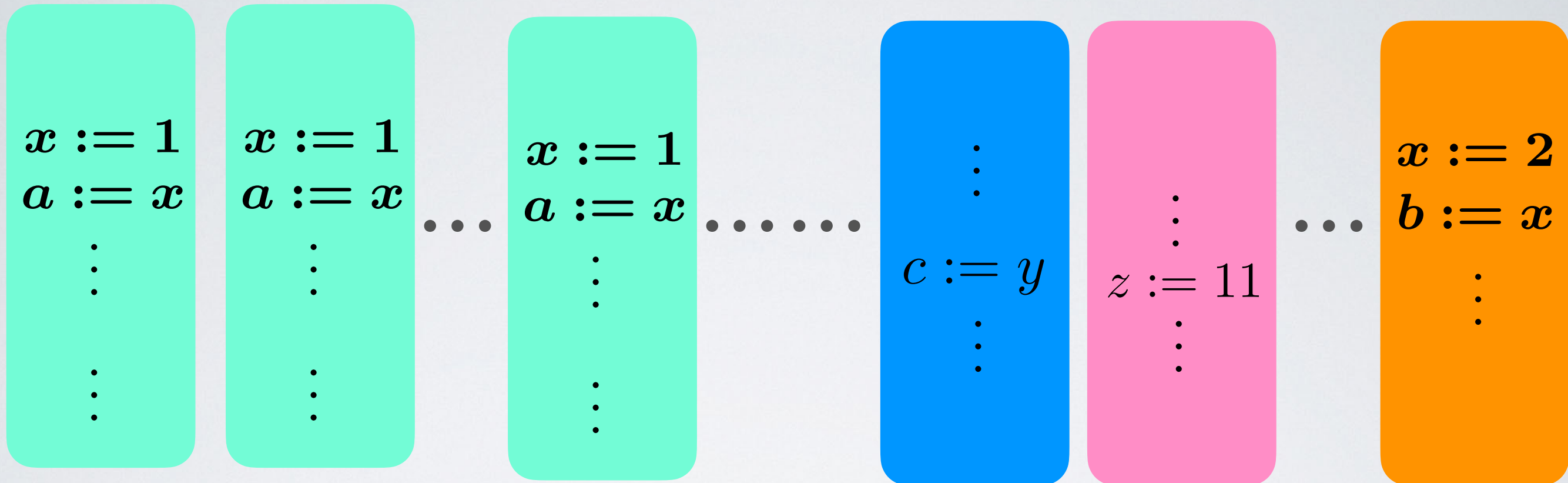
acyclic, distinguished
has CAS

\leq

Query evaluation in linear Datalog

QBF Sat

\leq



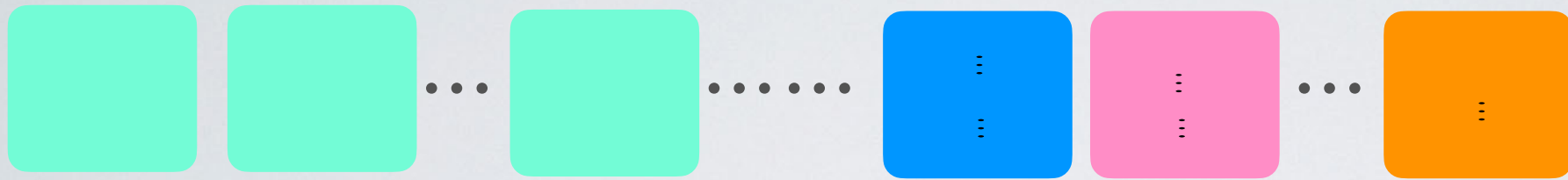
unbounded, identical
no CAS

acyclic, distinguished
has CAS

\leq

Query evaluation in linear Datalog

PSPACE completeness



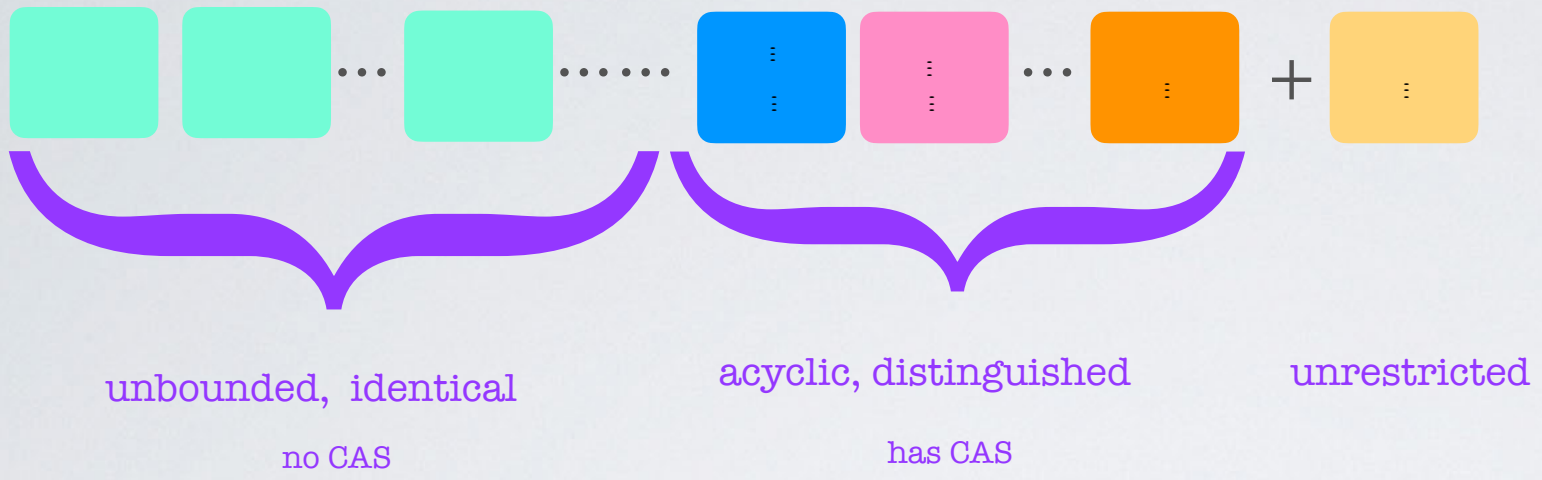
unbounded, identical
no CAS

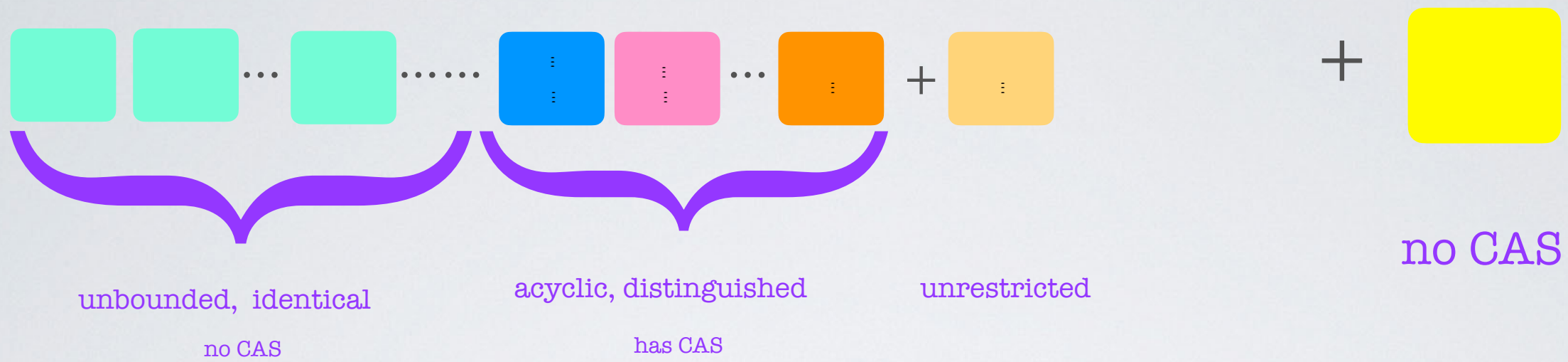
acyclic, distinguished
has CAS





NEXPTIME completeness





Non primitive recursive

Decidability Open

Thankyou