# Some data-flow based techniques for assertion checking in concurrent programs

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[with inputs from Suvam Mukherjee and Nisant Sinha]

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# Outline







#### Inductive annotation as proof for sequential programs



#### Inductive annotation as proof for sequential programs



#### What about concurrent programs?

• Collecting state for a concurrent program.

x := 0

i := 0 x := 2x := 1 j := 1y := x + 1

# Example analysis using Radar [CVJL2008]

	huffen liet thufe.	1
Aajuste	Jock buf look	
Annalyzatio	LOCK DUT_LOCK;	
Anaiysis	int pert_ctr;	
	thread producer1(){	
	ø P0: px = bufs;	
	ø P1: while (px != NULL){	
p	P2: lock(buf_lock);	
p	<pre>x P3: px-&gt;data = new();</pre>	
px->data, p	P5: perf_ctr++;	
px->data, p	P6: t=produce();	
px->data, p	<pre>P8: *px-&gt;data = t;</pre>	
px->data.p	P9: unlock(buf_lock);	
px->data, p	PA: $px = px -> next;$	buf -> data -> Null
	}	
	thread consumer1(){	nexc
	<pre> perf_ctr = 0; </pre>	
	ø C0: cx = bufs;	
	<pre>ø C1: while(cx != NULL){</pre>	
c	C2: lock(buf_lock);	
c	<pre>K C3: if(cx-&gt;data != NULL){</pre>	
cx->data,c	<pre> c C4: consume(*cx-&gt;data); </pre>	
cx->data,c	<pre>c C5: cx-&gt;data = NULL;</pre>	
c	C6: cx = cx->next;	
	}	
	<pre> ø C7: unlock(buf_lock); </pre>	
	}	

# Example analysis using Radar: Program 2

Adjusted	buffer list *bufs:	
Aujusteu	lock buf lock;	
Analysis	int perf_ctr;	
	thread producer1(){	
ø	P0: px = bufs;	
ø	P1: while (px != NULL){	
px	<pre>P2: lock(buf_lock);</pre>	
px	P3: px->data = new();	
px->data, px	<pre>P4: unlock(buf_lock);</pre>	
px->data, px	P5: perf_ctr++;	
px->data, px	P6: t=produce();	
px->data, px	<pre>P7: lock(buf_lock);</pre>	
px->data, px	P8: *px->data = t;	
px->data, px	<pre>P9: unlock(buf_lock);</pre>	
px->data, px	PA: px = px->next;	buf -> data -> Null
	}	next
	thread consumer1(){	
ø	perf_ctr = 0;	
ø	C0: cx = bufs;	
ø	C1: while(cx != NULL){	
a	C2: lock(buf_lock);	
CX	C3: if(cx->data != NULL){	
cx->data,cx	C4: consume(*cx->data);	
cx->data,cx	C5: cx->data = NULL;	
a	C6: cx = cx->next;	
	}	
ø	C7: unlock(buf_lock);	
	}	

#### Data-Flow analysis for datarace-free programs [De et al 2011]

- Lifts any value-set analysis (like constant progagation, non-null analysis) to a sound analysis for concurrent programs.
- Is potentially precise at non-racy reads.
- Add "sync" edges between statements across threads (from unlock to lock).
- Find the least fix point over this "sync-CFG".

#### Data-Flow analysis for datarace-free programs [De et al 2011]



# Modular reasoning about data and control [Farzan, Kincaid 2012]

- Define a notion of inductive annotation based on data-flow graphs.
- Show how to compute such a graph
  - Begin with intra-thread data-flow edges
  - Compute an inductive annotation for this DFG
  - Check if any more edges need to be added. If so repeat, else done.

 $\begin{array}{c|c} x := 0 & & lock(l) \\ lock(l) & & \\ \end{array} \\ \end{array}$  read x

unlock(l)

unlock(l)

x := -1

x := 0

# Example program from [FK2012]

$$\begin{array}{cccc} c := 0 & & c := 0 \\ incr := 0 & & & \\ incr := 0 & & & \\ incr := 1 & & incr := -1 \\ c := c + incr & & assert \ (c \ge 0) \end{array}$$

- Multiple instances of 2 static threads.
- Variable incr is local to each thread.

#### Example: Initial DFG using intra-thread data-flow edges



#### Example: Initial DFG with inductive annotation $\iota$



#### Example: Control-flow analysis: Adding *i*-feasible data-flow edges



#### **Example:** Iteration 2: Compute new inductive annotation $\iota_2$



# Example: Iteration 2: Check if any $\iota_2\text{-feasible data-flow edges can be added}$



# Why *i*-infeasibility helps

x := 0	11	lock(l)
lock(l)		read x
x := −1		unlock(l)
x := 0		
unlock(1)		

# **Problematic example**



# **Problematic example**



# **Problematic example**

